

12月8日(水)10:50-11:30

MEMSミラーと集積化

東北大学 工学研究科

羽根 一博

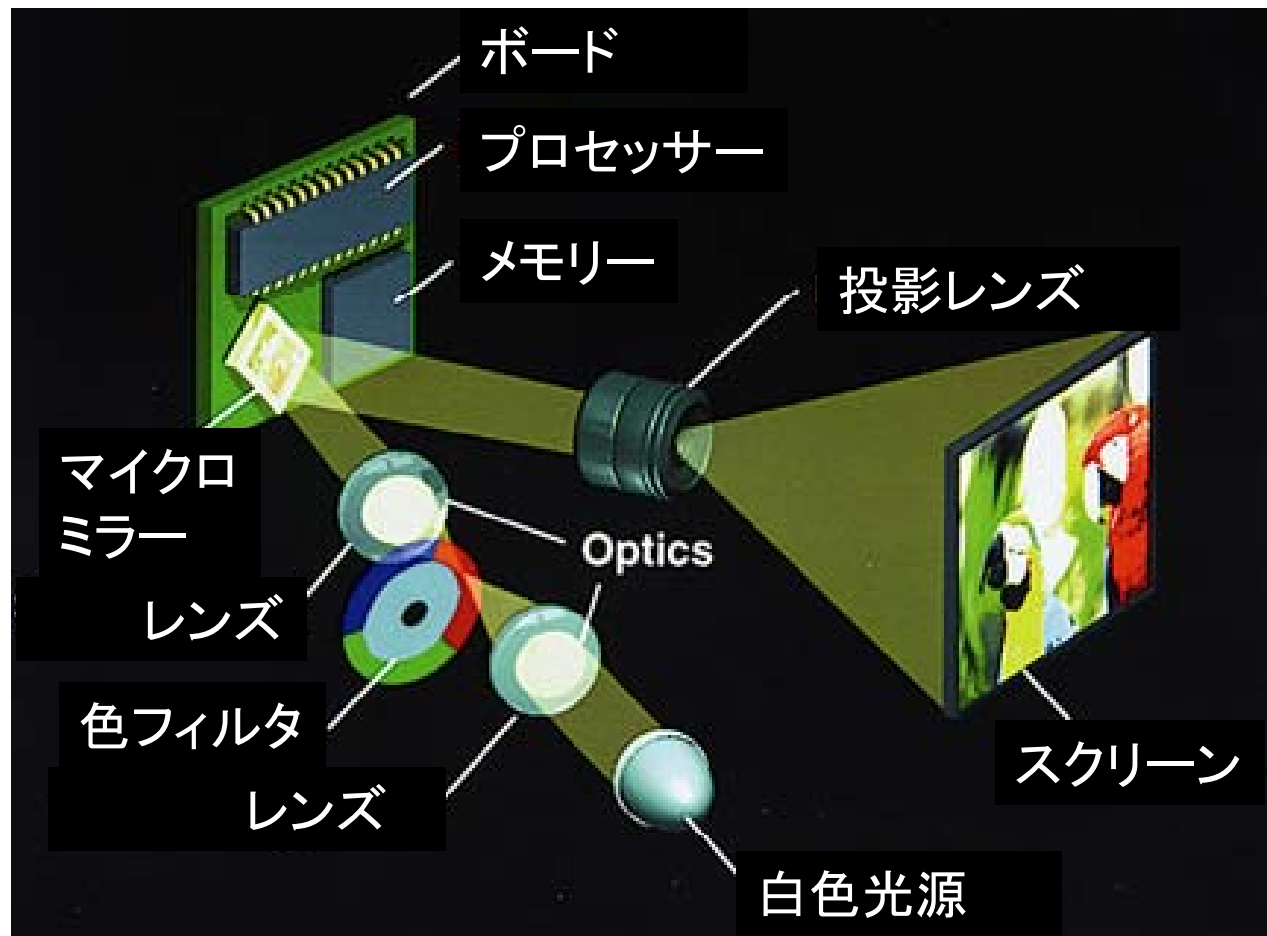
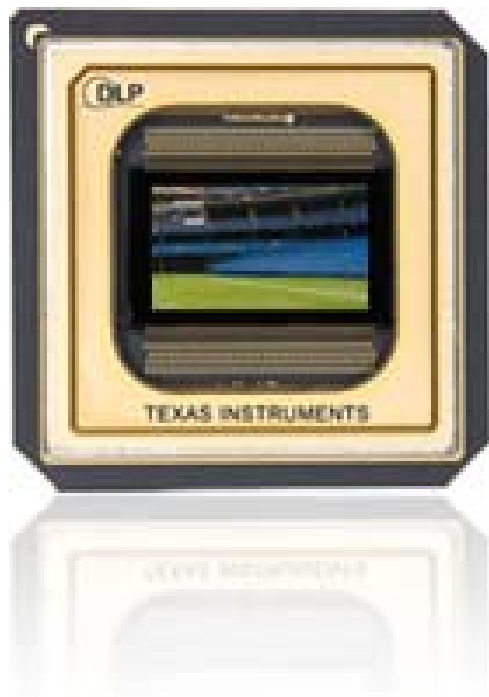
内容

1. 集積型MEMSミラーについて
2. LSIと集積した波長選択スイッチ用MEMSミラー
3. Si連続メンブレン-デフォーマブルミラーの開発状況

ホームシアター データプロジェクター

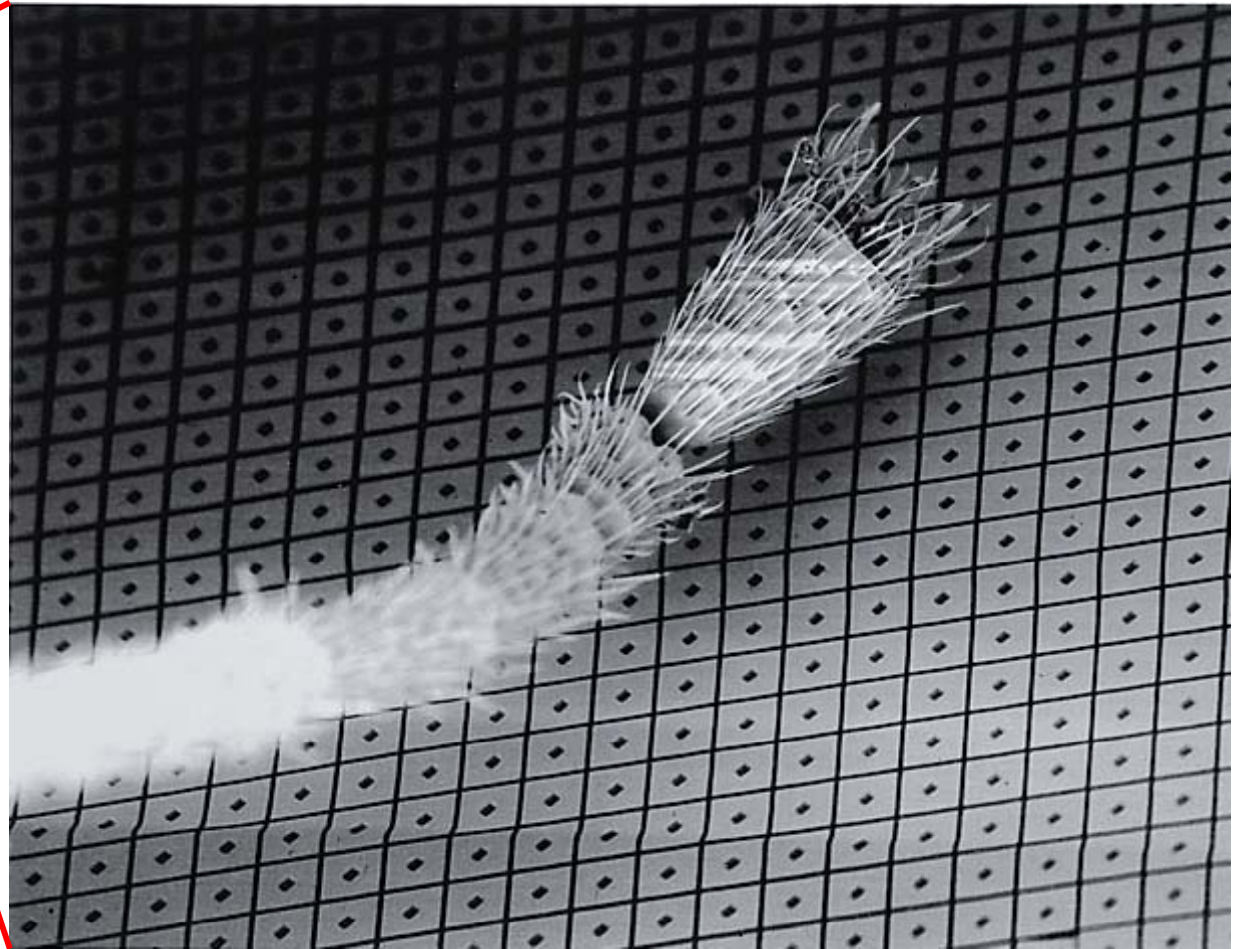
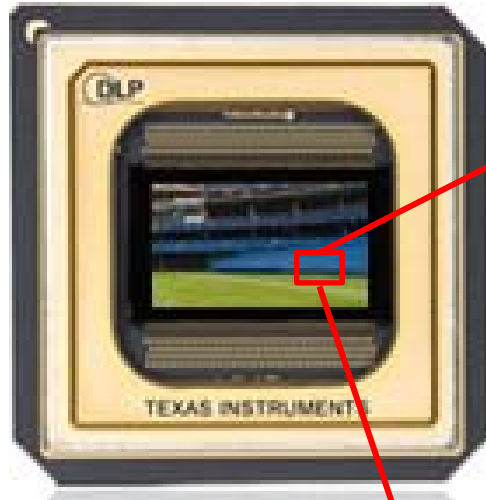


マイクロミラー(DMD)ディスプレイの光学系



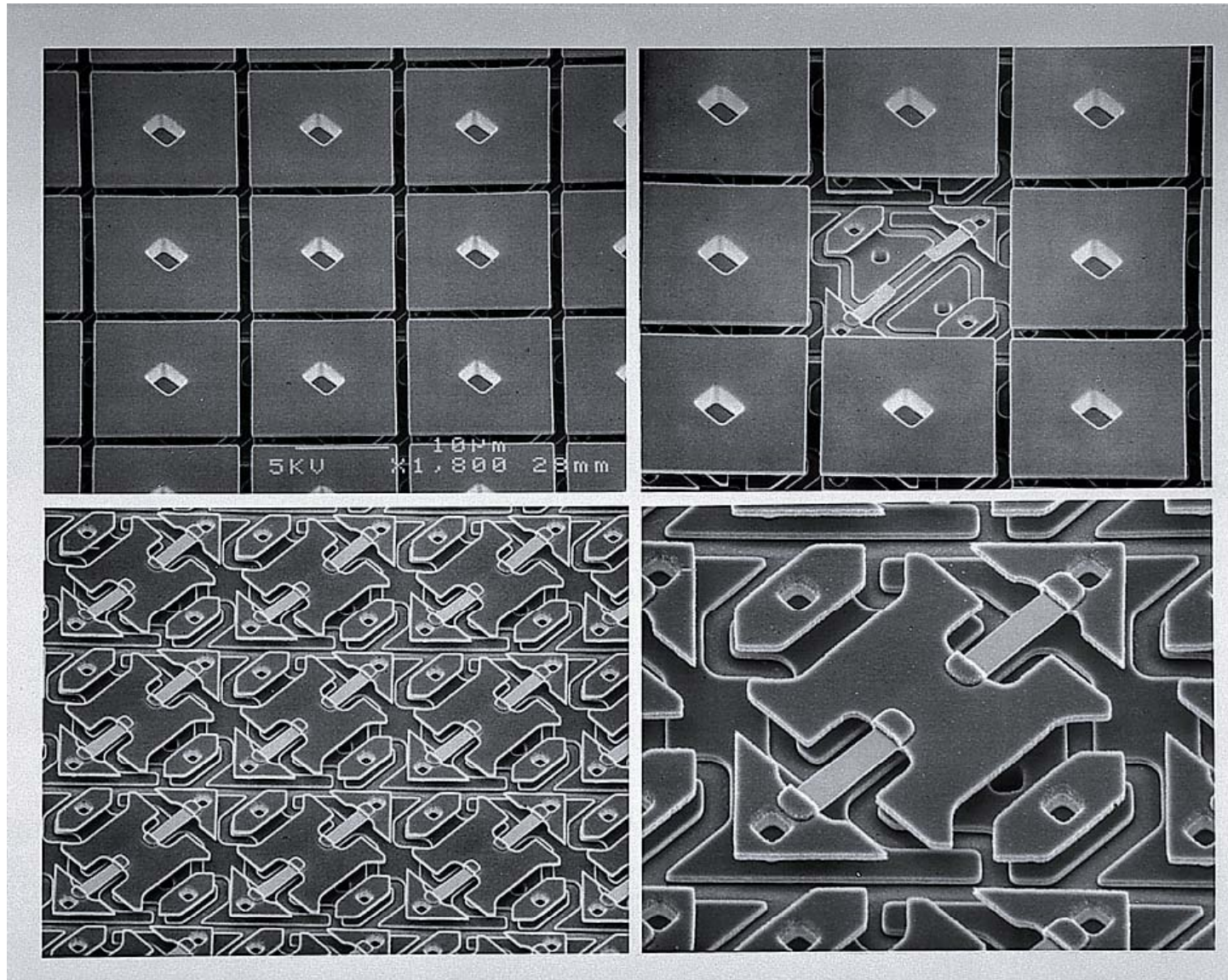
<https://youtu.be/qOsibeDX8jM>

マイクロミラー(DMD)ディスプレイの構造

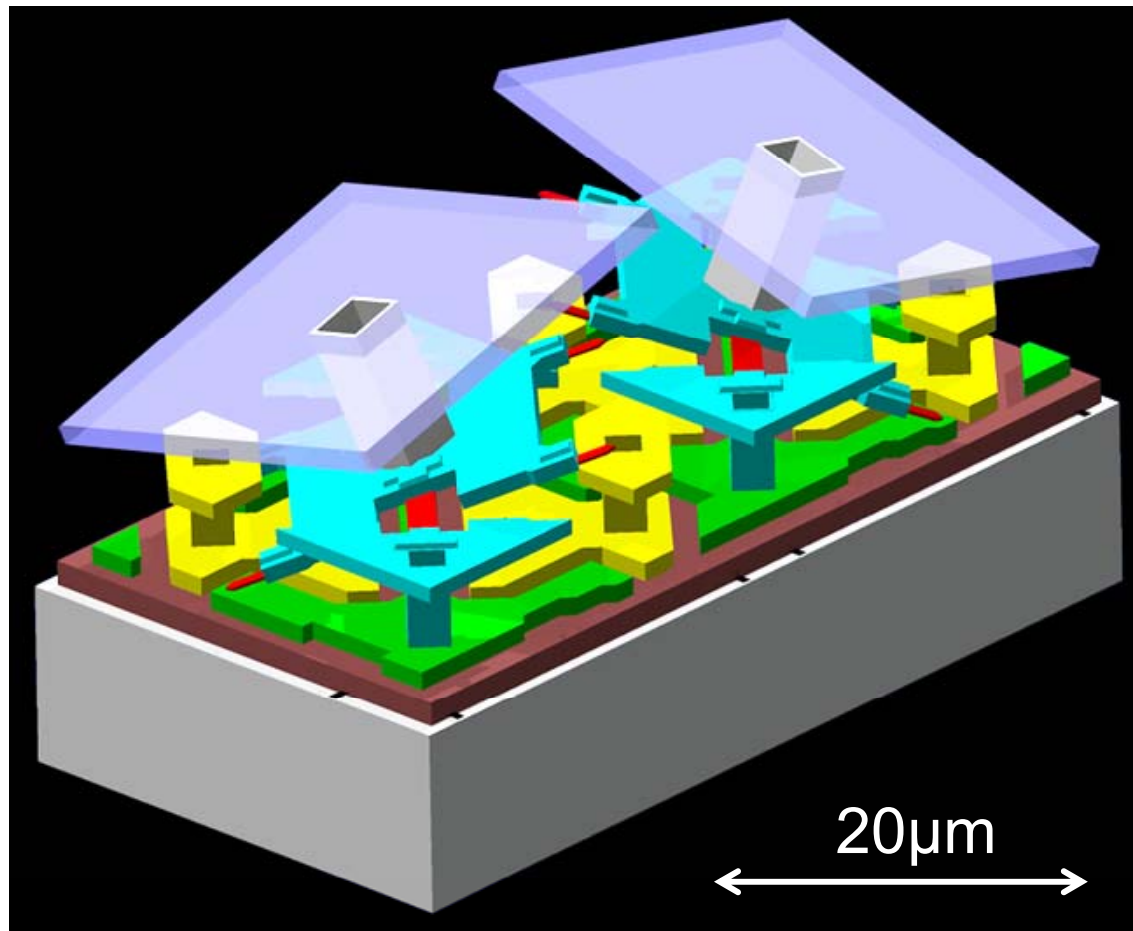


マイクロミラーとありの足

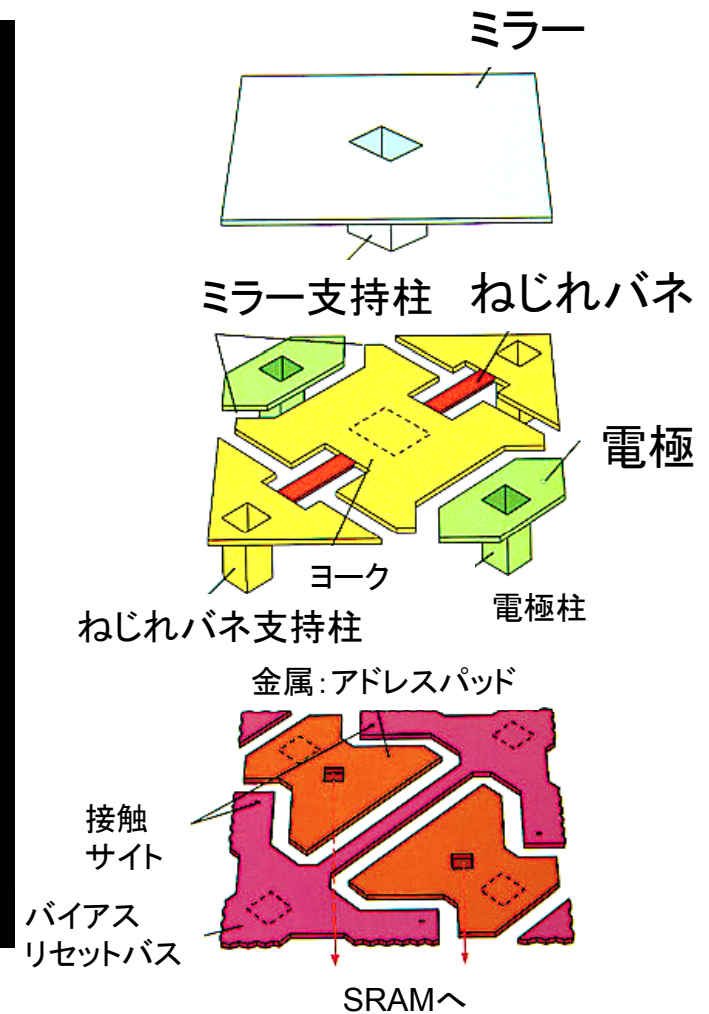
マイクロミラー(DMD)ディスプレイの内部構造



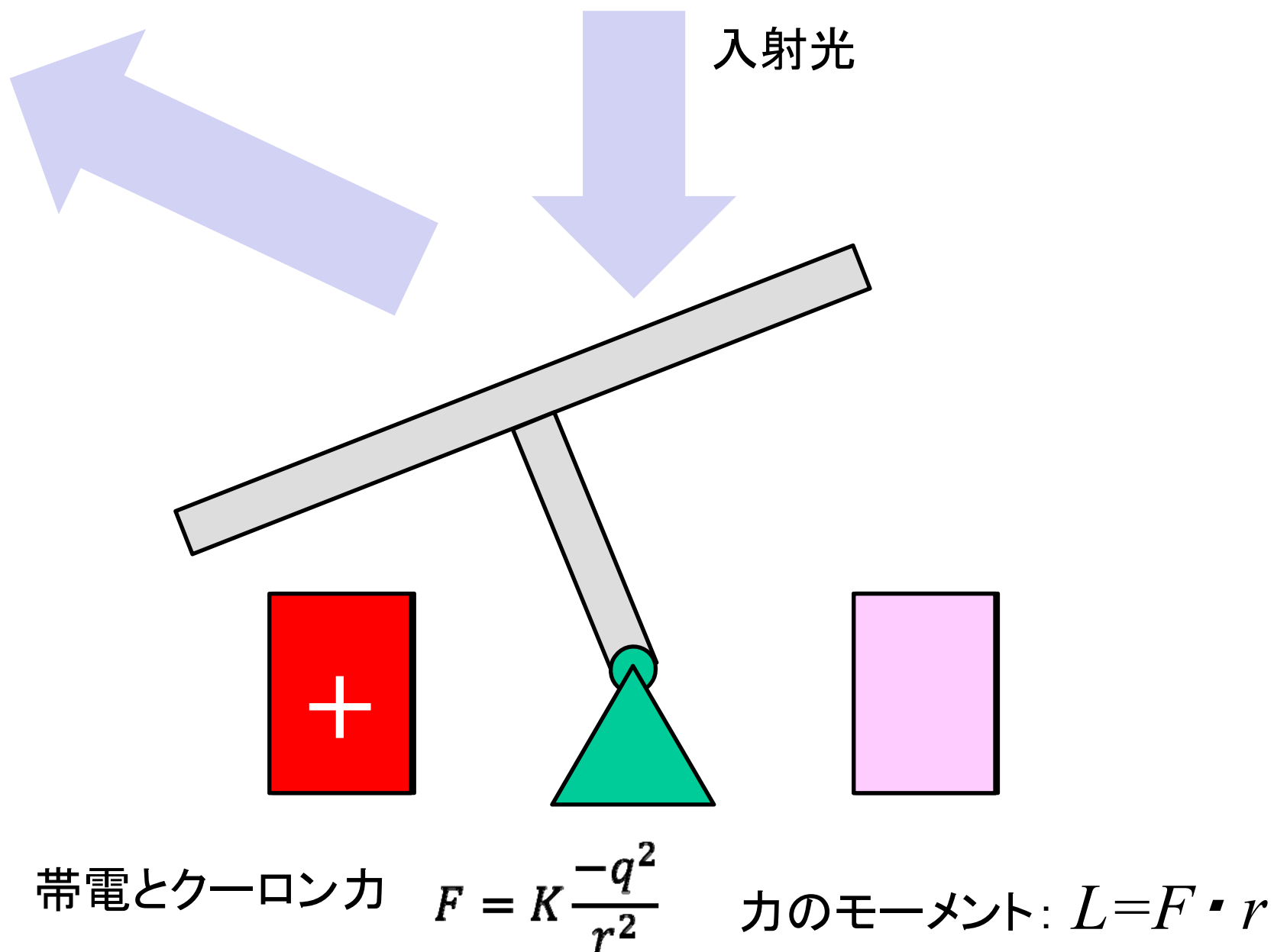
マイクロミラーの構造と動作



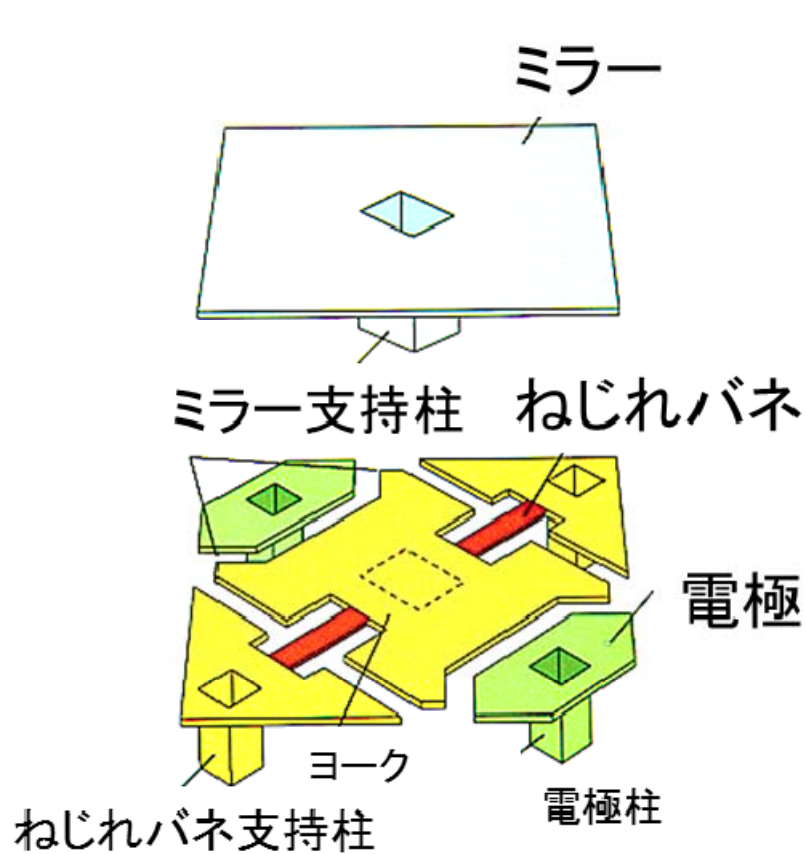
マイクロミラーの構造



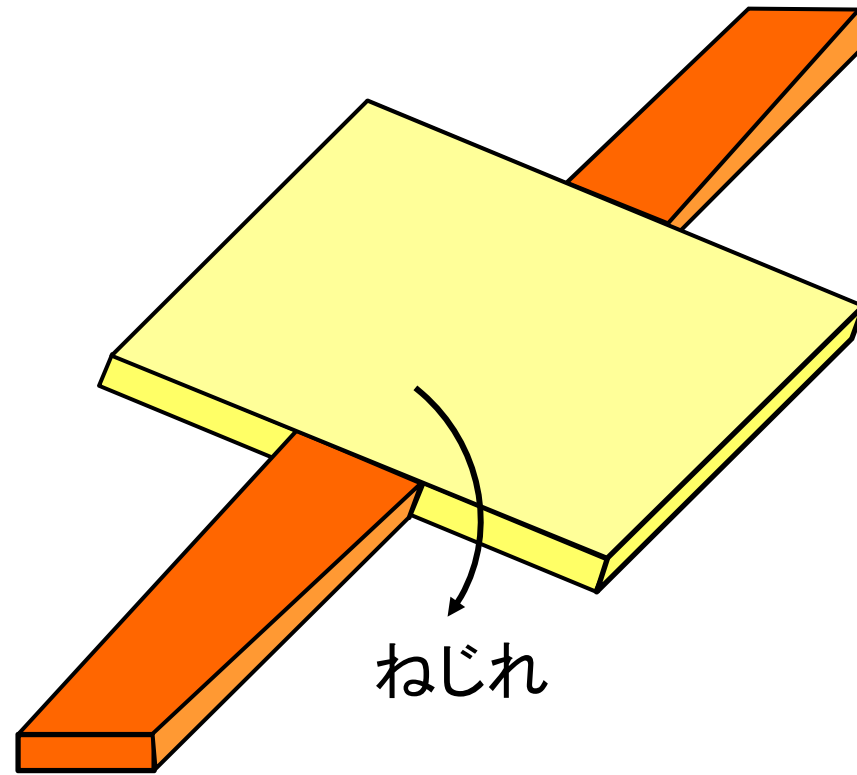
マイクロミラーの動作原理



マイクロミラーの機械構造

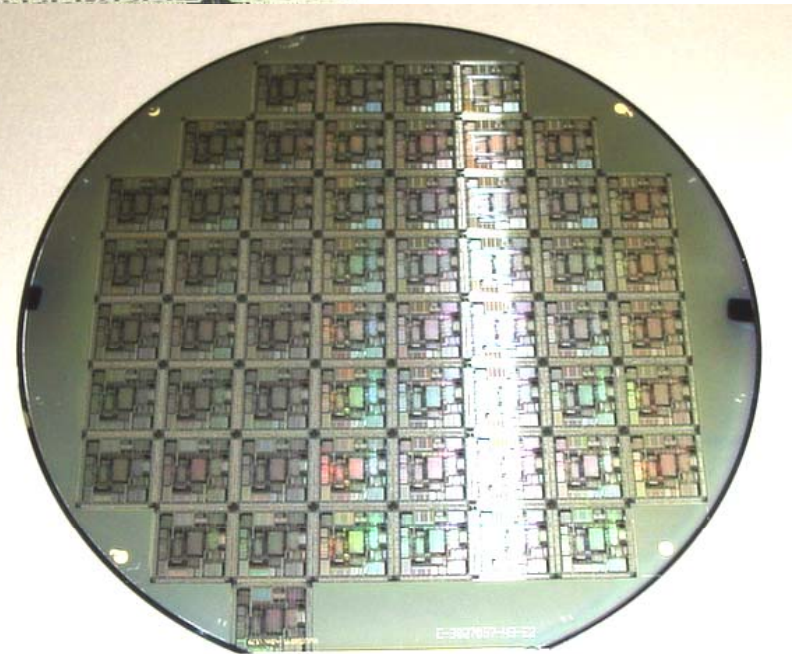
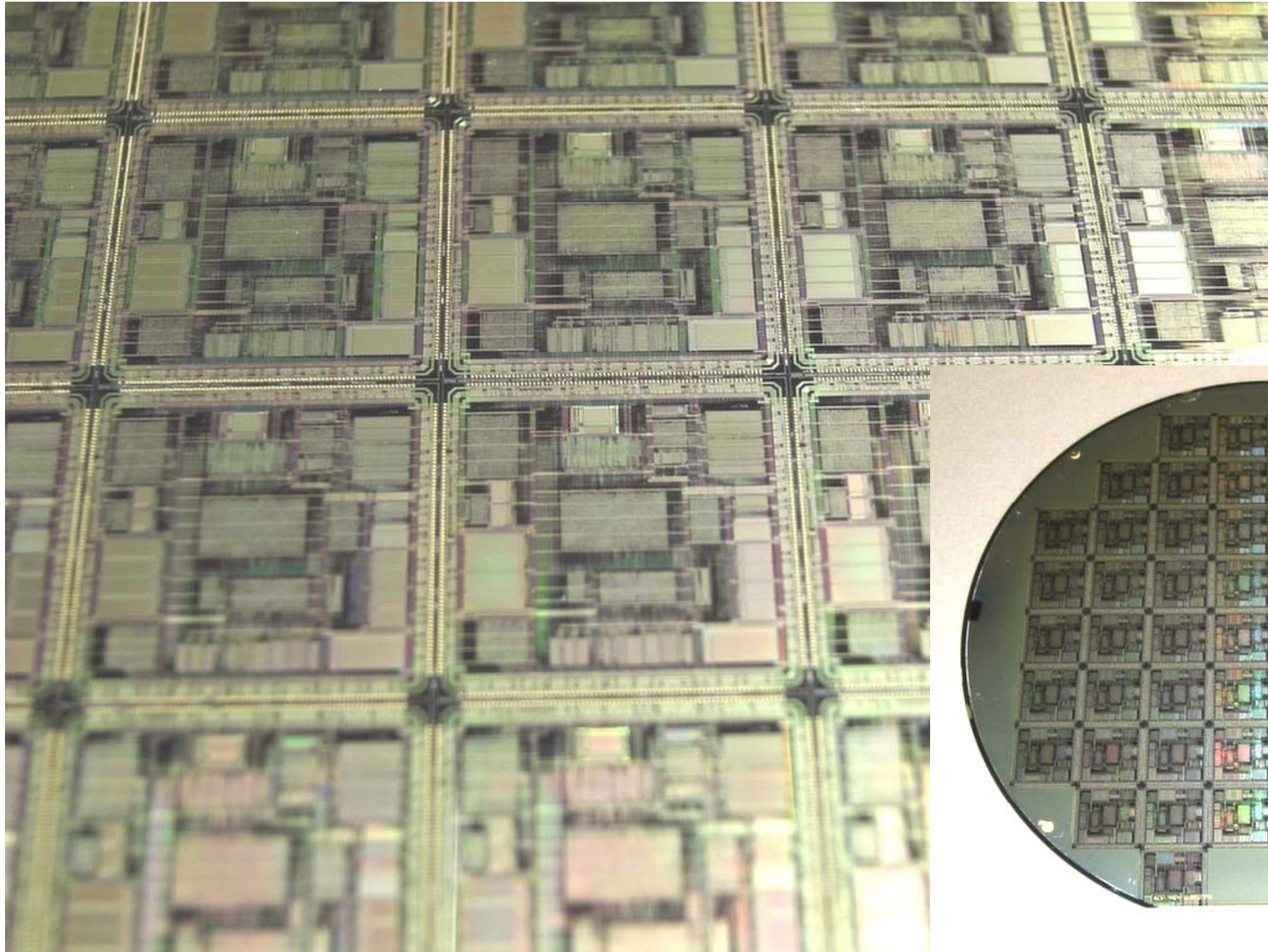


フックの法則 $F = kx$



ねじれのフックの法則 $L = k_{\theta} \theta$
(L :力のモーメント θ :回転角)

集積回路の微細加工技術



One-Megapixel Monocrystalline-Silicon Micromirror Array on CMOS Driving Electronics Manufactured With Very Large-Scale Heterogeneous Integration

F. Zimmer, M. Lapisa, T. Bakke, M. Bring, J. MEMS 20(3)(2011)564-572 Fraunhofer Institute

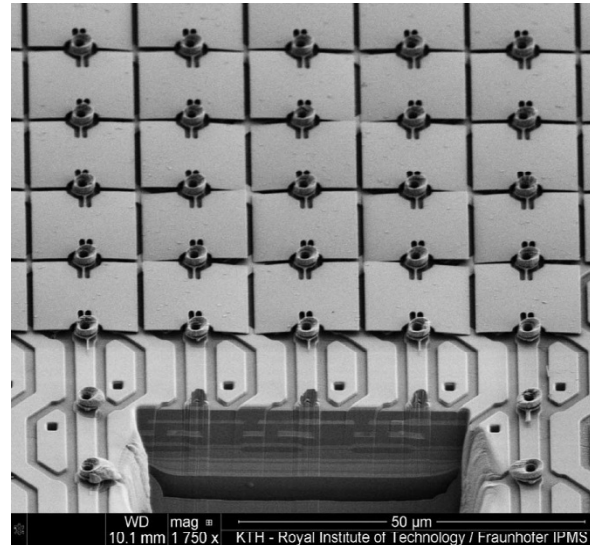
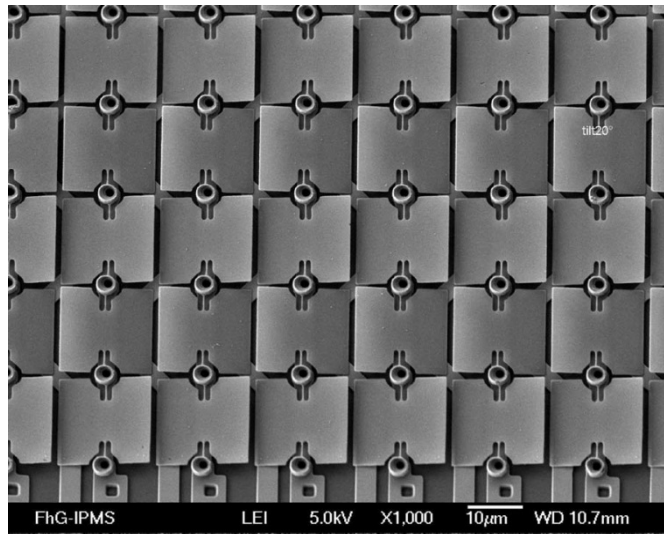
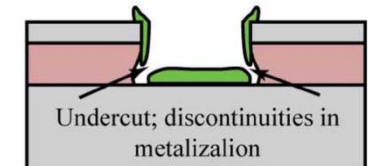
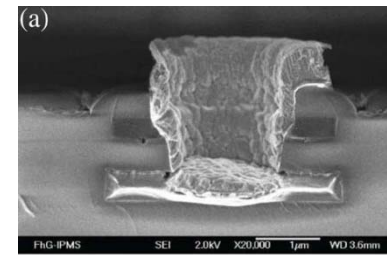
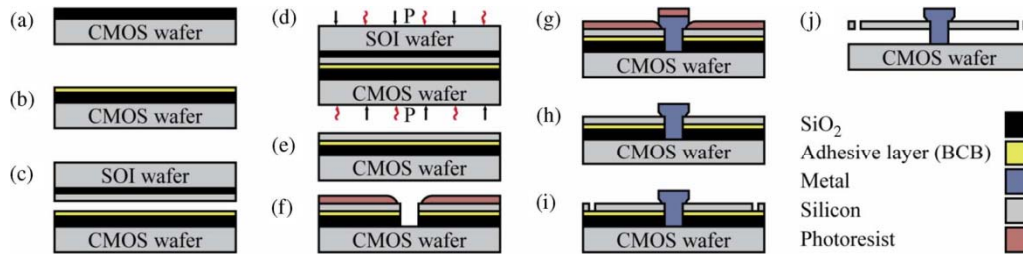
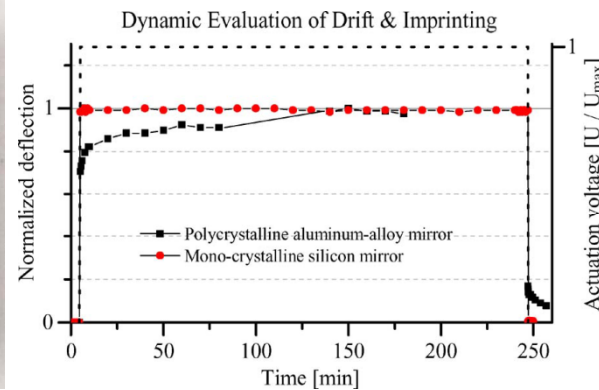
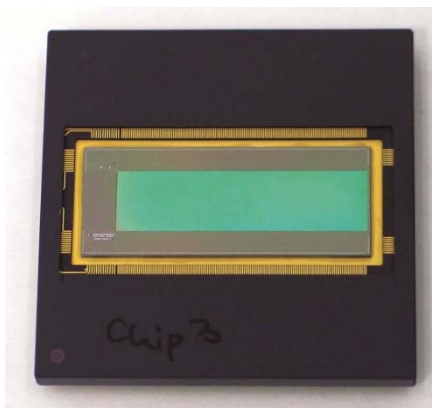
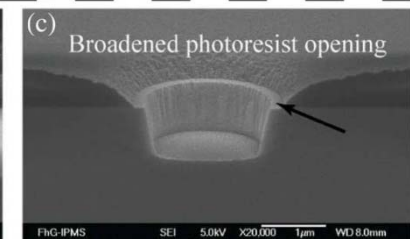
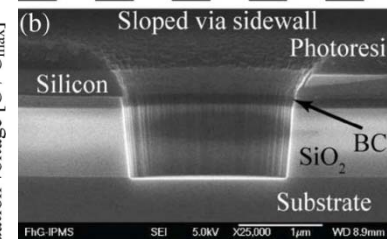


TABLE I
CHARACTERISTICS OF THE MONOCRYSTALLINE-SILICON SLM

Property	Nominal value
SLM architecture	Tilting micro-mirrors with reflective surface and hinges in same plane (1-level)
Array size	1 megapixel (2048x512 px)
Mirror material	Mono-crystalline silicon
Crystal type (c-Si)	Diamond-cubic (100)
Plastic deformation (c-Si)	None (brittle material)
Young's modulus	160 GPa
Hinge material	Mono-crystalline silicon
Hinge type	Torsional
Hinge dimensions (LxWxH)	2.6 μm x 0.6 μm x 340 nm
C-Si roughness (RMS)	<1 nm (for area of 1 μm ²) (measured)
Mirror size	16x16 μm ²
Smallest dimension of micro-mirror features	600 nm
Mirror-membrane thickness	340 nm
Mirror air gap to electrode	700 nm
CMOS operating voltage	~ 25 V
Max. edge deflection (tilt)	~ 160 nm



Legend:
 Silicon
 Sacrificial polymer
 Metal



PACKAGING OF 11 MPIXEL CMOS-INTEGRATED SIGE MICRO-MIRROR ARRAYS

A Witvrouw et.al. IMEC, Philips Applied Technologies, ASML

MEMS 2009 136-139

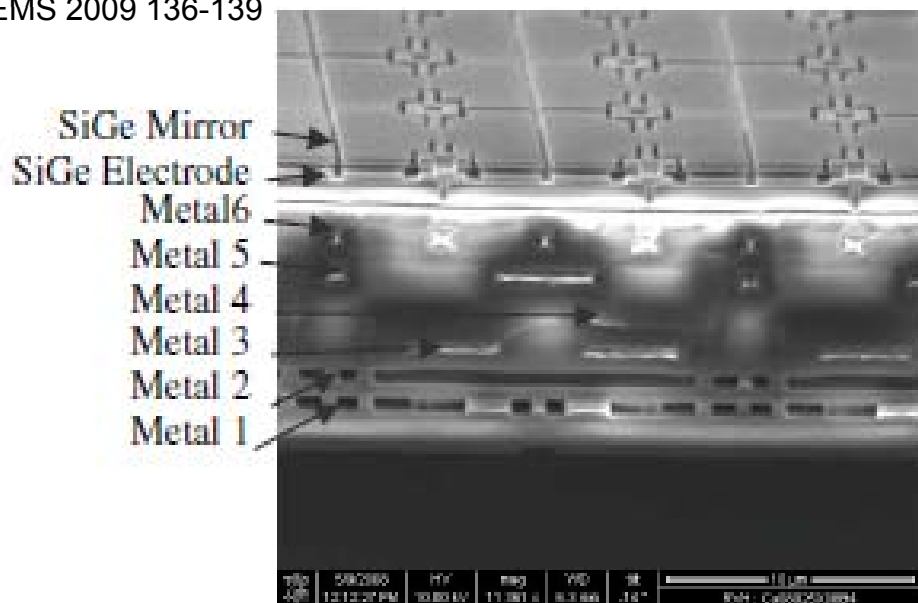


Figure 1: Cross-sectional view of the integrated micro-mirror array, showing the mirrors on top of the 6 layers of Al interconnect



Figure 11: Fully packaged and wire-bonded SLM assembly.

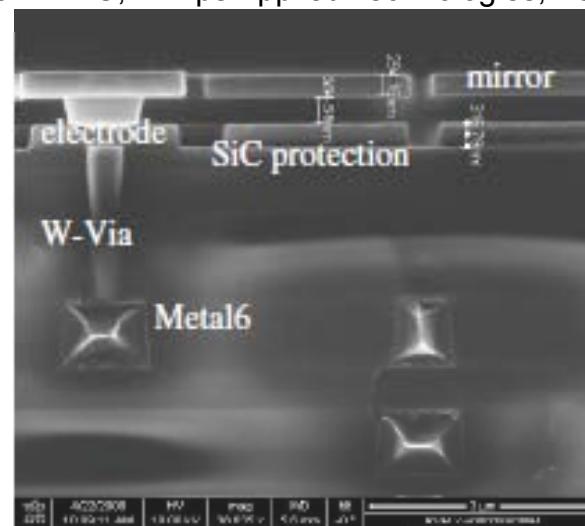


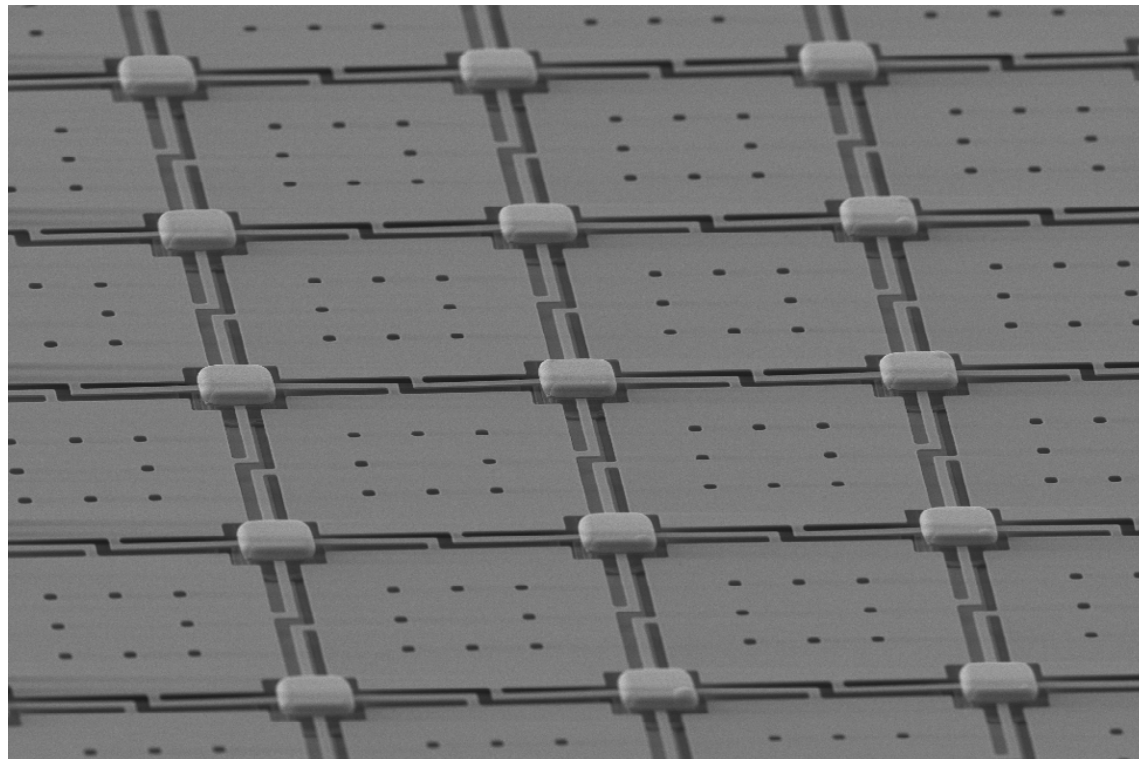
Figure 3: Cross-section of mirror showing W-via, SiC protection layer, SiGe electrodes, SiGe mirror layer.

Table 1: Schematic process flow of micromirror module.

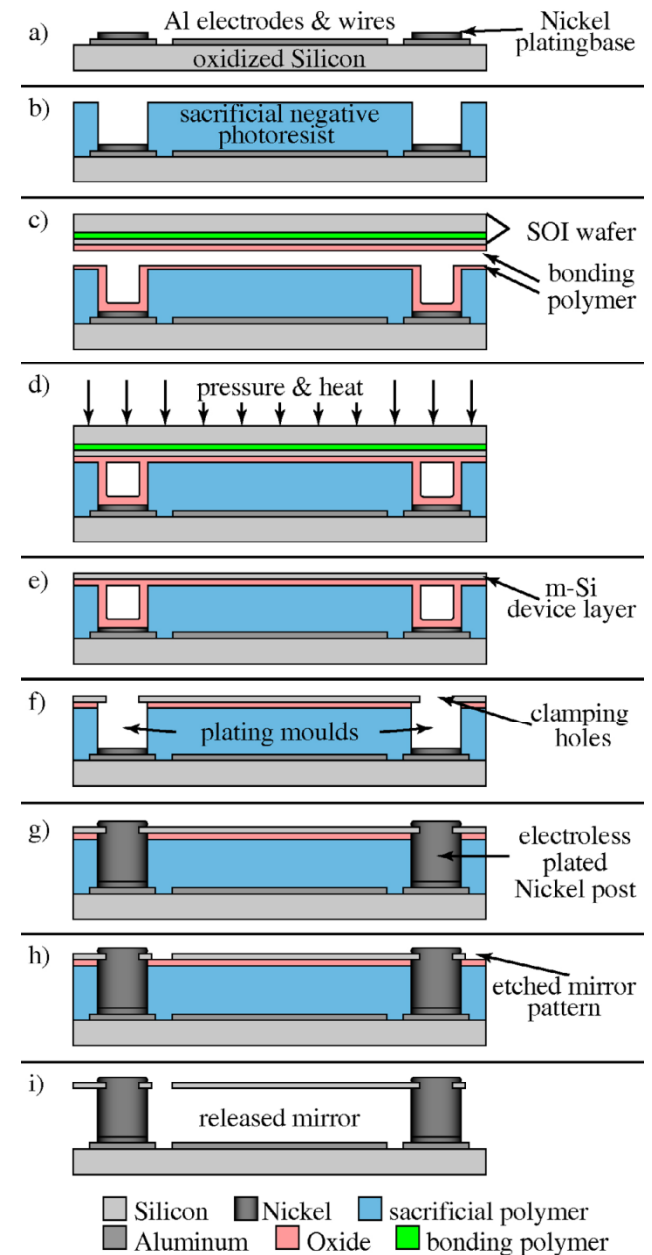
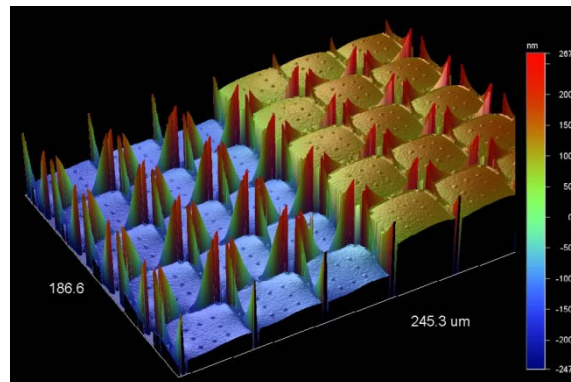
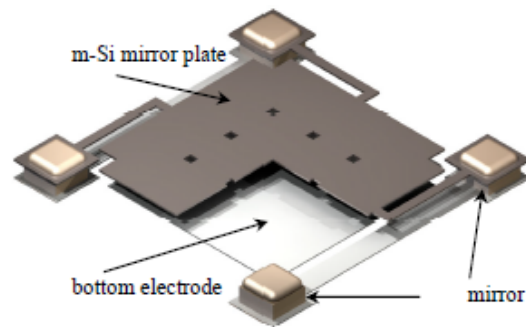
Start: CMOS base wafer (from NXP)
Top level metal planarisation + passivation
SiC protection layer
W-vias
SiGe electrode + planarisation
Sacrificial oxide deposition
Mirror hinge formation
SiGe mirror layer deposition + CMP
Phasestep etch (optional)
Al coating (optional)
Mirror etch
Oxide protection + Al bondpads
Release etch mirrors

CMOS-INTEGRABLE PISTON-TYPE MICRO-MIRROR ARRAY FOR ADAPTIVE OPTICS MADE OF MONO-CRYSTALLINE SILICON USING 3-D INTEGRATION

M Lapisa, F Zimmer, F Niklaus, A Gehner, G Stemme Royal Institute of Technology, Fraunhofer Institut MEMS (2009) 1007-1010



10 μm EHT = 5.00 kV Signal A = SE2 Date :11 Jul 2008
 WD = 8 mm Mag = 2.27 K X Time :16:37:49



32×32 OPTICAL PHASED ARRAY WITH ULTRA-LIGHTWEIGHT HIGH-CONTRAST-GRATING MIRRORS

Transducers 13 pp. 2505-2508

B. W. Yoo¹, M. Megens^{1,2}, T. K. Chan², T. Sun¹, W. Yang¹, D. A. Horsley², C. J. Chang-Hasnain¹, and M. C. Wu¹

¹University of California, Berkeley, CA, USA

²University of California, Davis, CA, USA

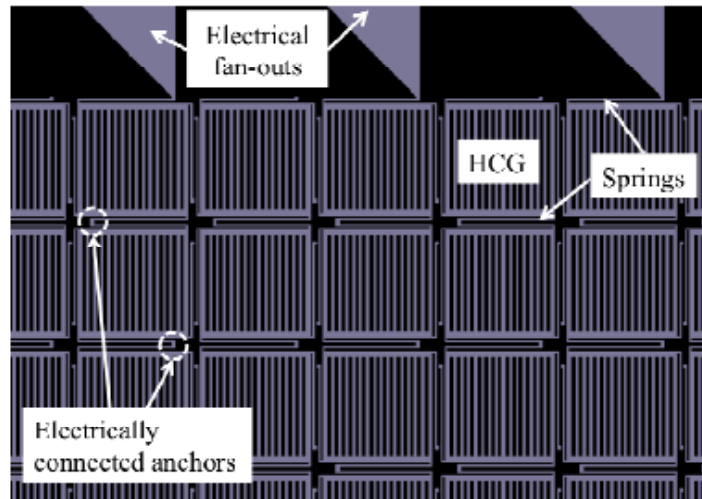


Figure 1: Layout of the MEMS optical phased array. Anchors of HCG mirrors electrically connect adjacent mirrors in the same row, increasing fill-factor.

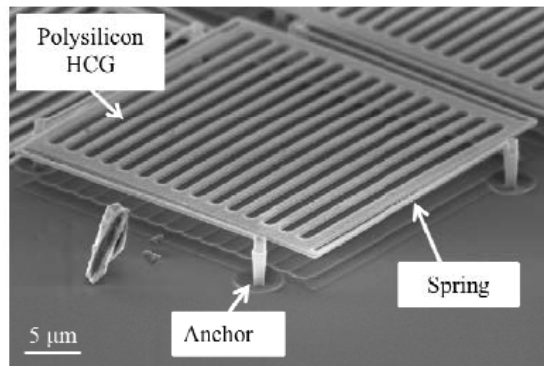


Figure 2: Scanning electron microscope (SEM) image of a polysilicon high contrast sub-wavelength grating (HCG). The pixel surface is extremely flat due to the low stress 400

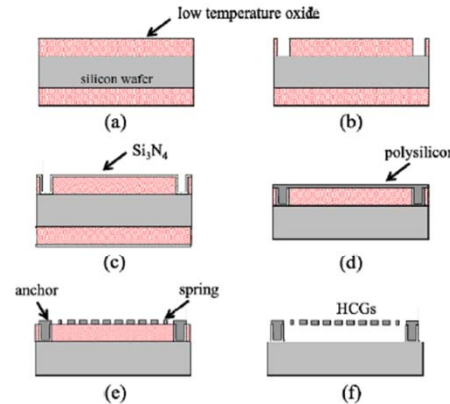


Figure 5: Fabrication process.

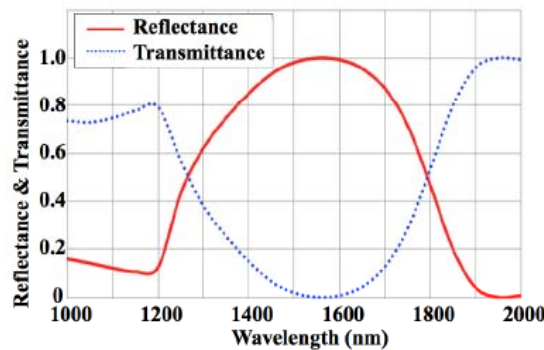


Figure 4: Theoretical Reflectance and transmittance spectra of the HCG mirror.

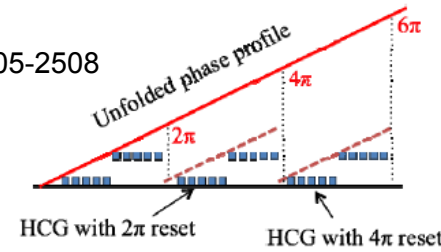


Figure 8: Modulo 2π phase shifting using HCGs to create beamsteering at maximum angle. Light is in the form of a sine wave. Thus, $2n\pi$ ($n=0,1,2\dots$), from a phase point of view, are all the same so that a stair-step ramp approach can be used to steer light.

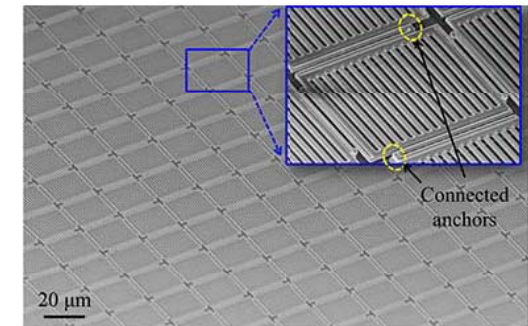


Figure 6: SEM image of the fabricated HCG phase shifter array. Inset shows that anchors in the same column are linked to adjacent HCGs via mechanical springs for one-dimensional beamsteering. This can be extended to two-dimension beamforming by adding multi-layer interconnects underneath the HCG mirrors.

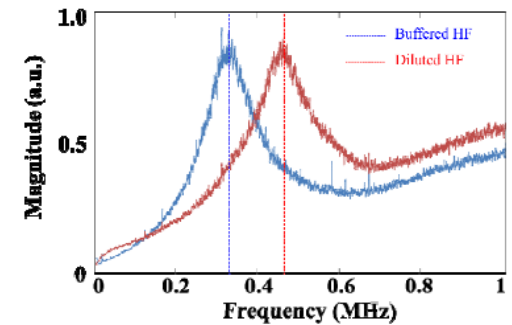
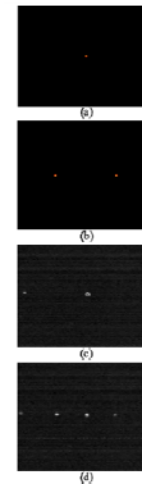
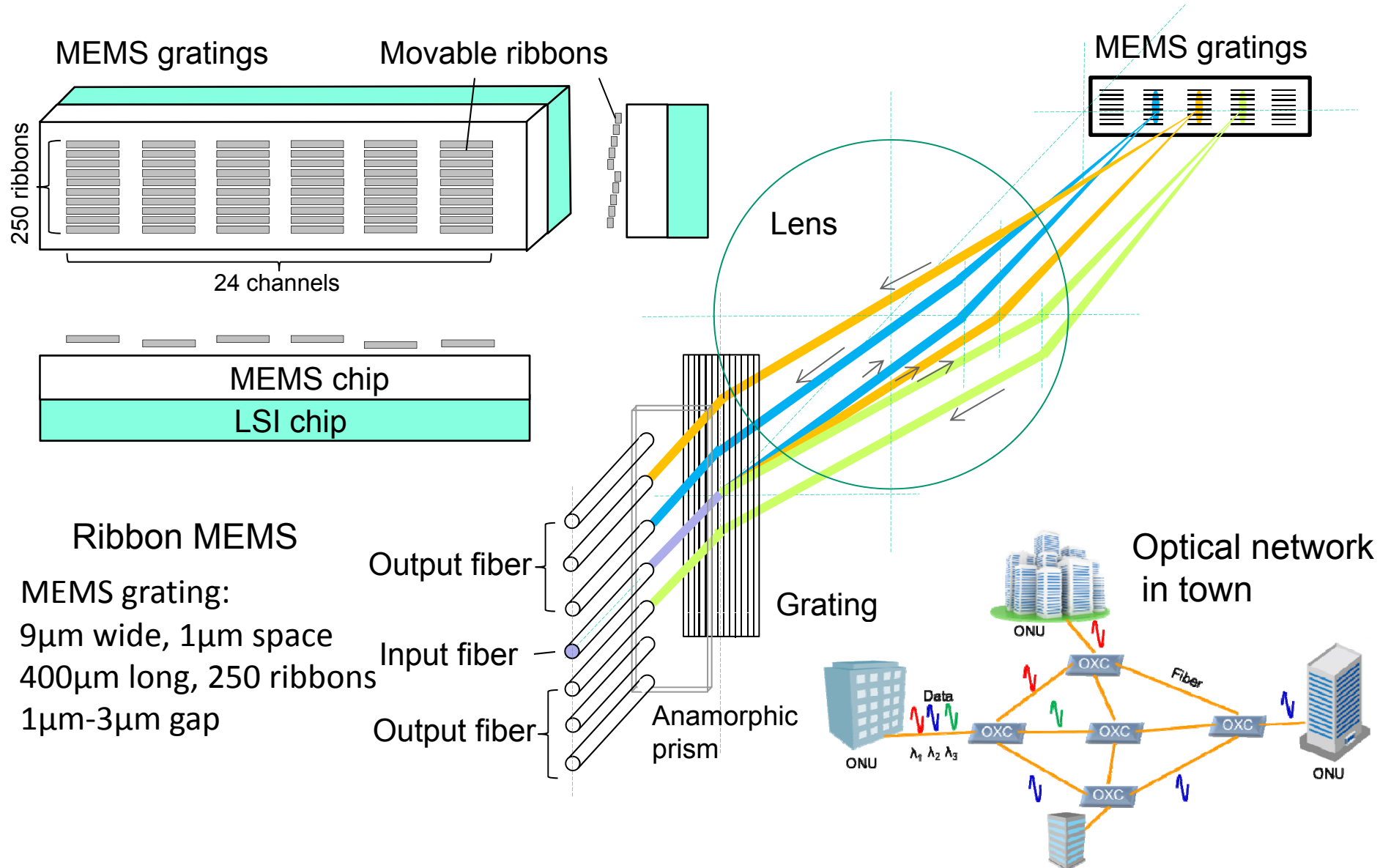


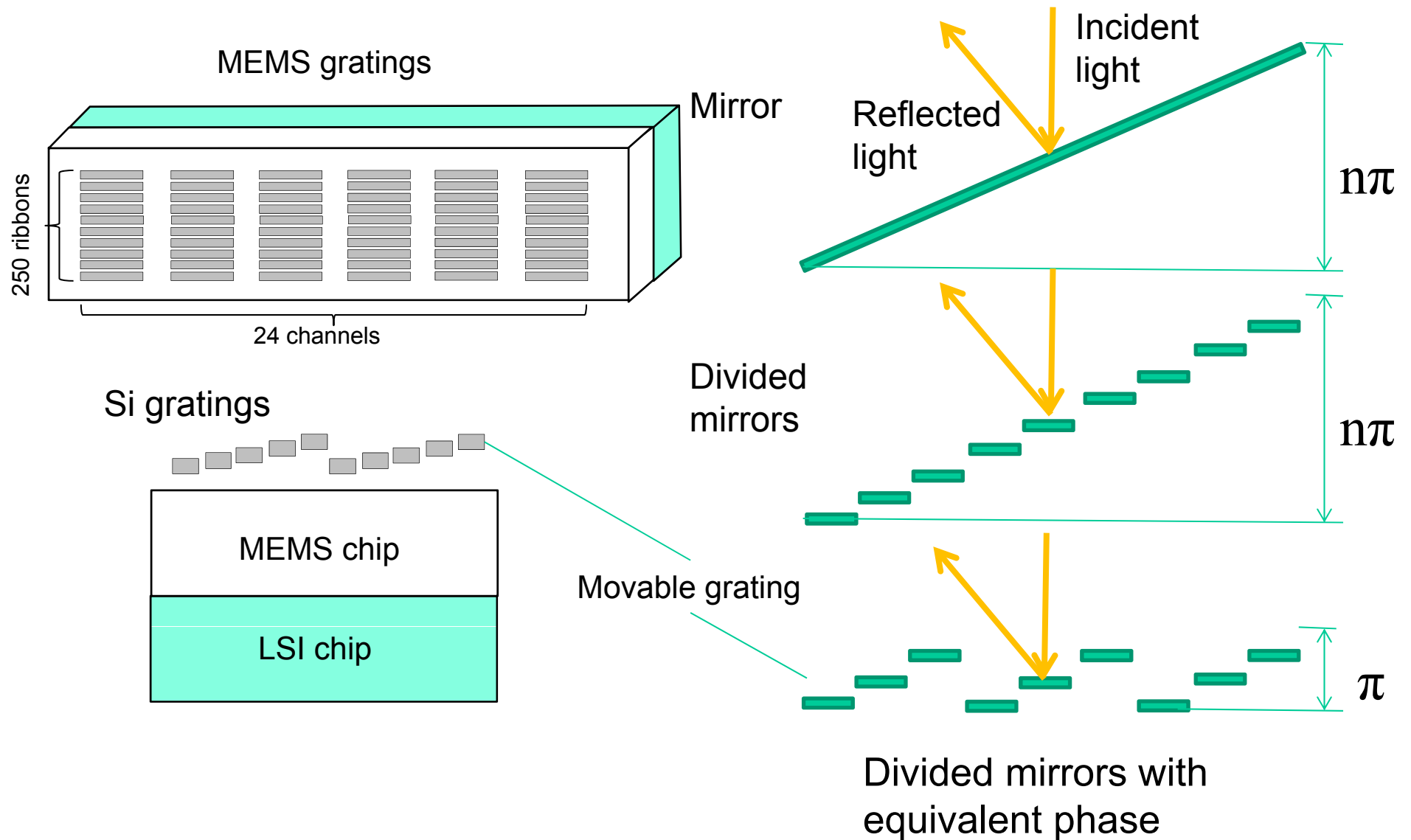
Figure 7: Experimental resonant frequencies of an HCG using a laser Doppler vibrometer (LDV) system. A HCG released in diluted HF depicts the measured resonant frequency is 0.46 MHz, which agrees well with the theory.



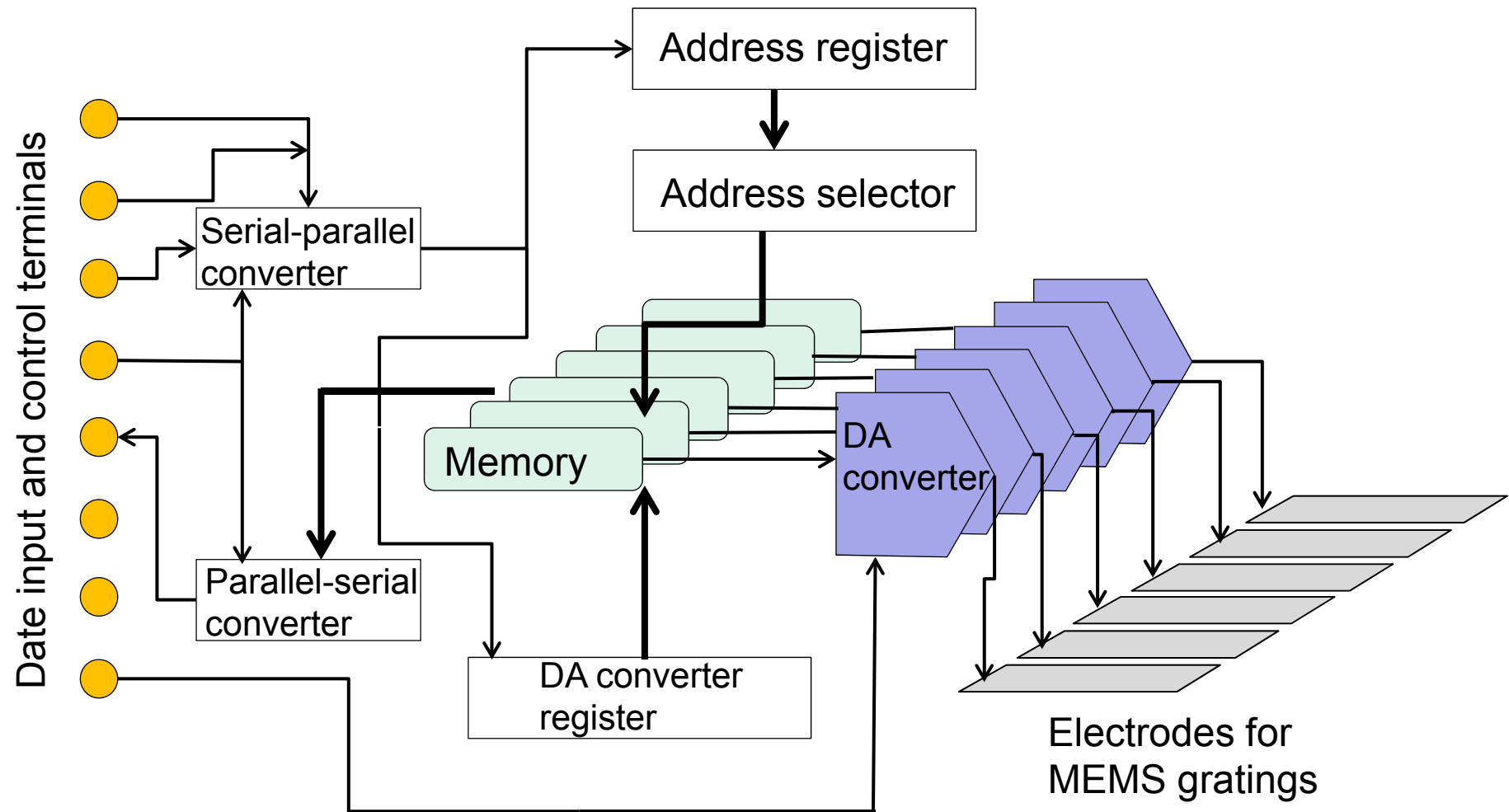
波長選択スイッチ (MEMSグレーティング)



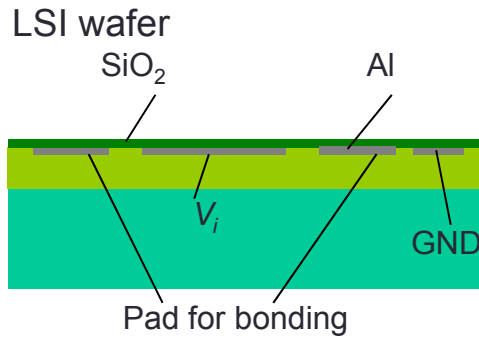
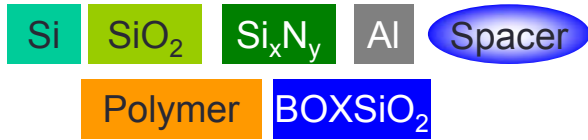
光ビーム走査の原理



制御用LSIの設計



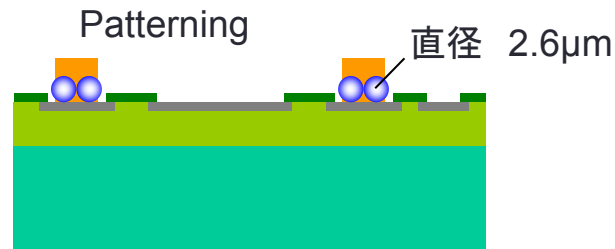
製作プロセス



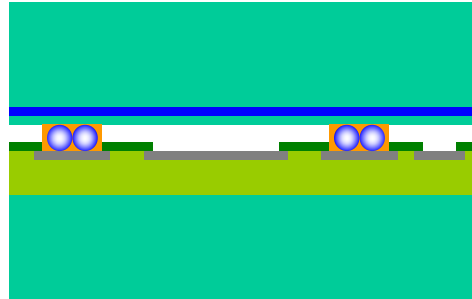
(a) Photolithography, SiN etching



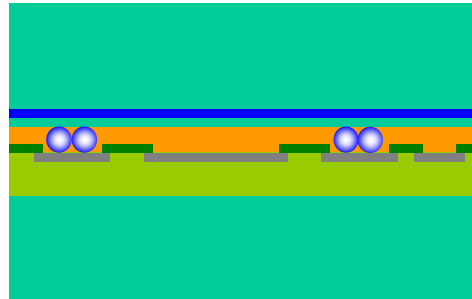
(b) Polymer & spacer deposition,



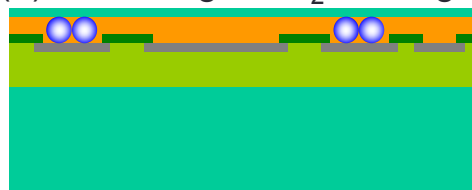
(c) SOI wafer bonding



(d) Polymer filling



(e) Si etching, SiO₂ etching



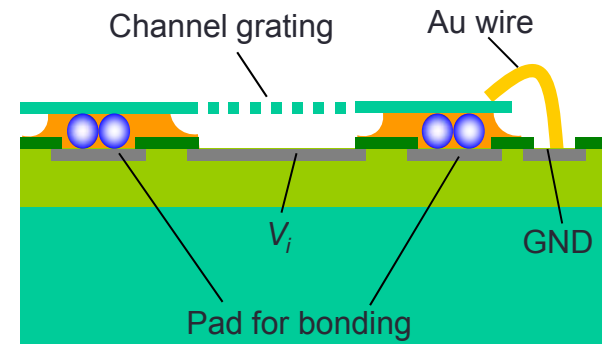
(f) Si patterning



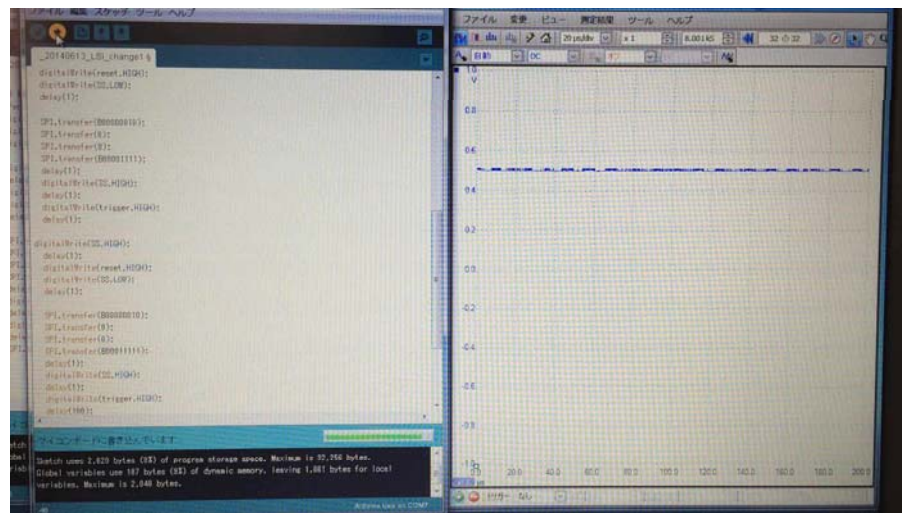
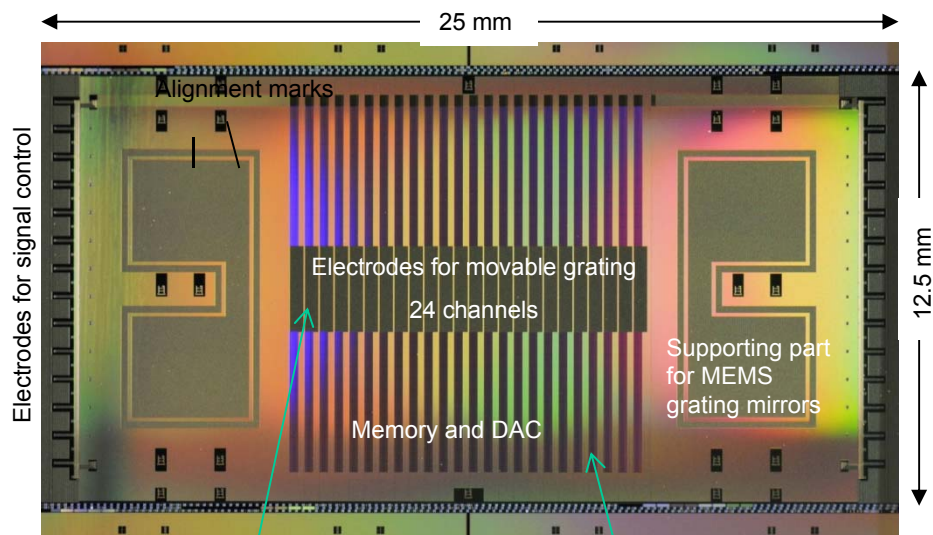
(g) Polymer etching



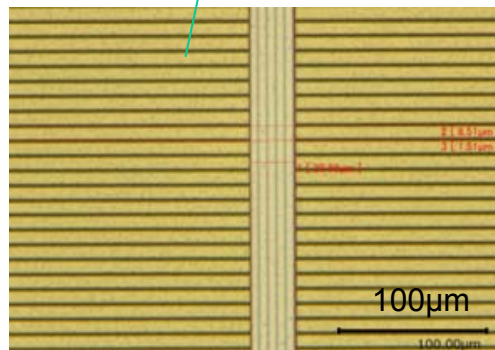
(h) Wire bonding



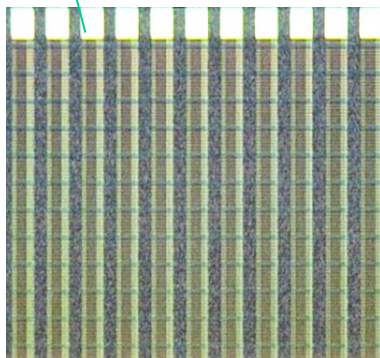
製作したLSI



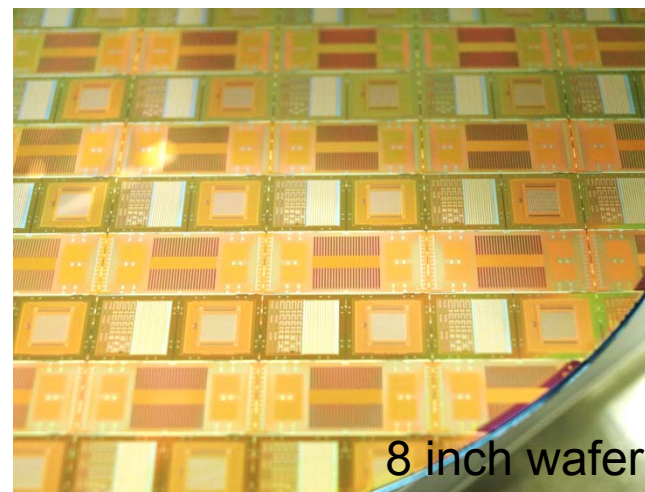
Voltage output by program



Electrodes for grating

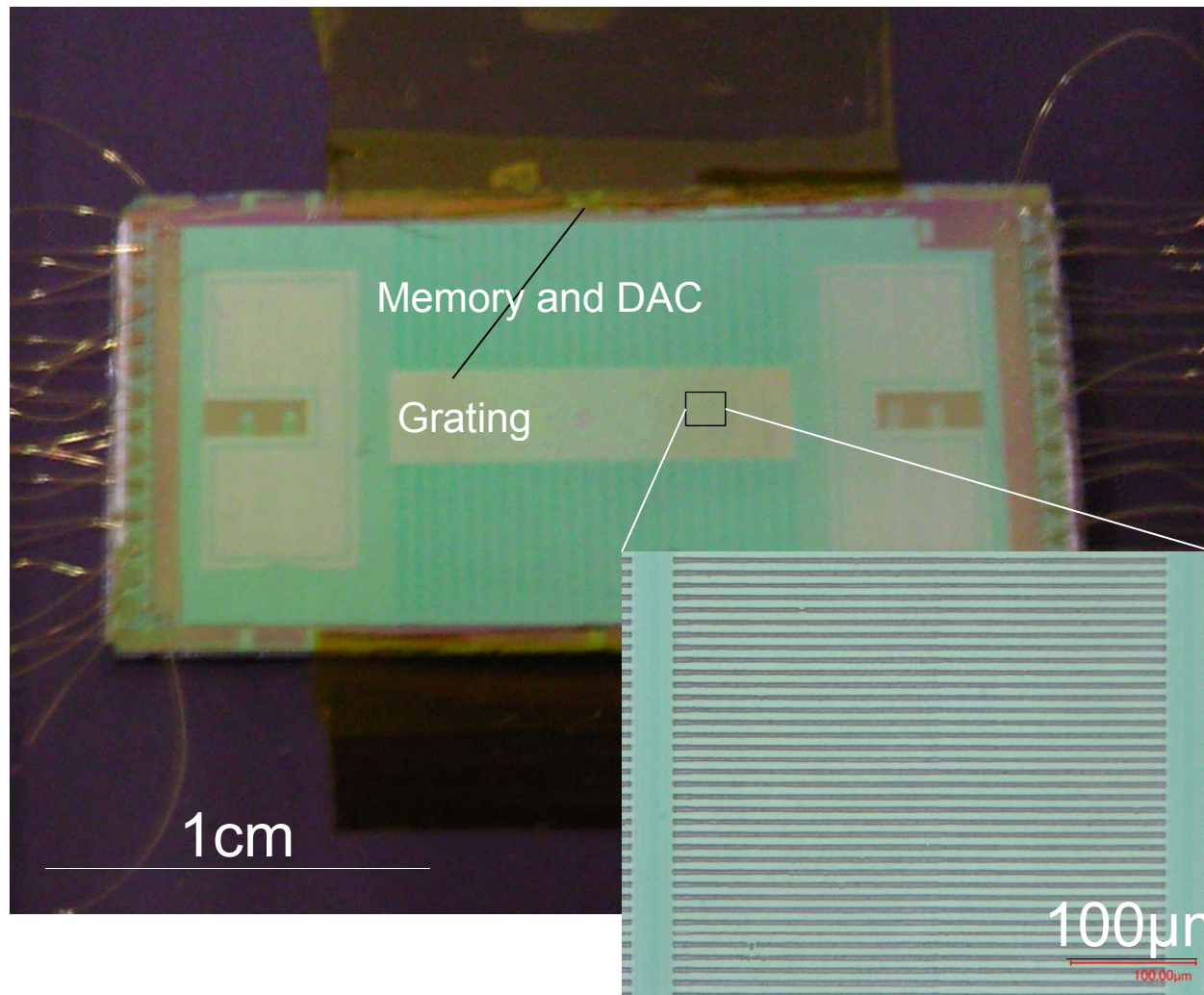


Memory and DAC



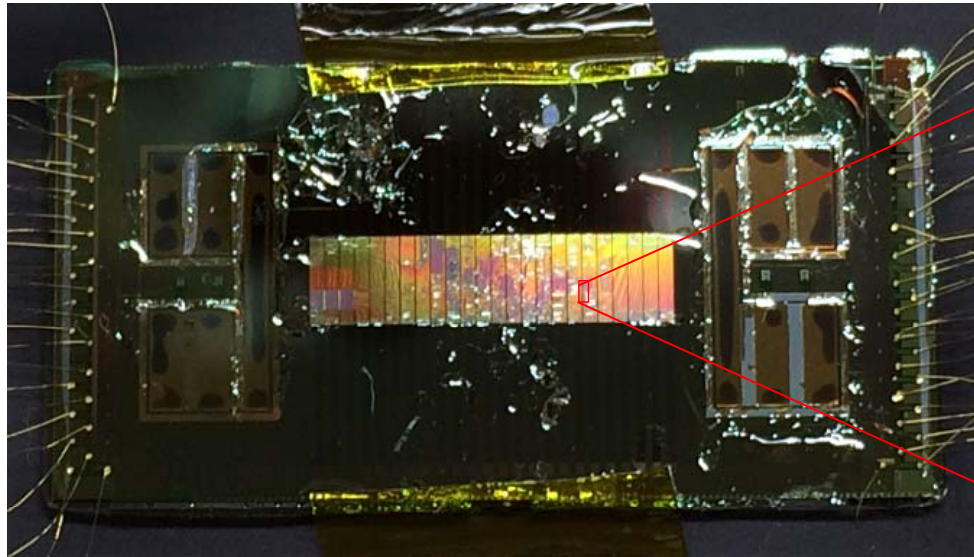
8 inch wafer

LSI上に製作したSiグレーティング

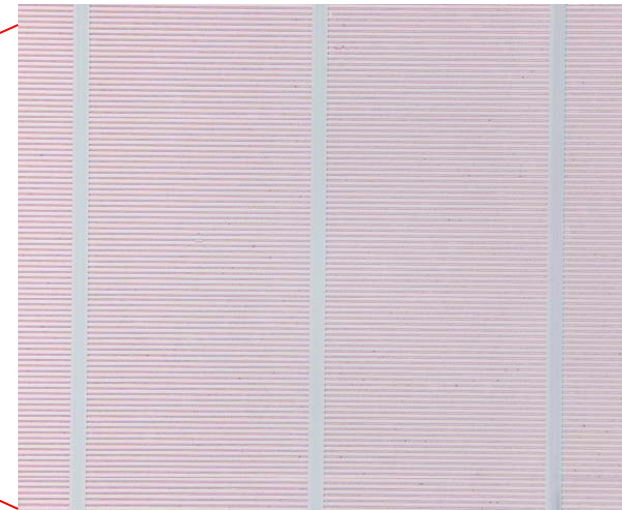


Silicon grating on LSI circuit

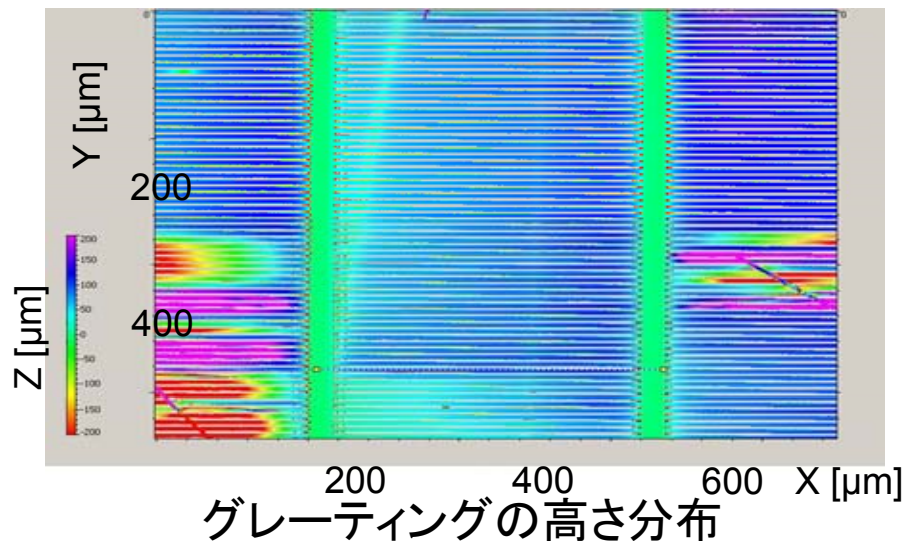
LSI上に製作したMEMSグレーティング



LSI上に集積したグレーティング



グレーティングの光学顕微鏡写真

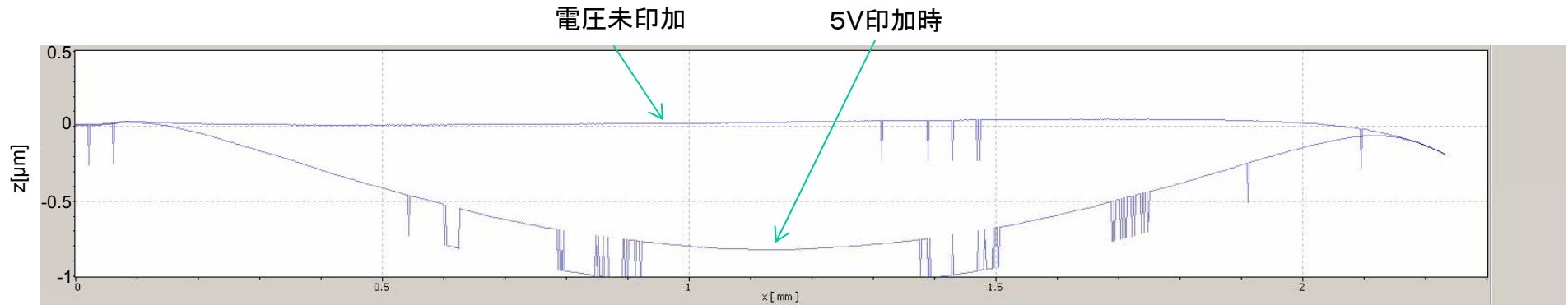


グレーティングの高さ分布

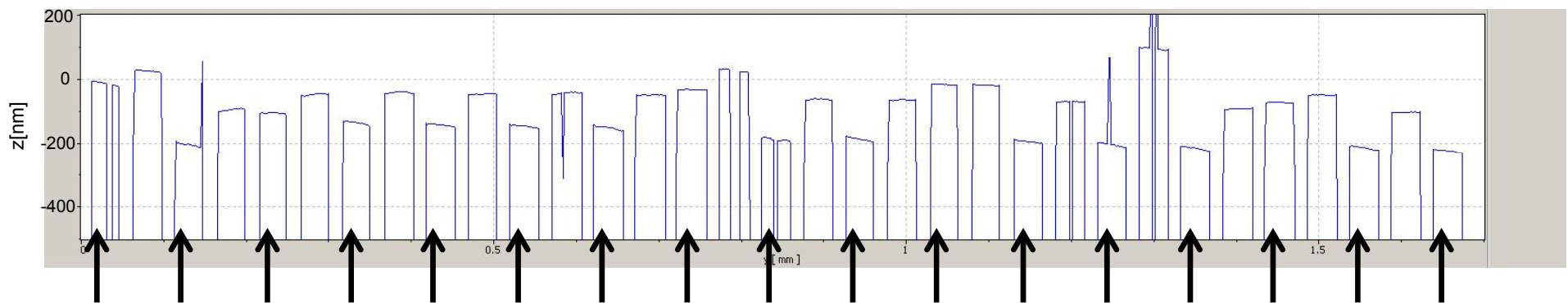
- * 一部にポリマー接合が不十分であるが、グレーティングは精度よく製作できた
- * ガラス上に製作したグレーティングで見られた凹凸も解消された

電圧印加時のグレーティング変位測定

グレーティング形状測定： 下部電極に5Vを印加し800nmの変位が得られた



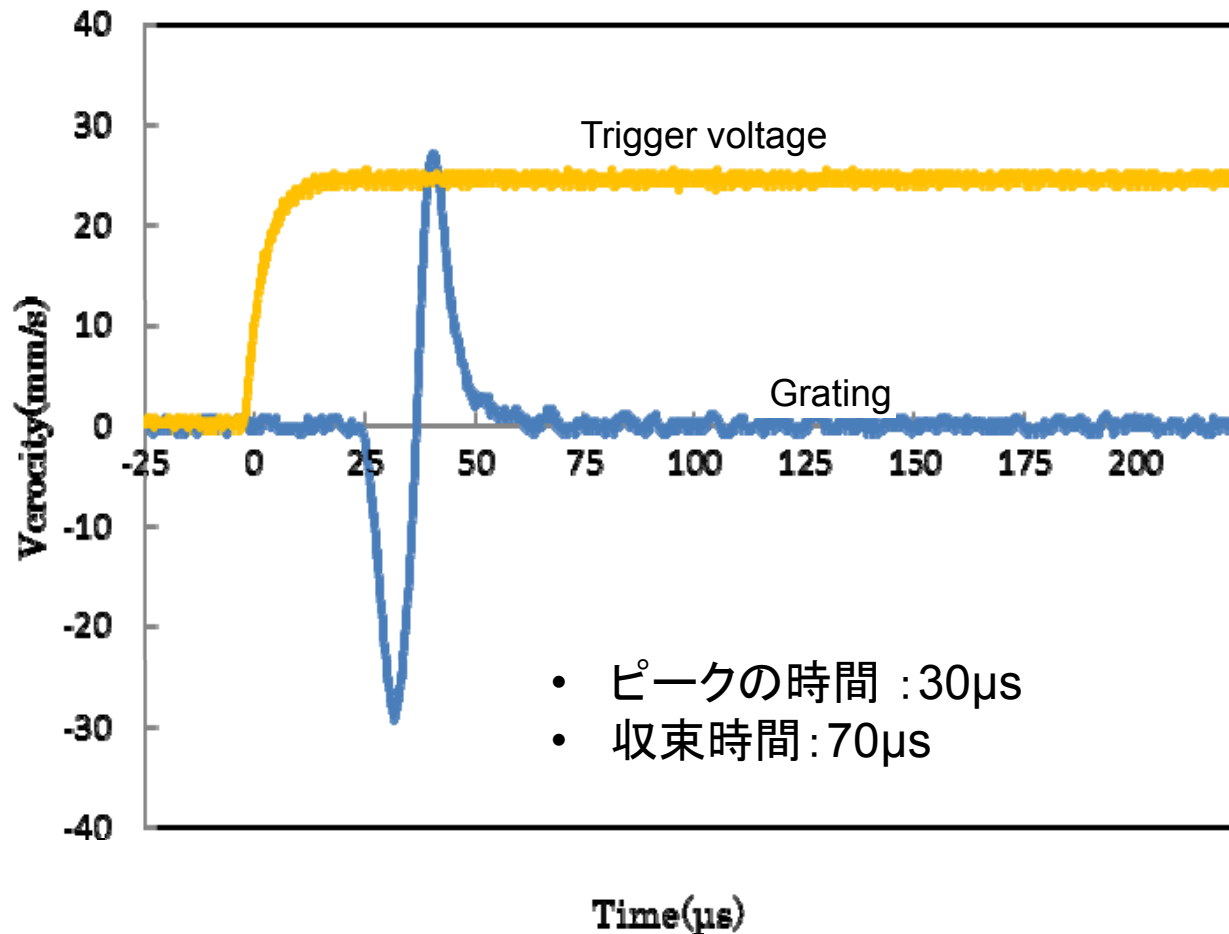
グレーティングを交互の2レベルに動作させた場合の形状測定： おおよそ目標の断面形状へと変化させることができた



矢印部のグレーティングに5Vの電圧を印加(マイコンの不具合により印加できていない部分がある)

応答速度測定

グレーティングの動的特性をレーザードップラー振動計を用いて測定した



LSIによるSiグレーティングの動作



Operation of addressed ribbons
of grating

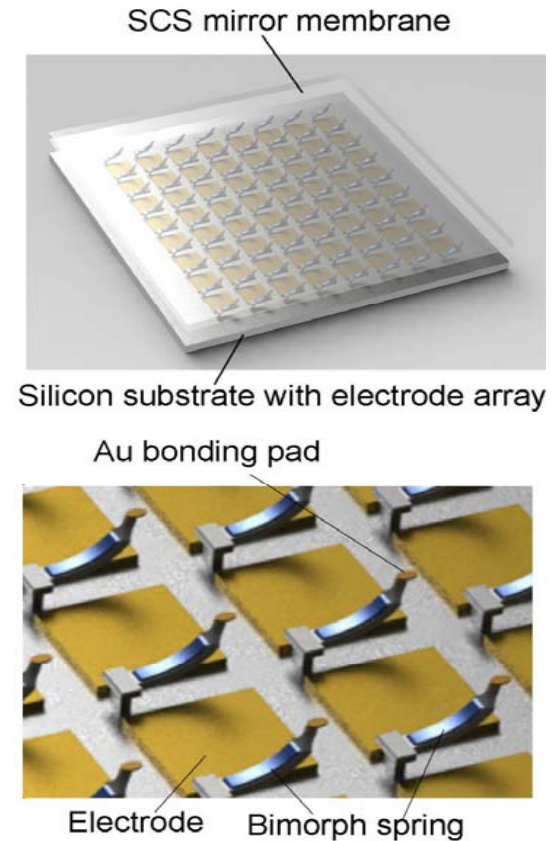
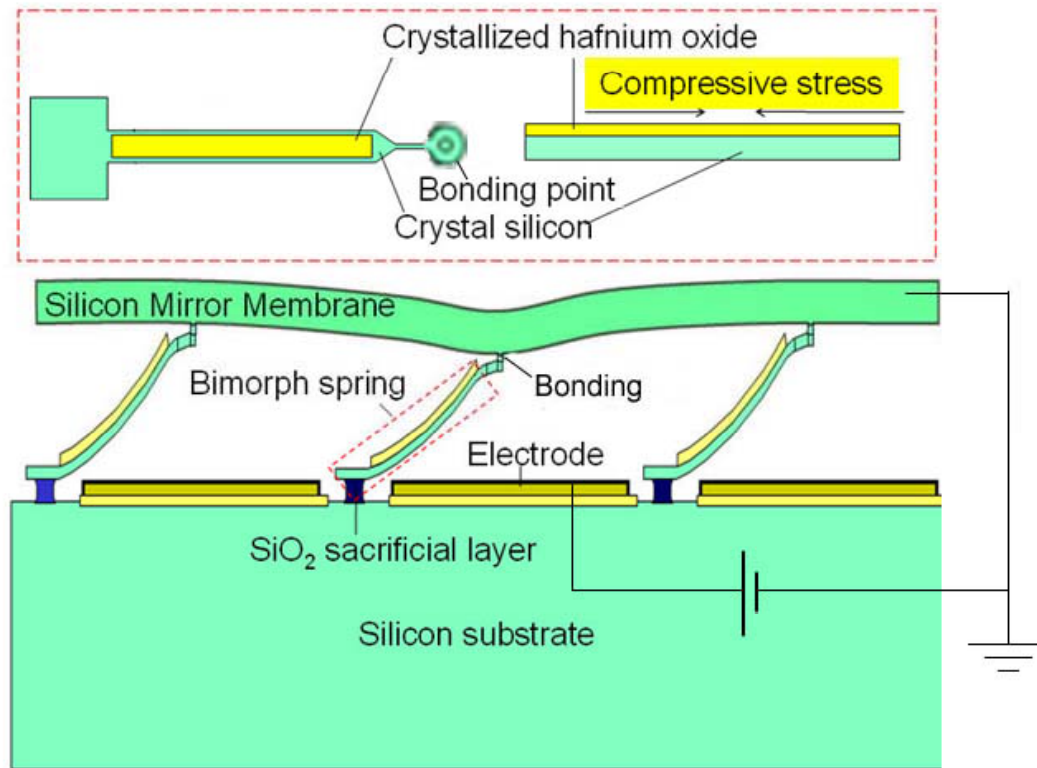


Switching in
diffraction pattern

Experimental demonstration by T. Suzuki and T. Sasaki

MEMS-DM – Main design -

We propose a new structure membrane MEMS-DM by combining **wafer bonding process** and **Si/HfO₂ Bimorph spring array**



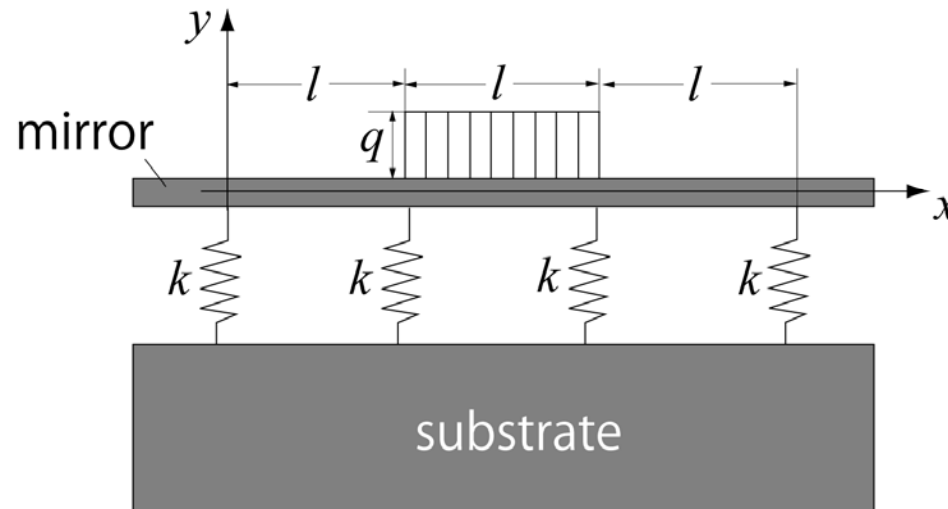
1. HfO₂ crystallization-induced stress is used to introduce **large air gap**.
2. Relatively **soft spring** structure (small spring constant) instead of fixed posts is used to increase the stroke.
3. High optical quality mirror surface is guaranteed by **single-crystal-silicon membrane**.

MEMS-DM – Inter-actuator coupling

It is important to choose the inter-actuator coupling of the mirror actuator at adjacent actuator.

We considered an approximated model for analytic calculation of the influence function. As the inter-actuator coupling is defined as the ratio of the deflection of the pixel adjacent to the actuated pixel to that of the actuated pixel, thus, in the coordinate shown in Fig. 1, the inter-actuator coupling IC is given by

$$IC = \frac{y|_{x=l/2}}{y|_{x=3l/2}} = \frac{552EIkl^3 + 1152E^2I^2 - 6k^2l^6}{1260EIkl^3 + 1152E^2I^2 + 13k^2l^6} \quad (EI \text{ is the flexure rigidity})$$

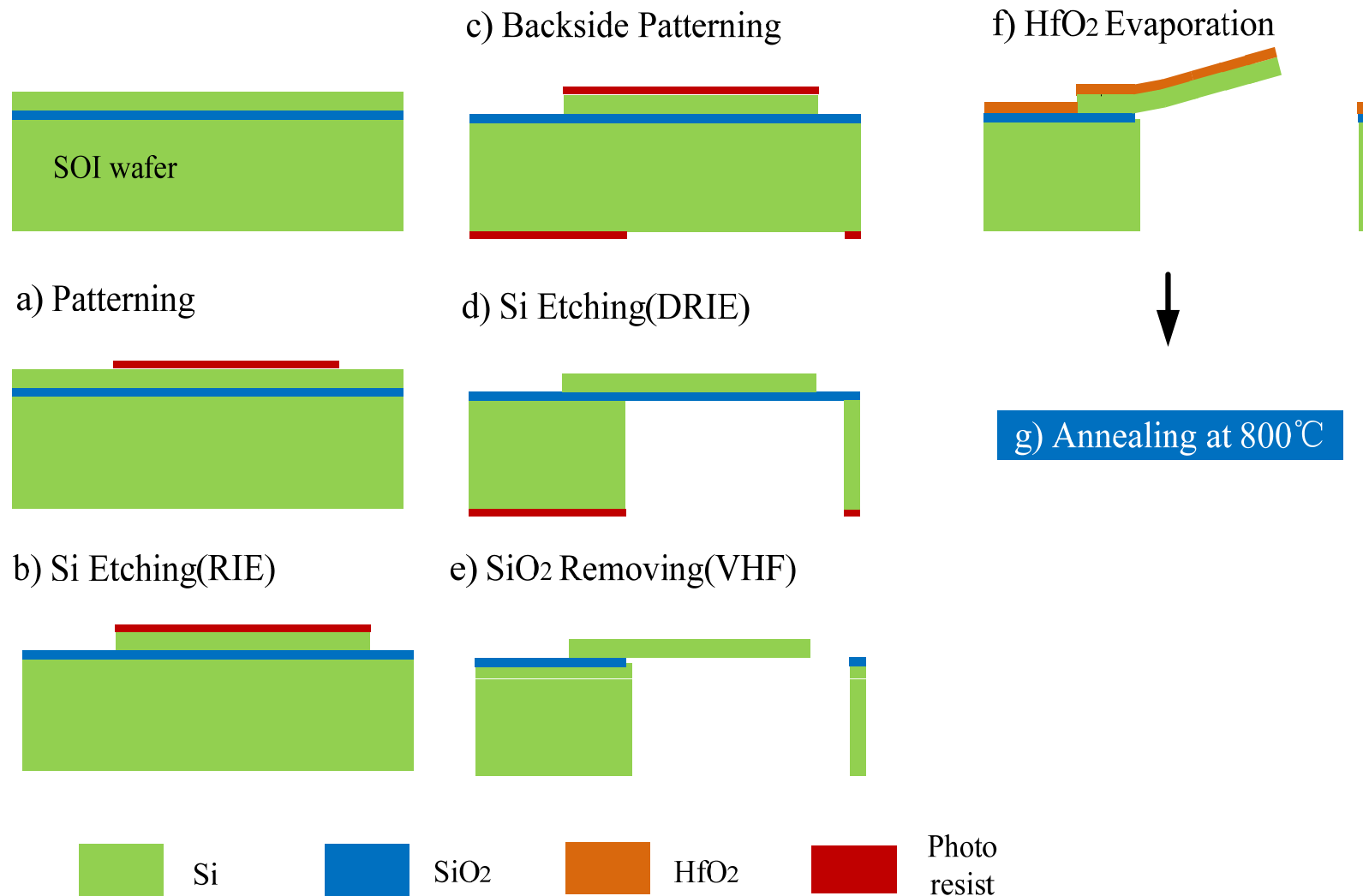


Calculation model of the continuous membrane DM

The calculated inter-actuator coupling for the previous bimorph spring structure: **59%**. The measured inter-actuator coupling is **51%**.

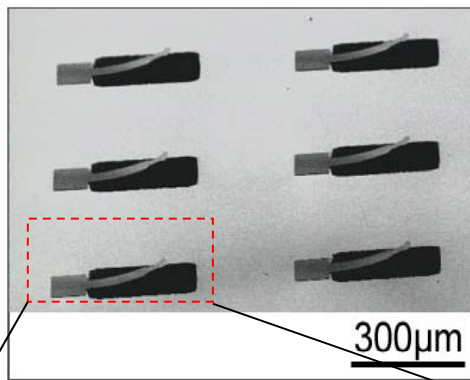
MEMS-DM – Si/HfO₂ Bimorph spring

The out-of-plane deflection needed to be checked to determined the length of the bimorph spring using in DM.

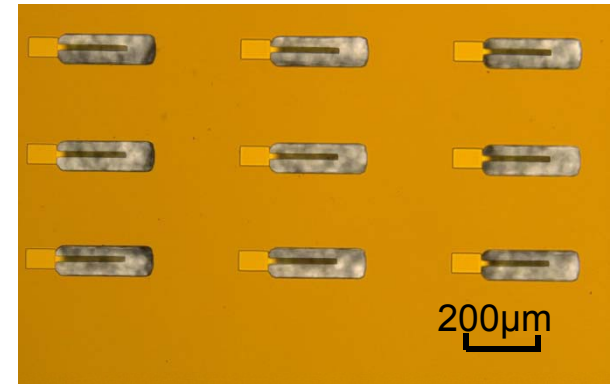
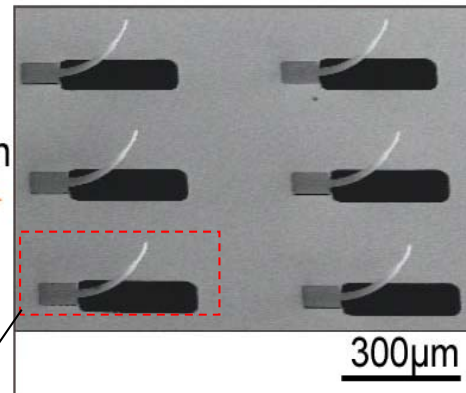


MEMS-DM – Si/HfO₂ Bimorph spring

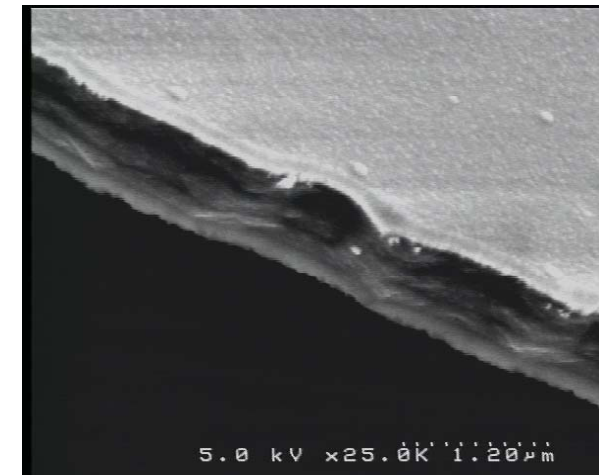
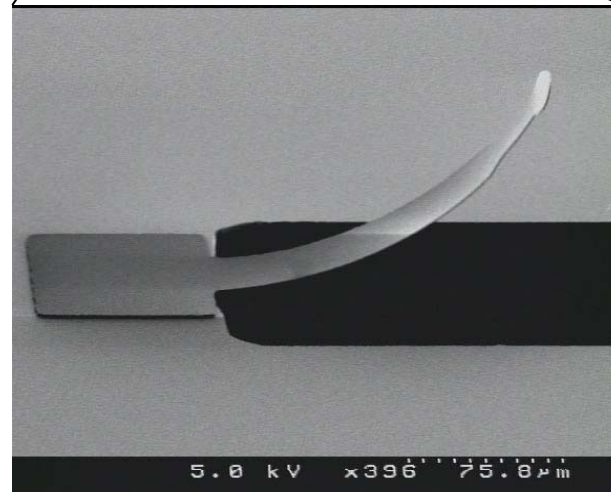
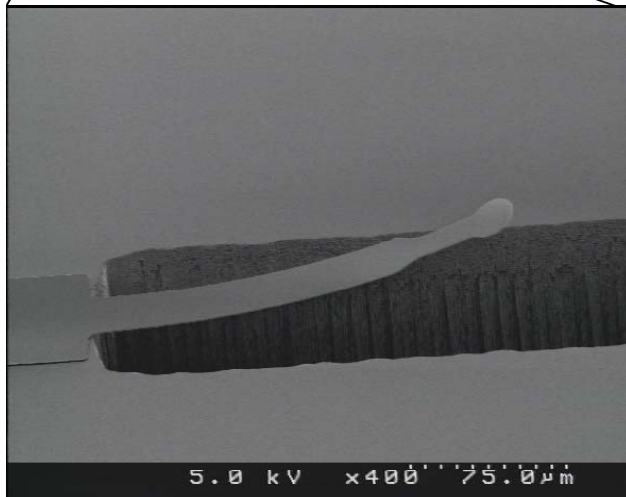
Fabricated bimorph spring



HfO₂
Crystallization
→

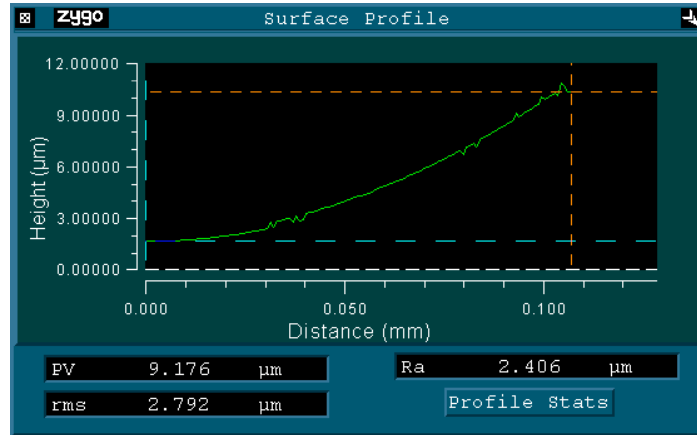


Microscope image



MEMS-DM – Si/HfO₂ Bimorph spring: out-of-plane deflection

Before Crystallization



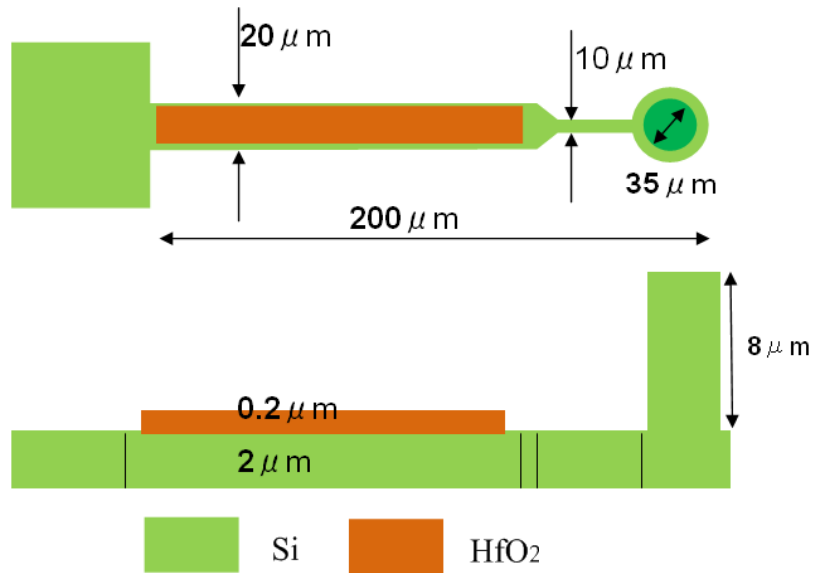
No	Length(µm)	Deflection(µm)	Curvature radius(mm)
1	100	9.29	0.538
2	100	10.06	0.497
3	100	8.65	0.578
4	100	8.78	0.56
		Average Curvature radius(mm)	0.545

After Crystallization

Measured by Confocal laser scanning microscope

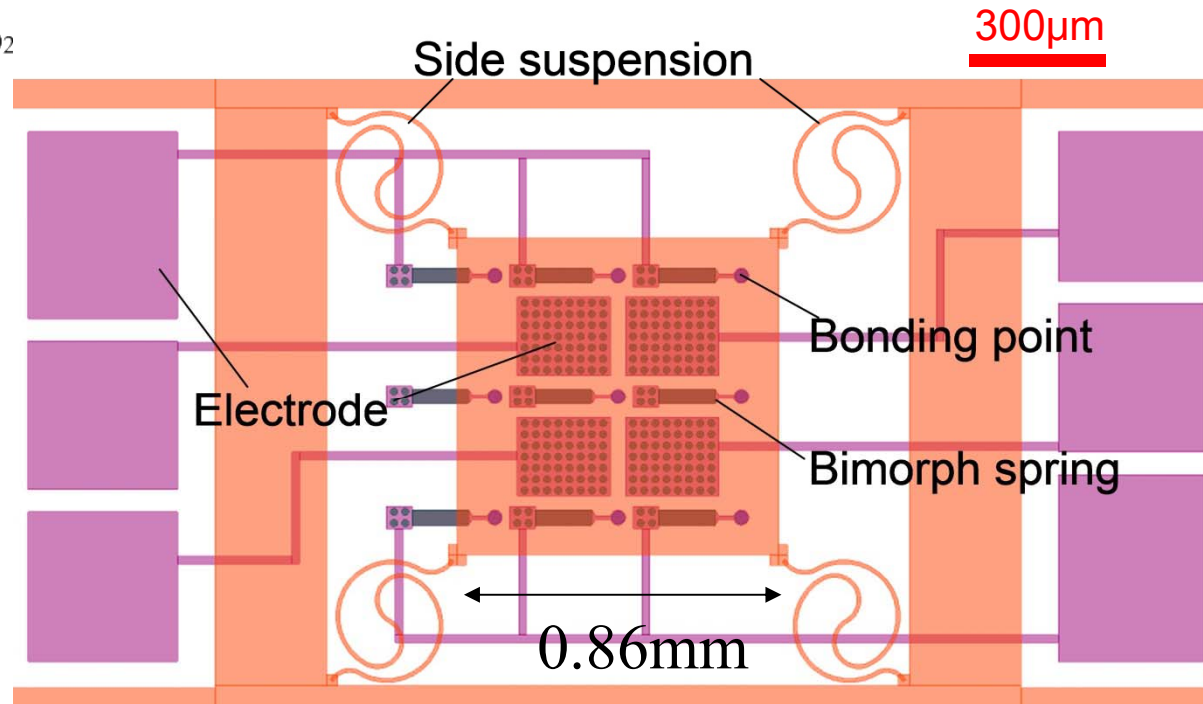
No	Length(µm)	Deflection(µm)	Curvature radius(mm)
1	100	34.44	0.145
2	100	33.13	0.150
3	100	33.70	0.148
4	100	33.59	0.148
5	200	130.46	0.153
6	200	133.80	0.149
7	200	132.64	0.150
8	200	130.54	0.153
		Average Curvature radius(mm)	0.150

MEMS-DM – Design

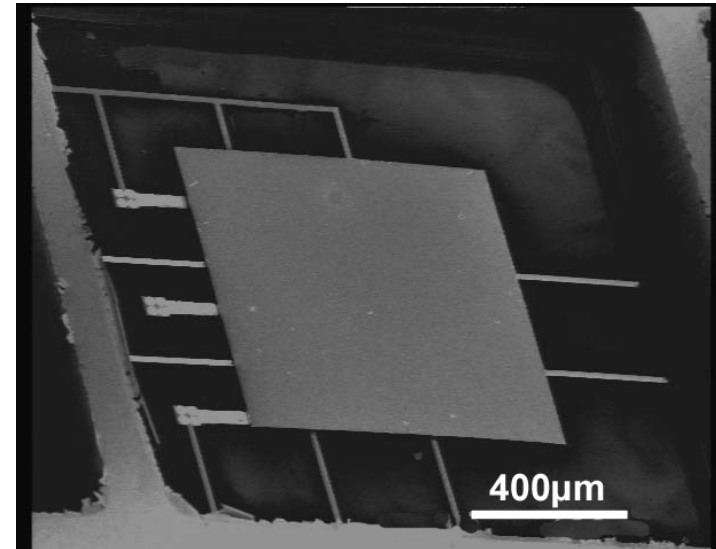
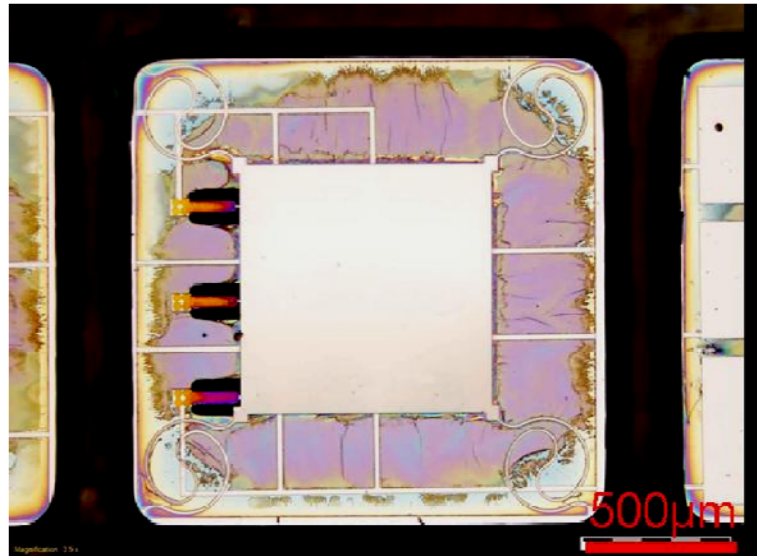


Dimensions of the bimorph spring

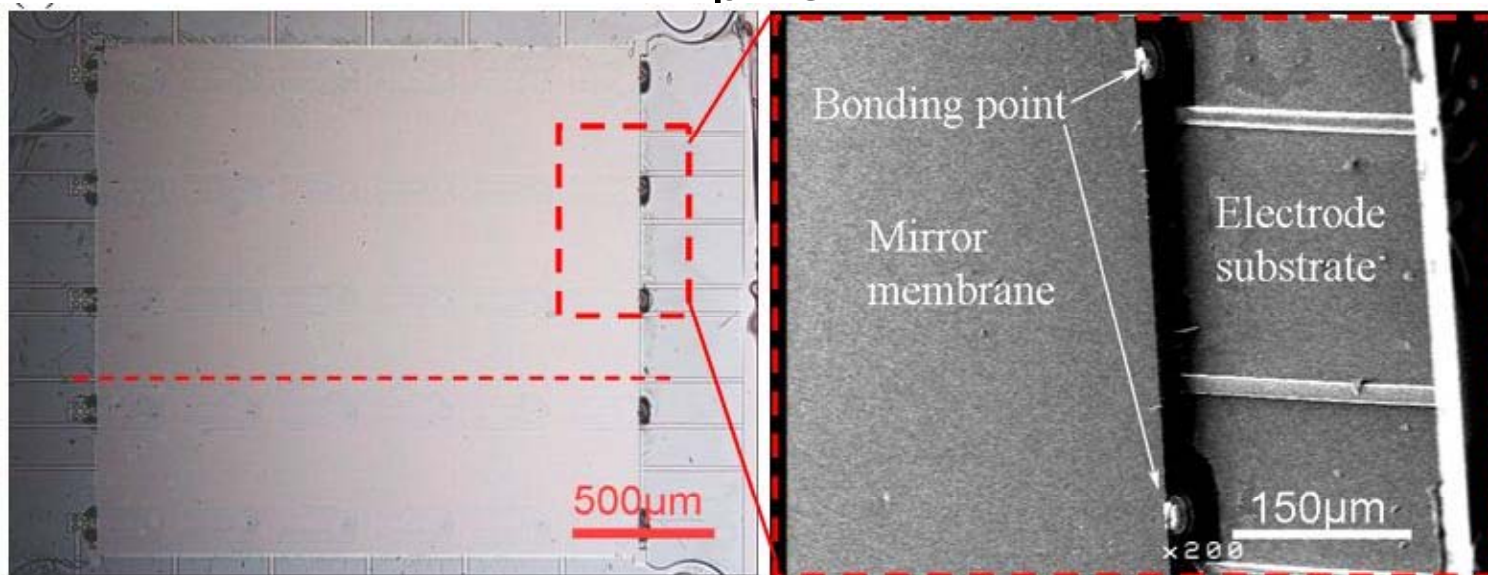
Design of the 2×2 Pixels DM



MEMS-DM – Fabrication results(3)

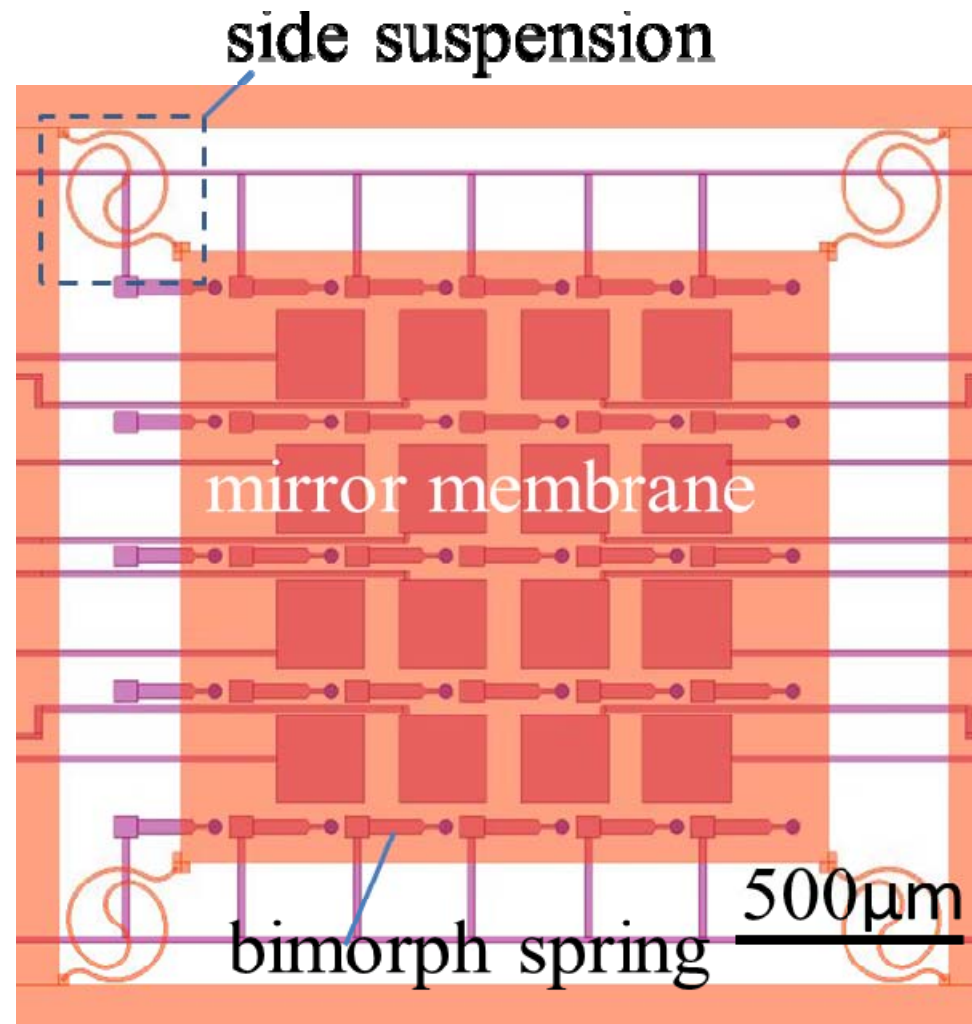


2 × 2 pixel DM



4 × 4 pixel DM

MEMS-DM – Design



Design of the 4 × 4 Pixels DM

MEMS-DM –Process flow

(a) Actuator chip

(1) Silicon DRIE (Form bonding point) (4) Annealing (HfO₂ crystallization)



(2) Silicon DRIE (Form Electrode) (5) Au/Cr deposition and patterning

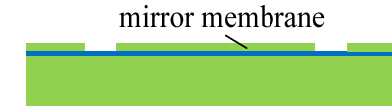


(3) HfO₂ Liftoff



(b) Mirror chip

(1) Silicon DRIE (Form mirror membrane)



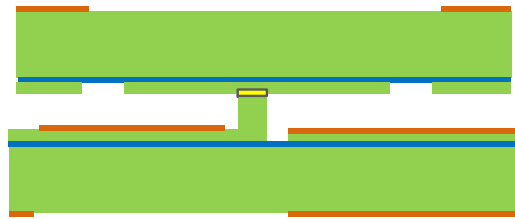
(2) HfO₂ liftoff



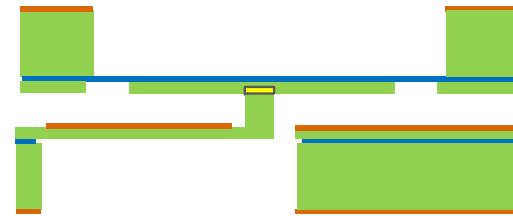
MEMS-DM –Fabrication flow : Bonding and Release

(c) Bonding and release

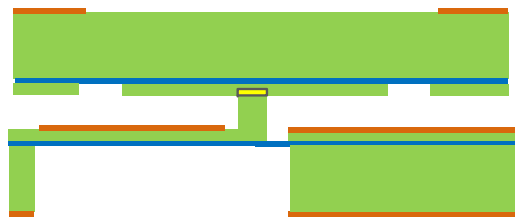
(1) Au-Si eutectic bonding



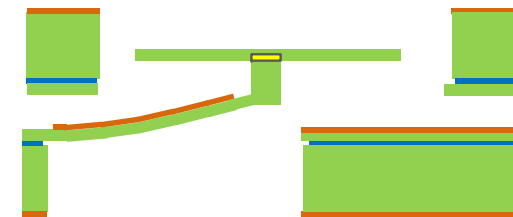
(4) SiO₂ Dry Etching (CHF₃)



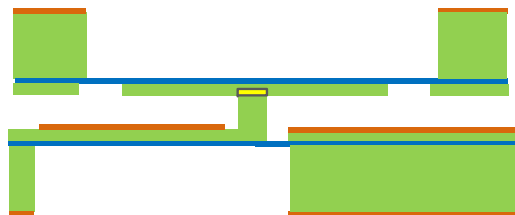
(2) Actuator chip handle layer etching



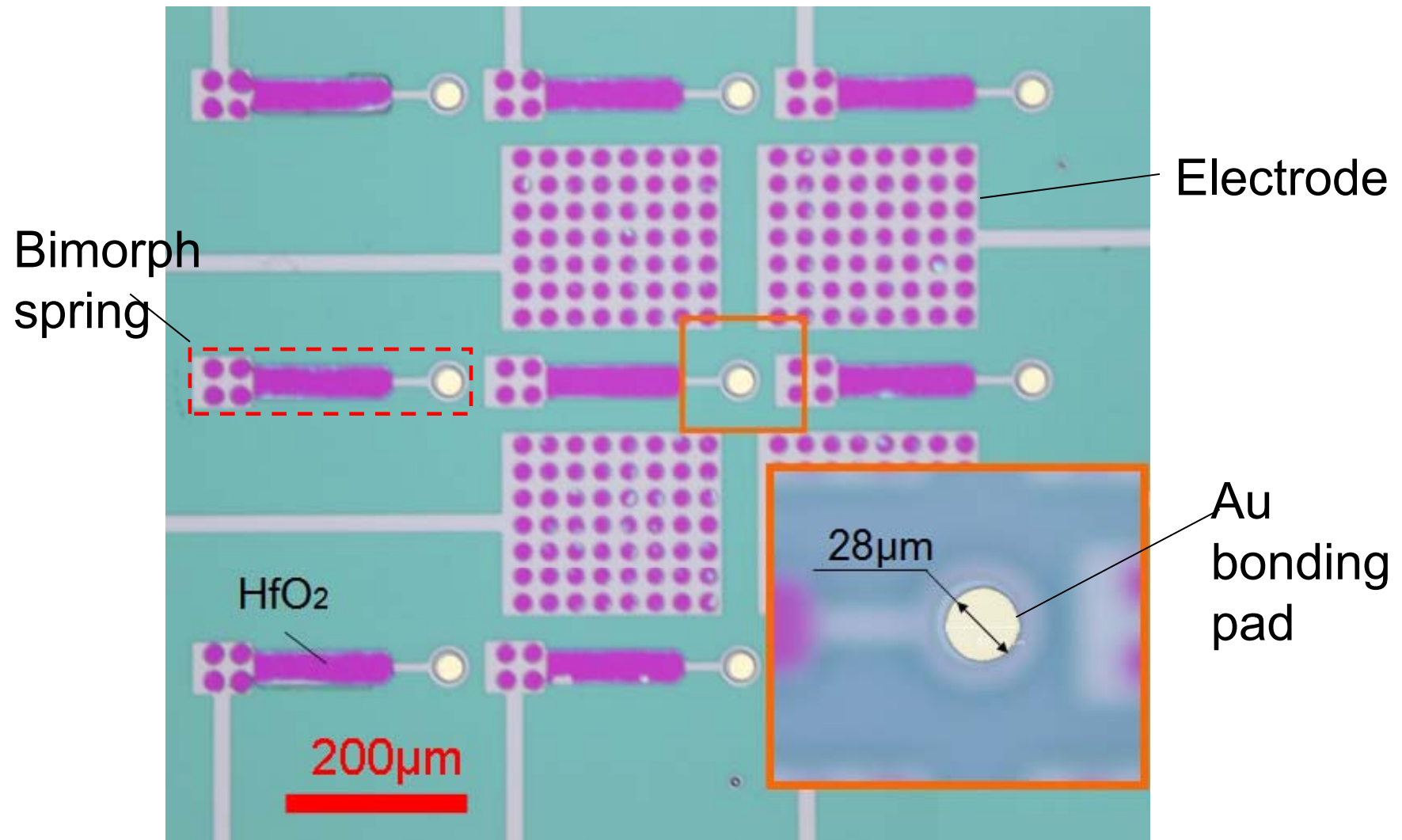
(5) SiO₂ Dry Etching (CHF₃)



(3) Mirror chip handle layer etching



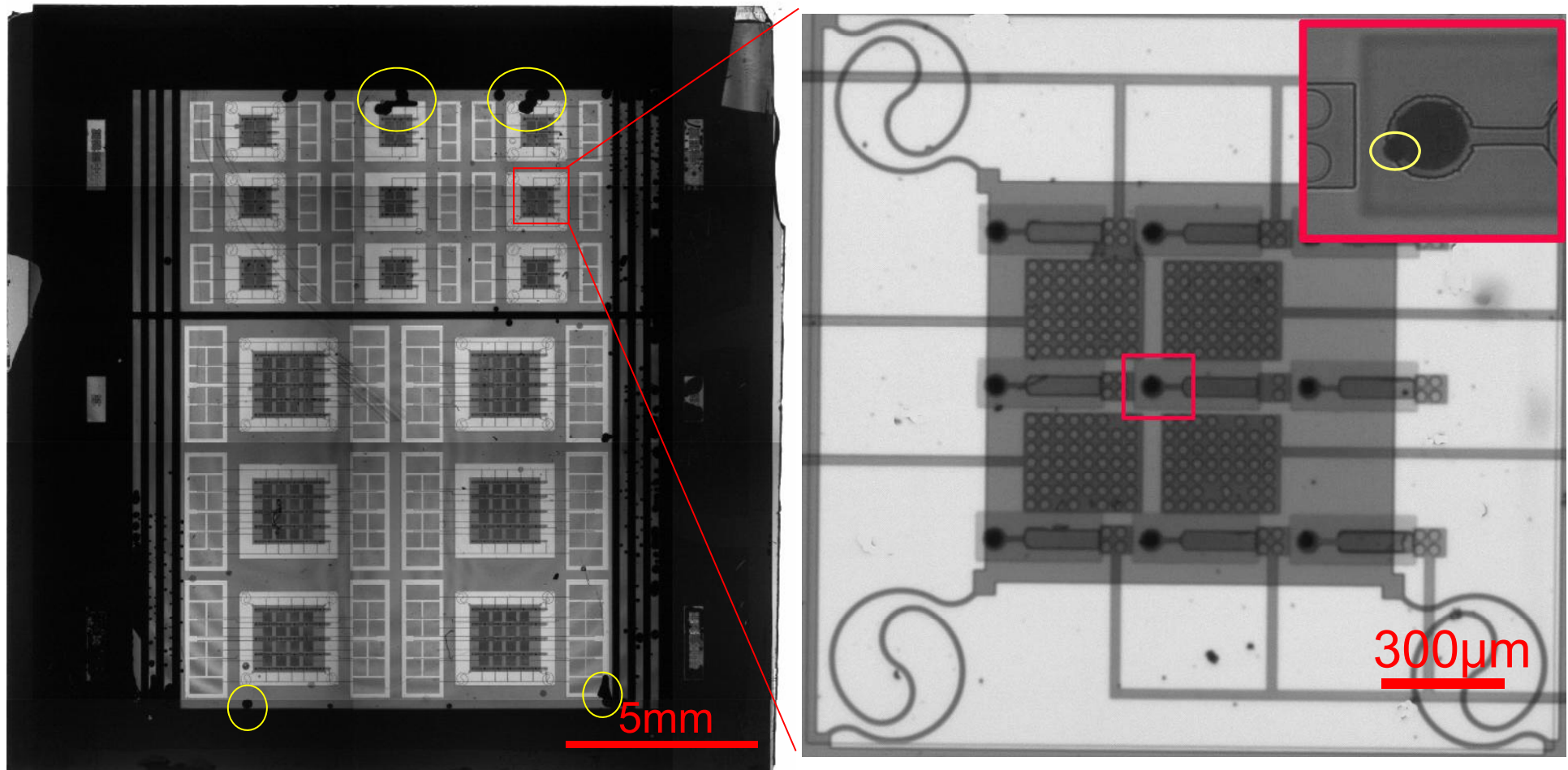
MEMS-DM – Fabrication results(1)



Fabricated actuator chip

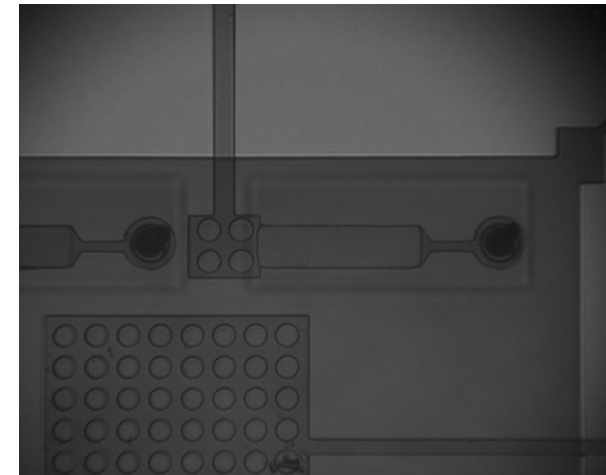
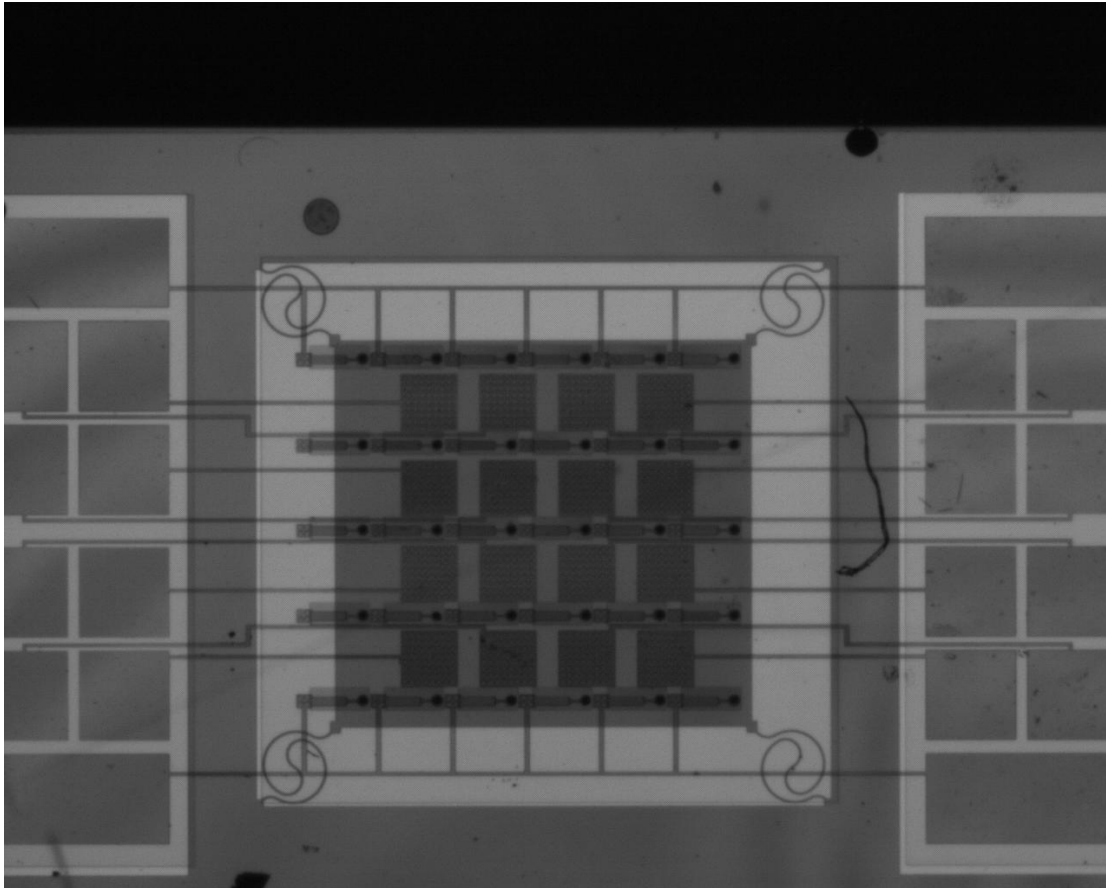
MEMS-DM – Fabrication results(2)

Au-Si eutectic bonding: 400°C, 1.67MPa, 30min, 4.5×10^{-2} Pa



The irregular bonding edge(in yellow line) suggests that **Au was melted during bonding.**

MEMS-DM – Fabrication results(2)

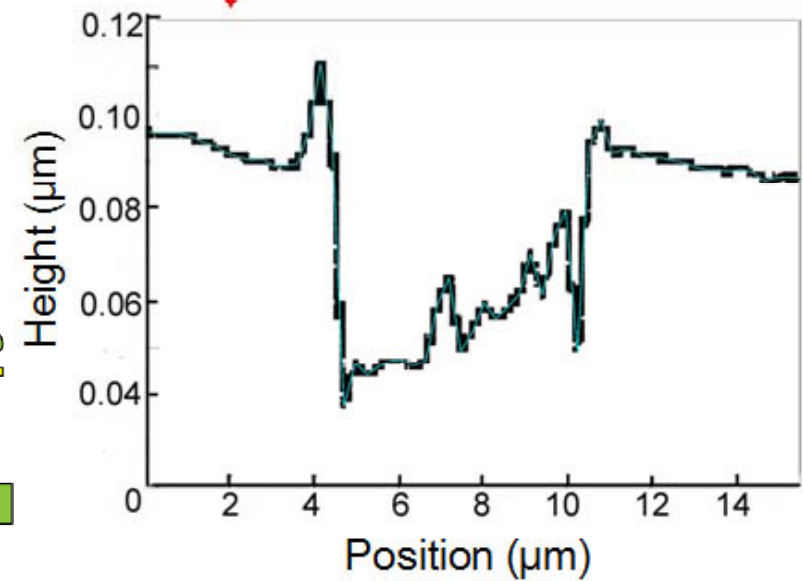
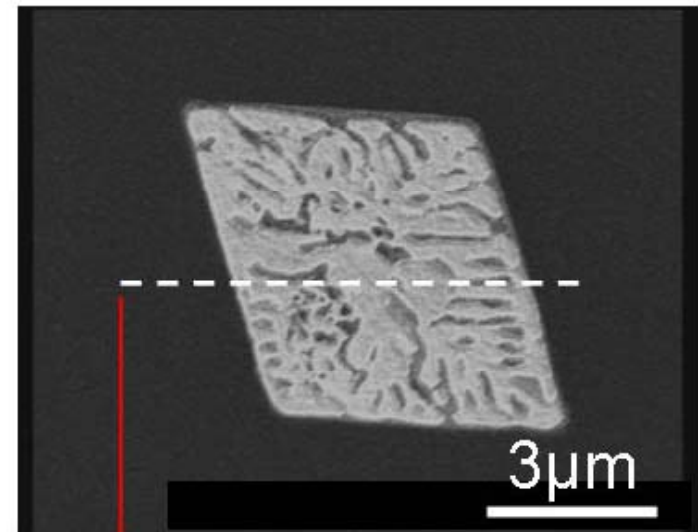


4×4 pixel mirror

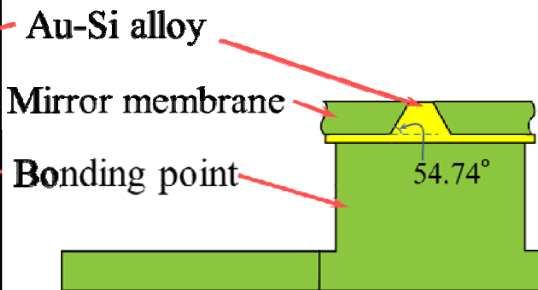
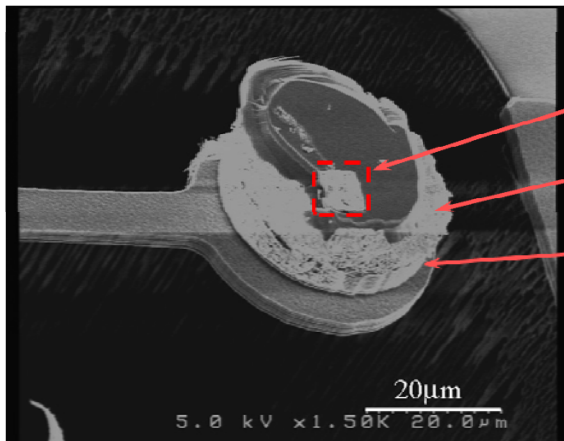
MEMS-DM – Fabrication results(4) –



The optical micrograph of the mirror membrane with the square Au-Si reaction area

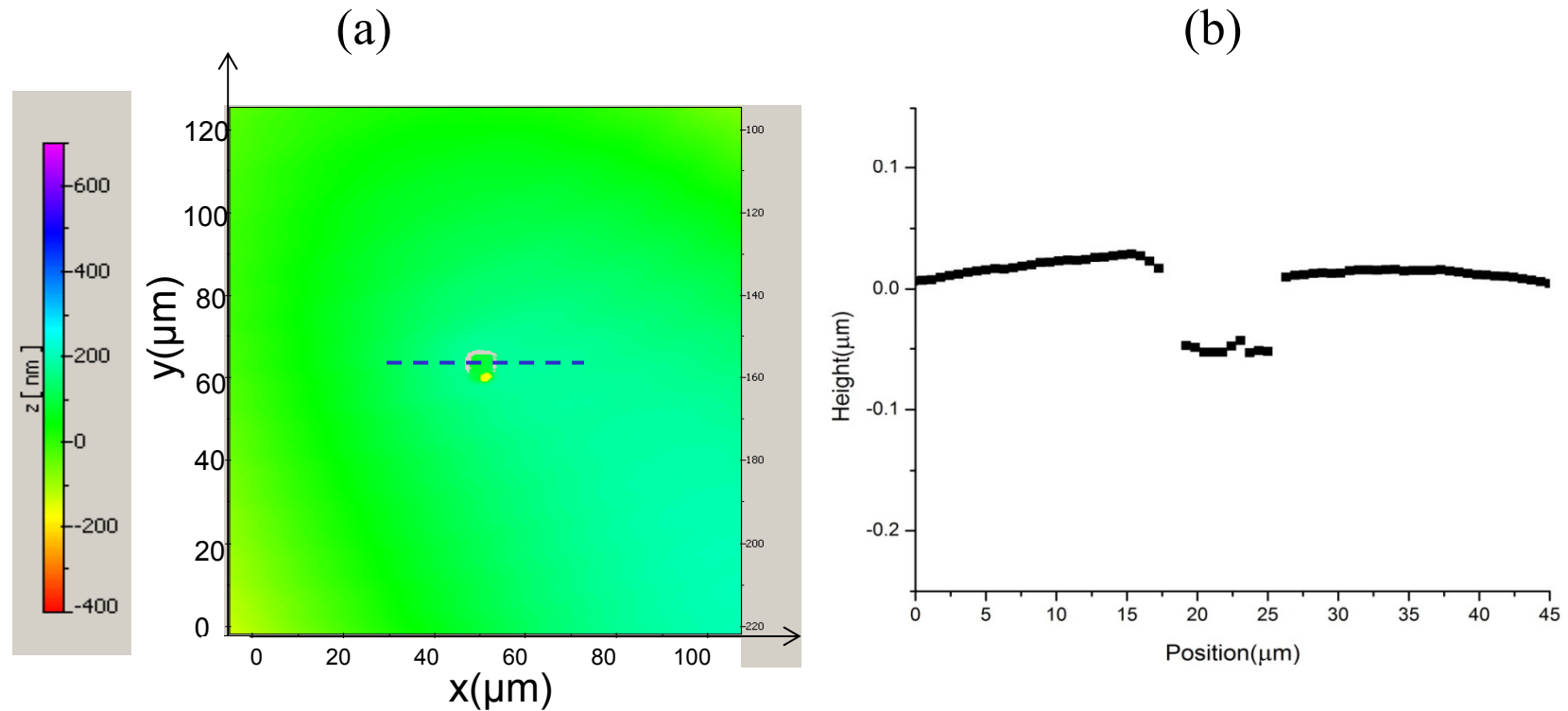


The SEM image and the surface profile of the bonding reaction area



SEM image of the bonding point after peeling the mirror membrane

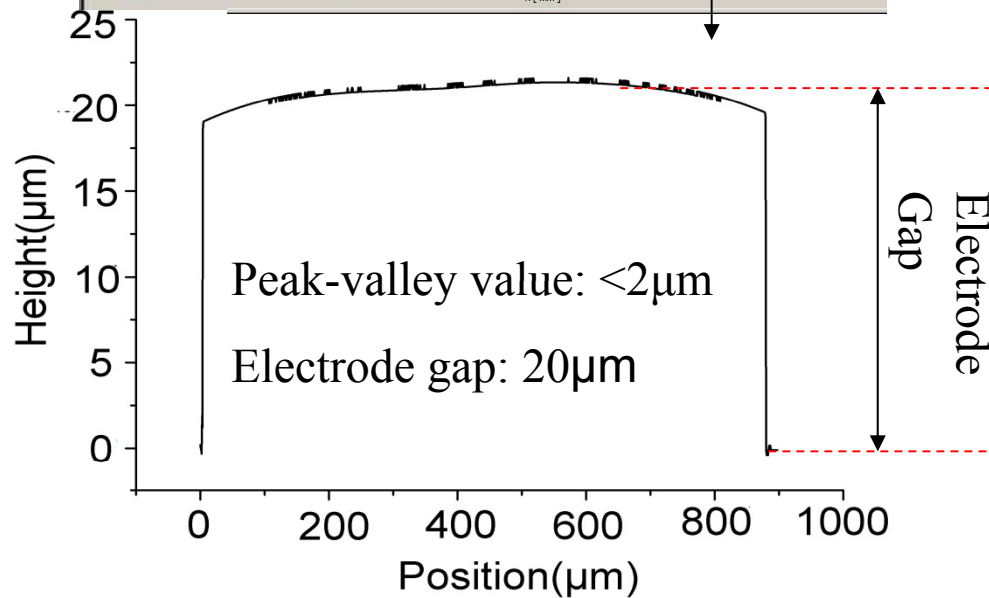
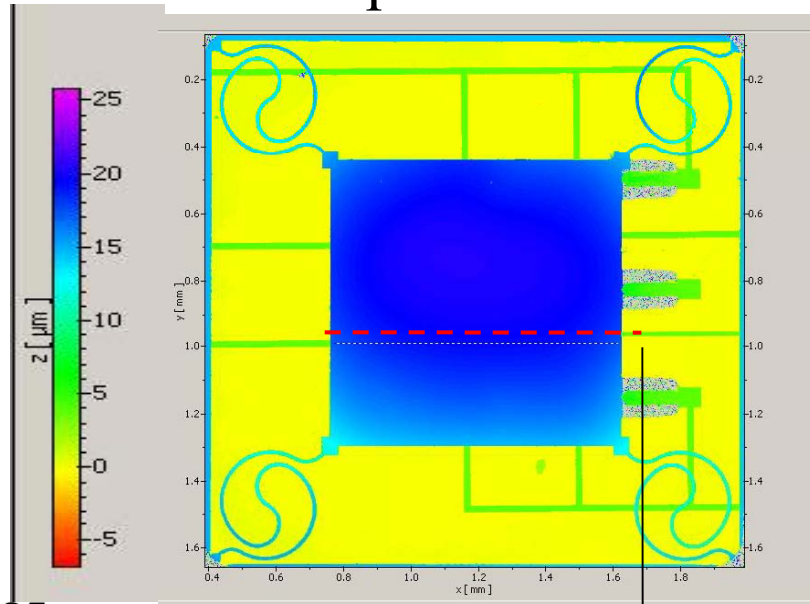
MEMS-DM – Fabrication results(5) –



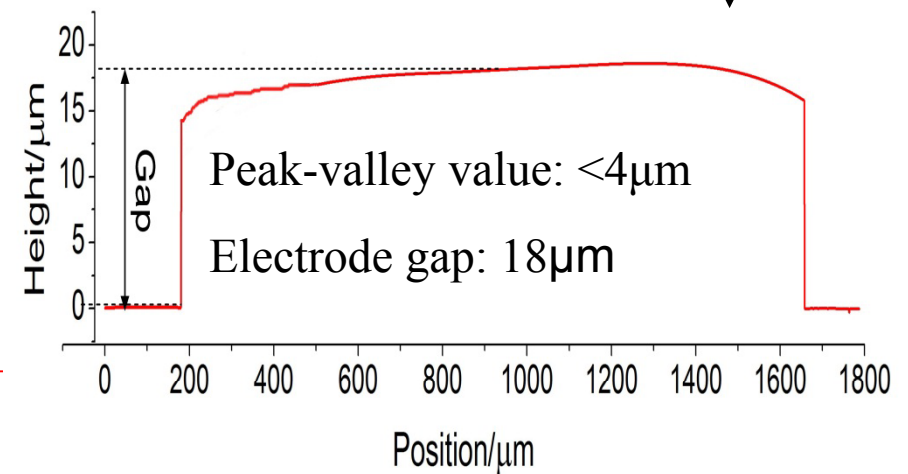
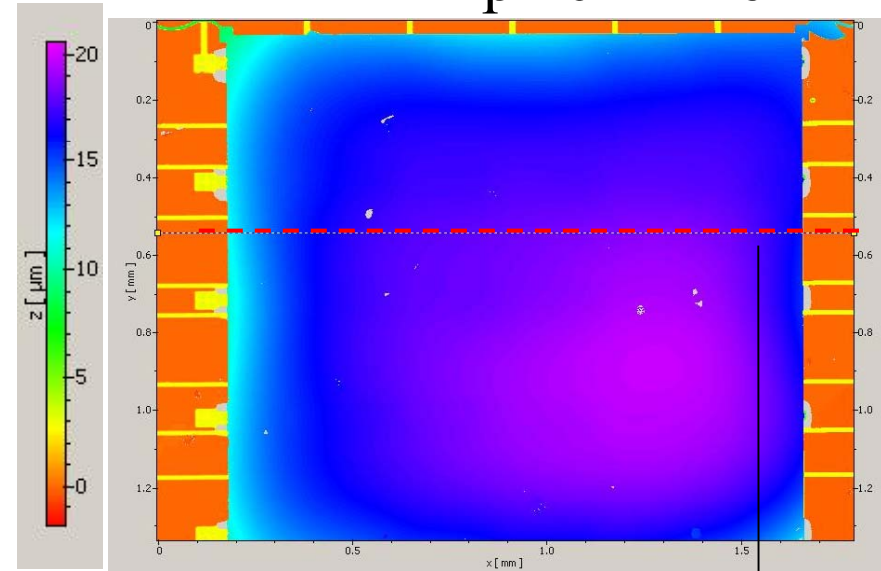
The surface around the bonding point is flat and smooth except the $\sim 5\mu\text{m}$ reaction area. By removing the area of reaction point, the **fill factor** of the fabricated mirror is calculated to be **99.9%**.

MEMS-DM – Device initial deflection

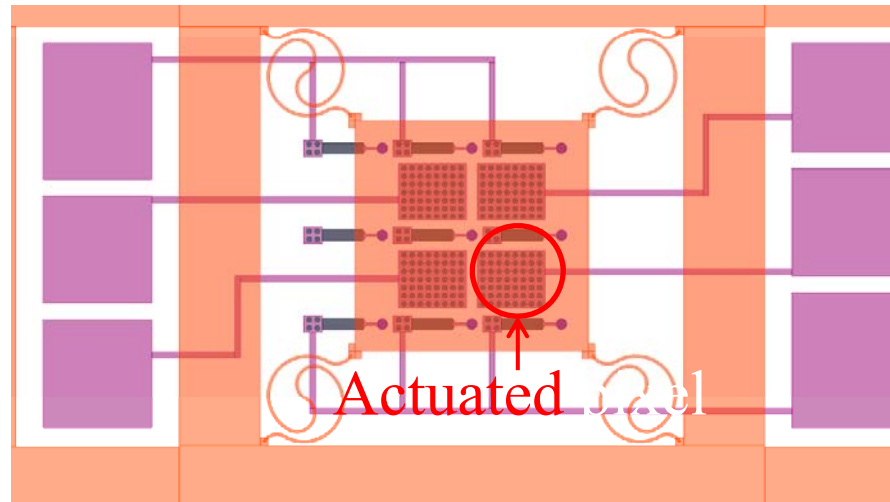
2 × 2 pixel mirror



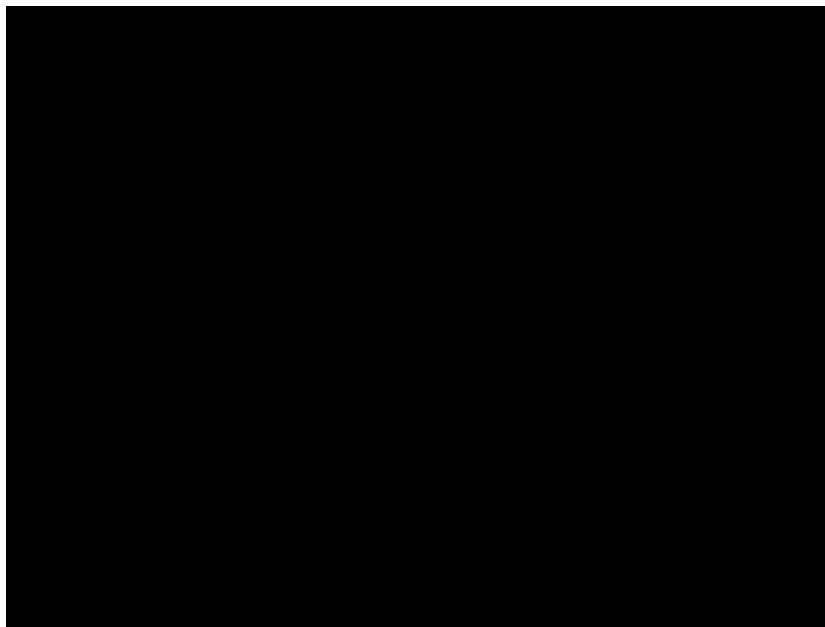
4 × 4 pixel mirror



MEMS-DM – device driving experiment(2×2 pixel mirror)

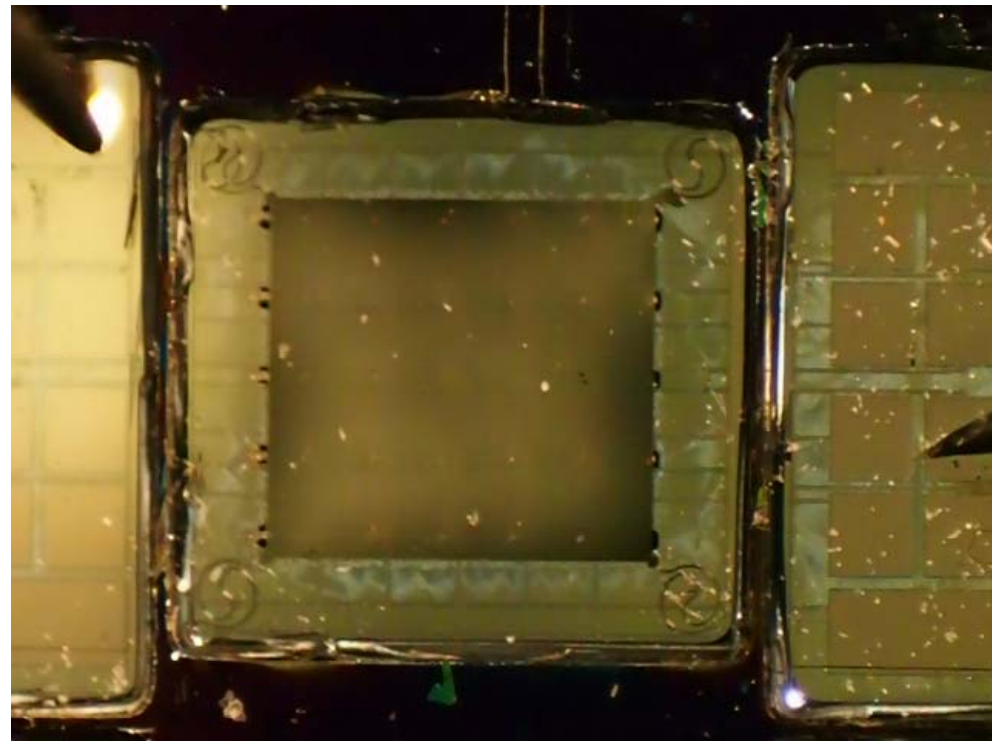
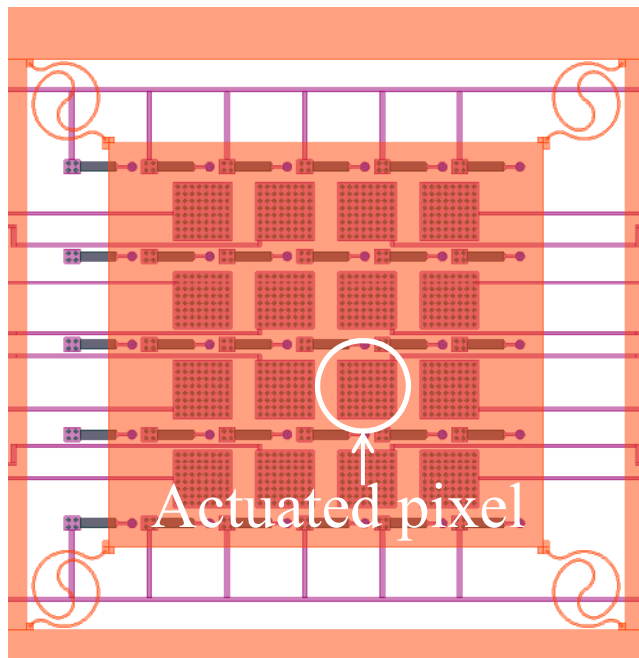


Sine wave driving test: Frequency: 1Hz→20Hz→1Hz A pull-in voltage(~120V) is applied.



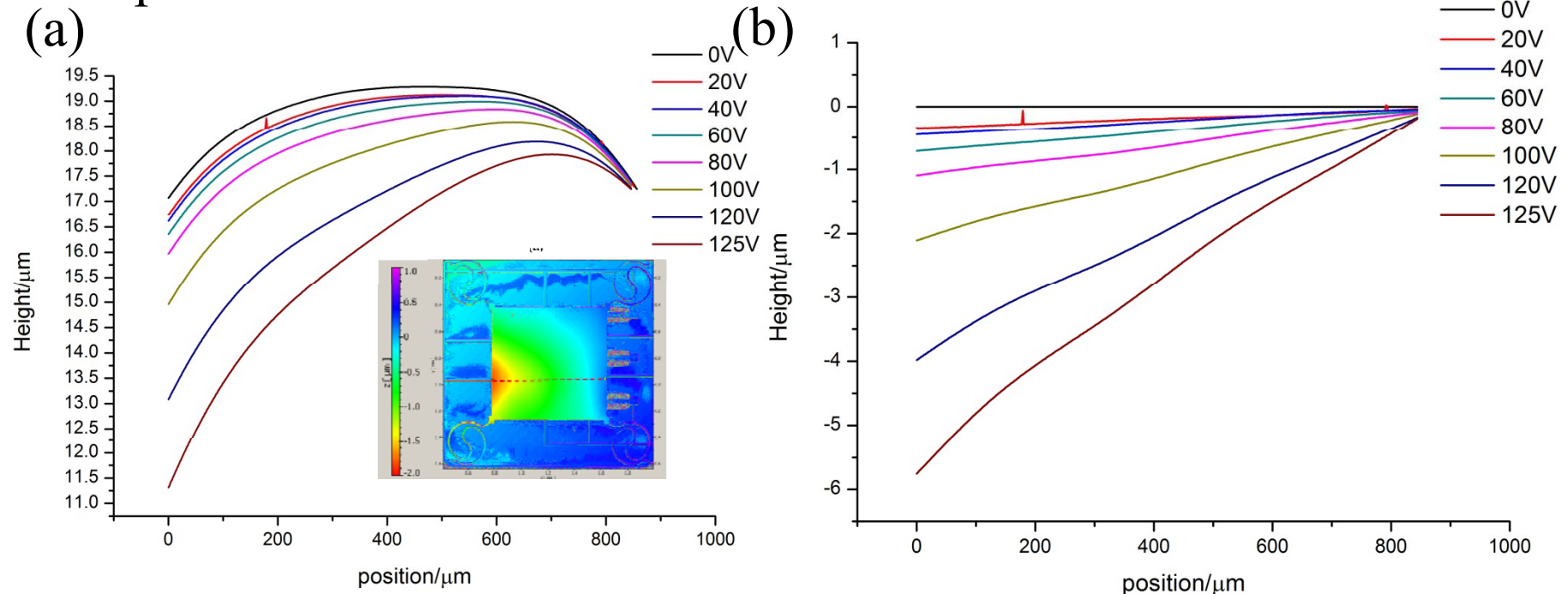
MEMS-DM – device driving experiment(4 × 4 pixel mirror)

Sine wave driving test: Frequency: 1Hz→20Hz→1Hz 80V(peak-peak)



MEMS-DM – static deflection measurement

2 × 2 pixels mirror

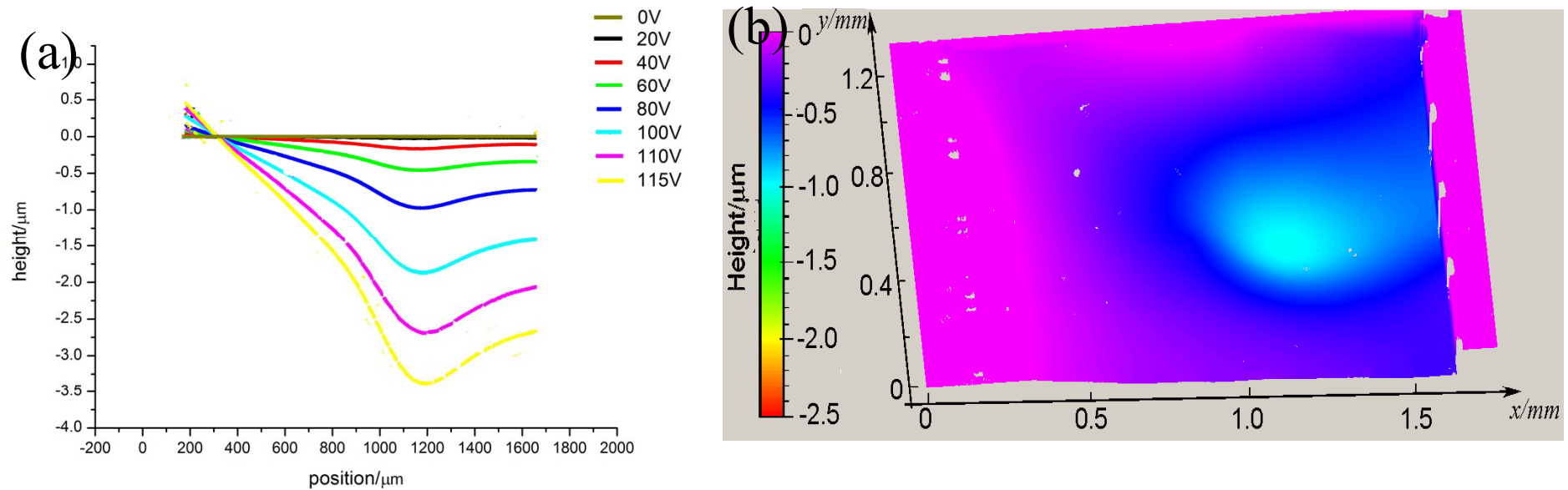


(a) deflection profile of the 2 × 2 pixels DM with a single pixel actuated at different voltage

(b) deflection profile of the 2 × 2 pixels DM with a single pixel actuated at different voltage with zero-voltage profile subtracted;

MEMS-DM – static deflection measurement

4 × 4 pixel mirror

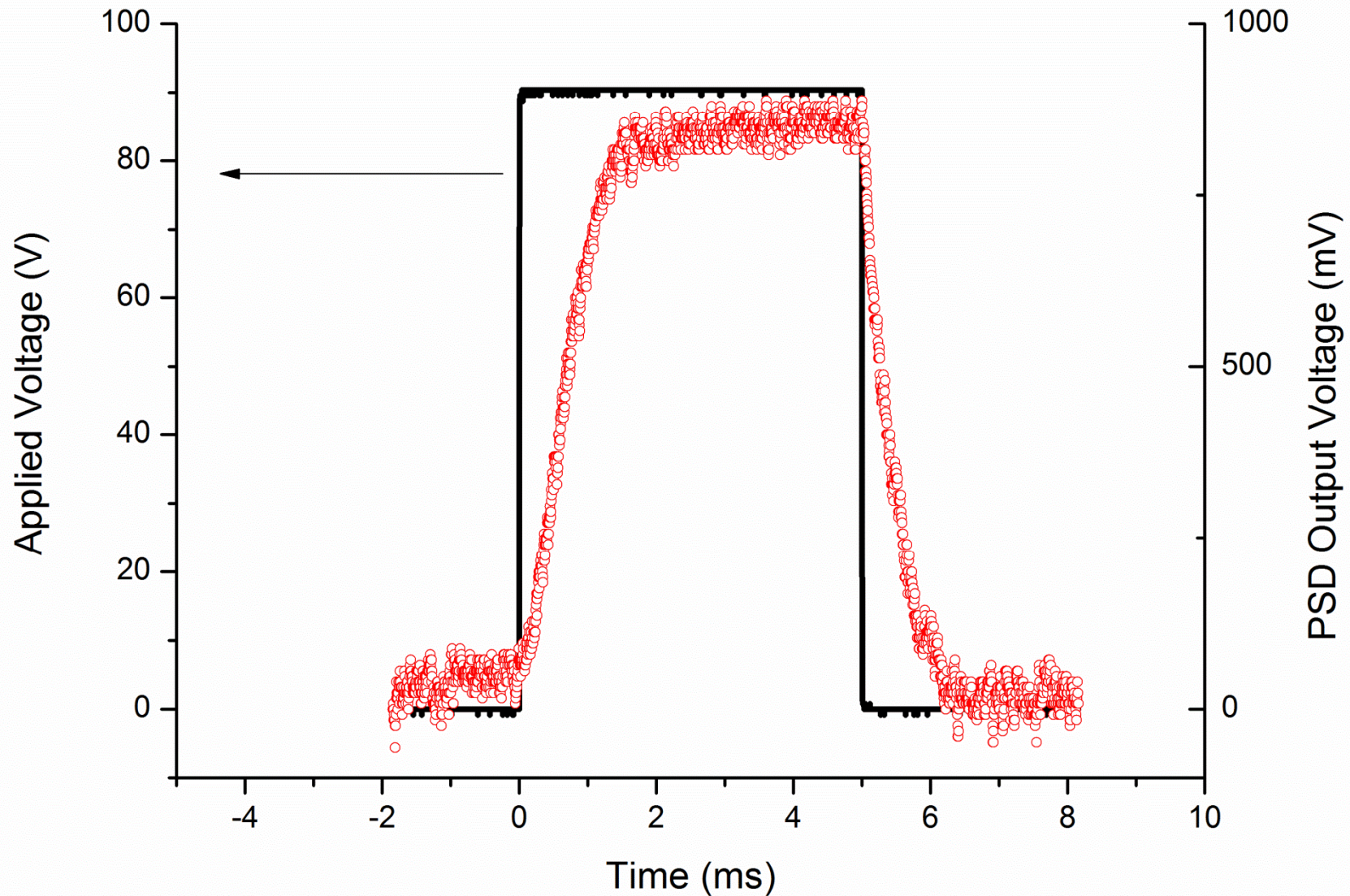


(a) deflection profile of the 4 × 4 pixel DM with a single pixel actuated at different voltage with zero-voltage profile subtracted;

(b) color-coded surface profile of the 4 × 4 pixel DM at 80V with zero-voltage profile subtracted.

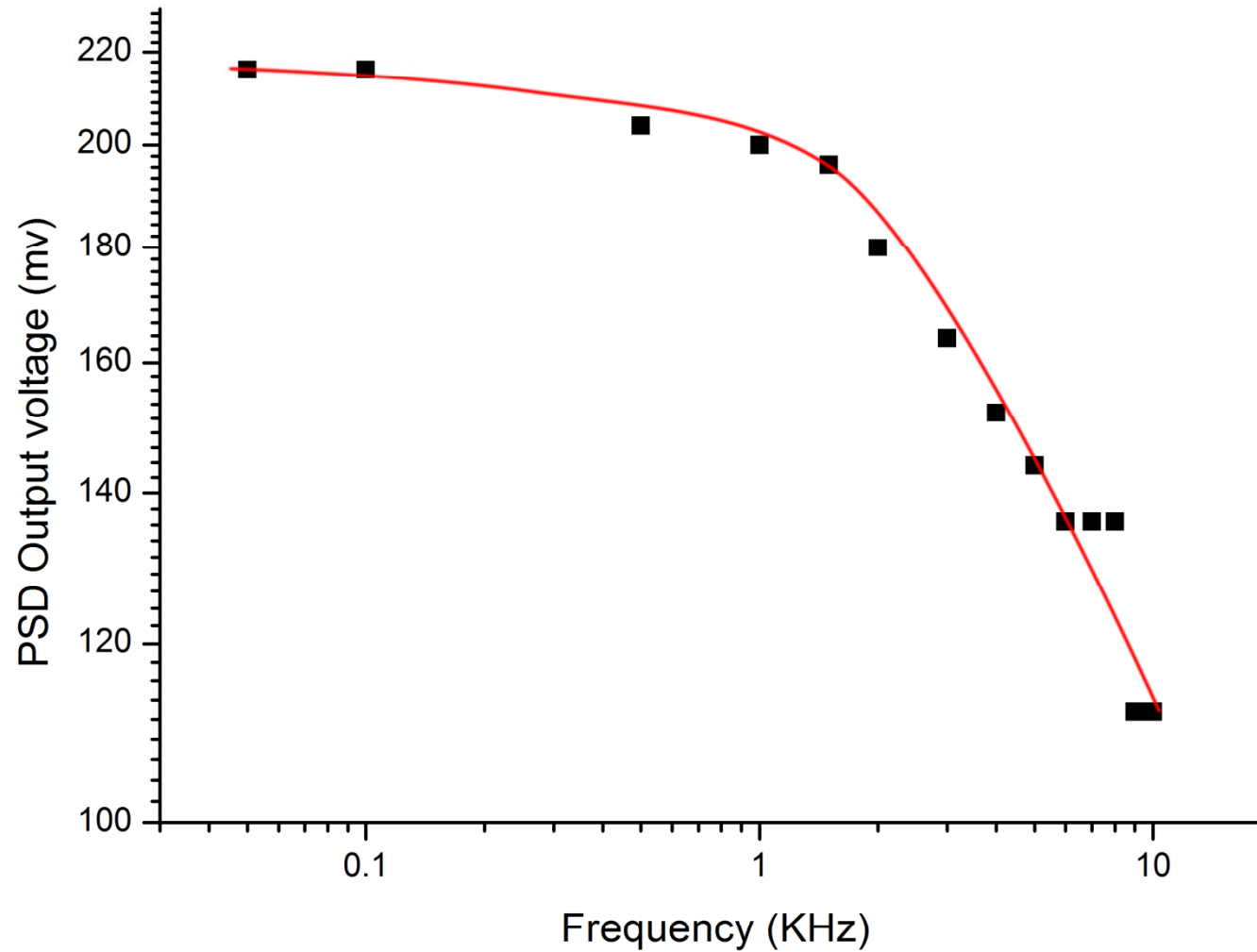
MEMS-DM – response time measurement

Rise time: 1.3ms Fall time: 0.9ms



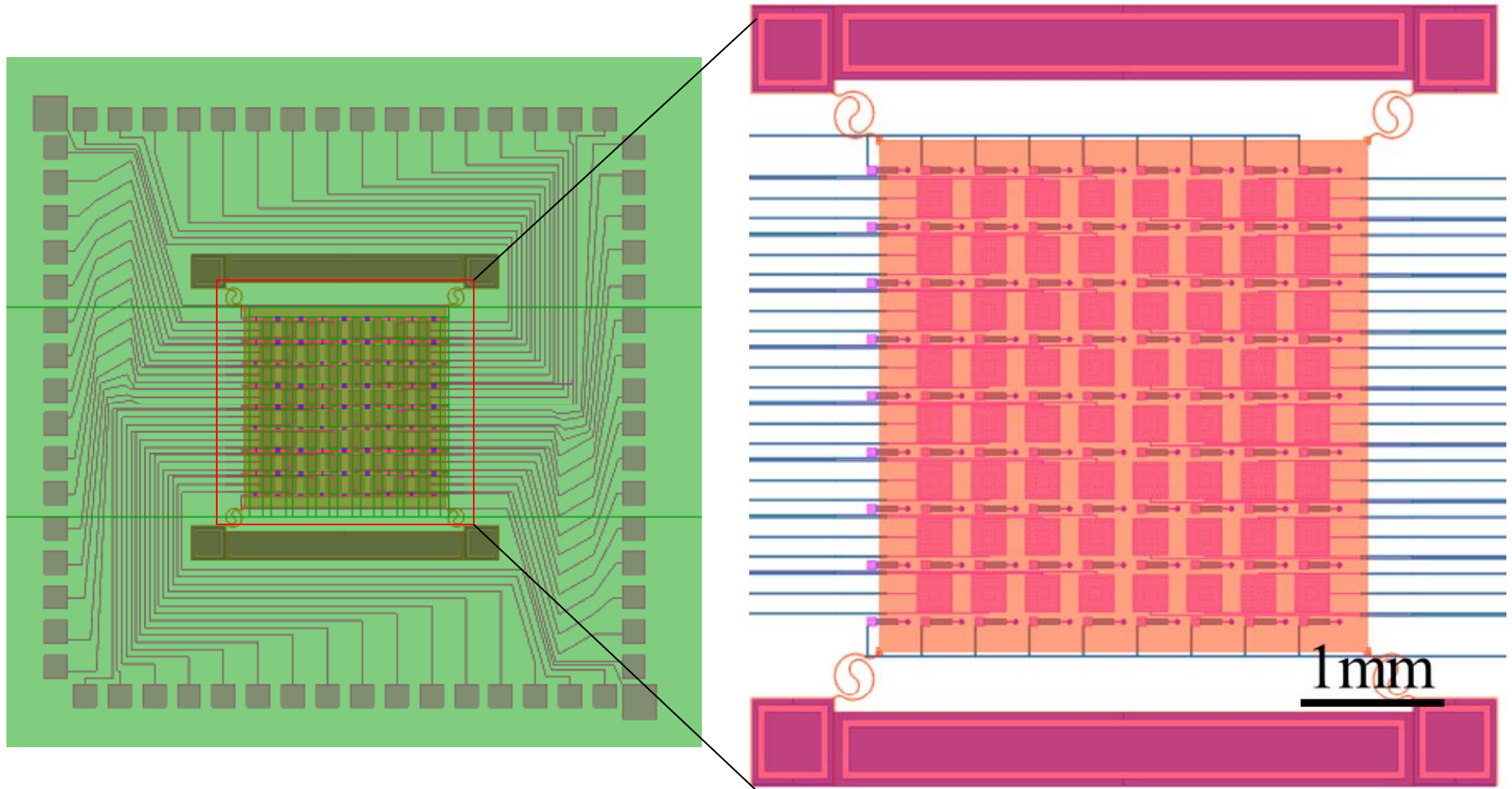
Dynamic response of the fabricated 4 × 4 DM with a single pixel actuated

MEMS-DM – frequency response



frequency response of the fabricated 4×4 pixel DM

8 × 8 pixel MEMS-DM -Design



Design of the 8 × 8 Pixels DM

8 × 8 pixel MEMS-DM –Process flow

(a) Actuator chip

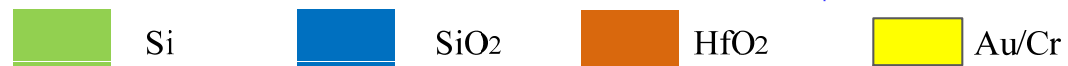
(1) Silicon DRIE (Form bonding point) (4) Annealing (HfO₂ crystallization)



(2) Silicon DRIE (Form Electrode) (5) Au/Cr deposition and patterning

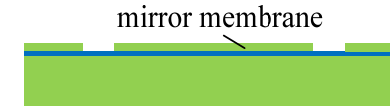


(3) HfO₂ Liftoff (6) Al liftoff



(b) Mirror chip

(1) Silicon DRIE (Form mirror membrane)



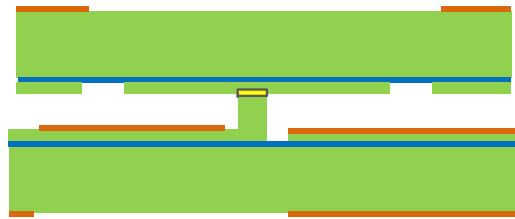
(2) HfO₂ liftoff



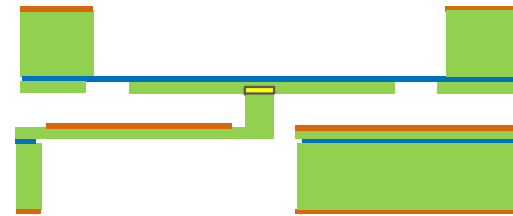
8 × 8 pixel MEMS-DM –Process flow

(c) Bonding and release

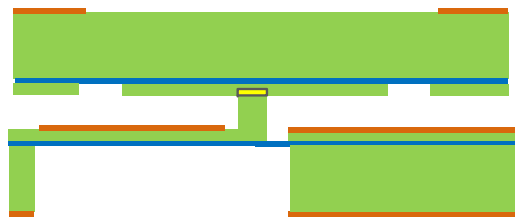
(1) Au-Si eutectic bonding



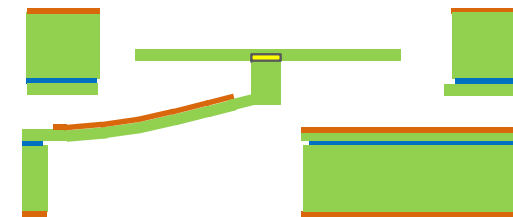
(4) SiO₂ Dry Etching (CHF₃)



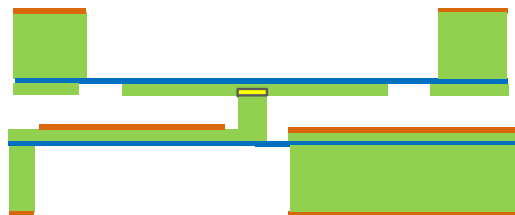
(2) Actuator chip handle layer etching



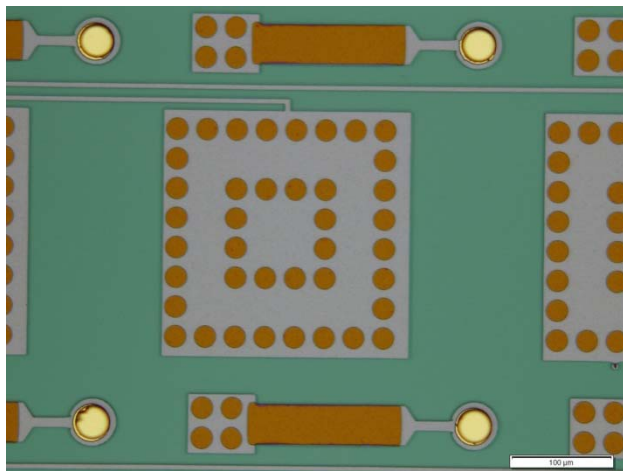
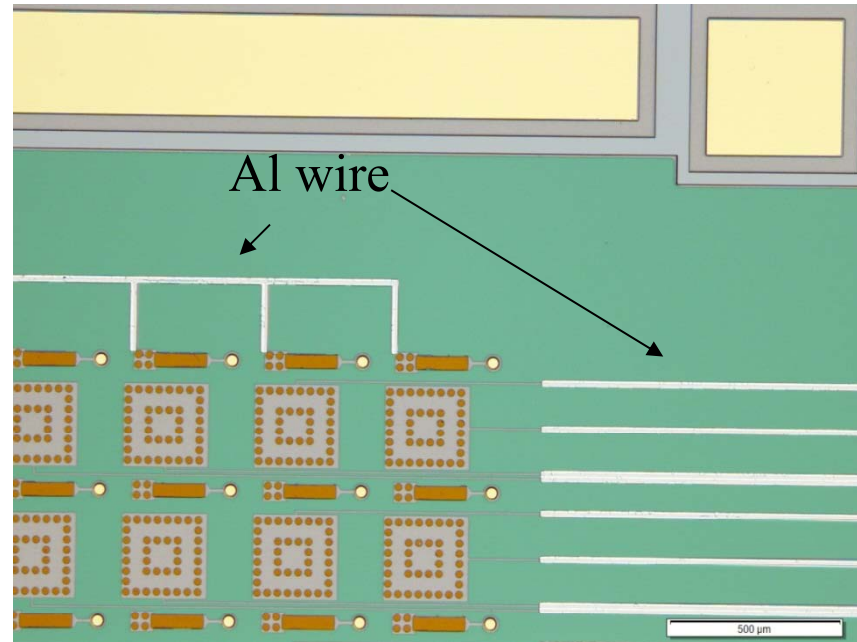
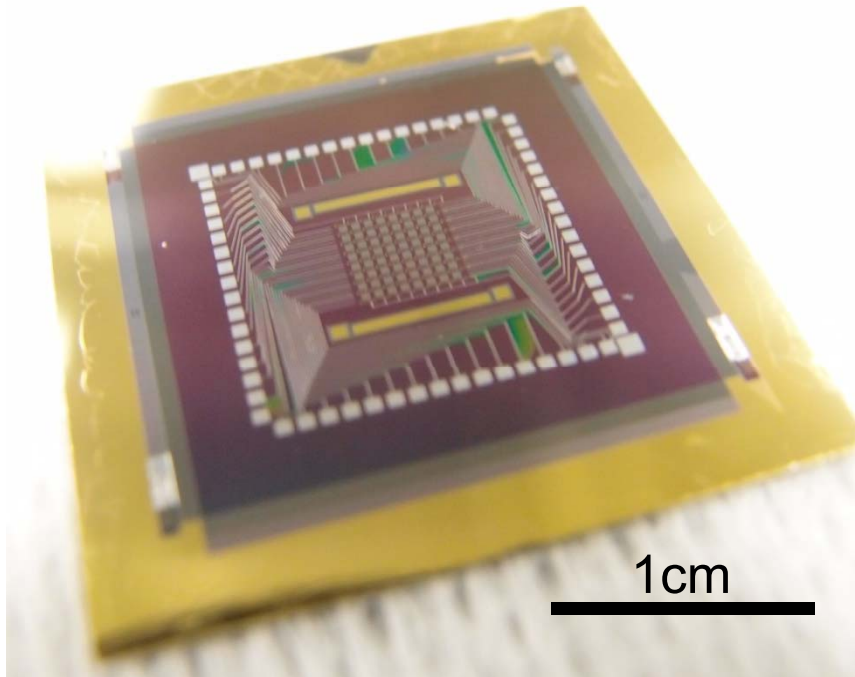
(5) SiO₂ Dry Etching (CHF₃)



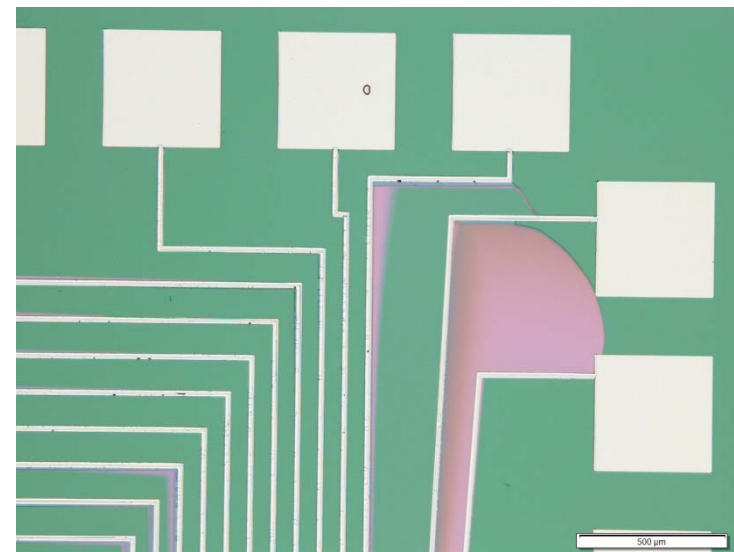
(3) Mirror chip handle layer etching



8 × 8 pixel MEMS-DM –Fabrication result (1)

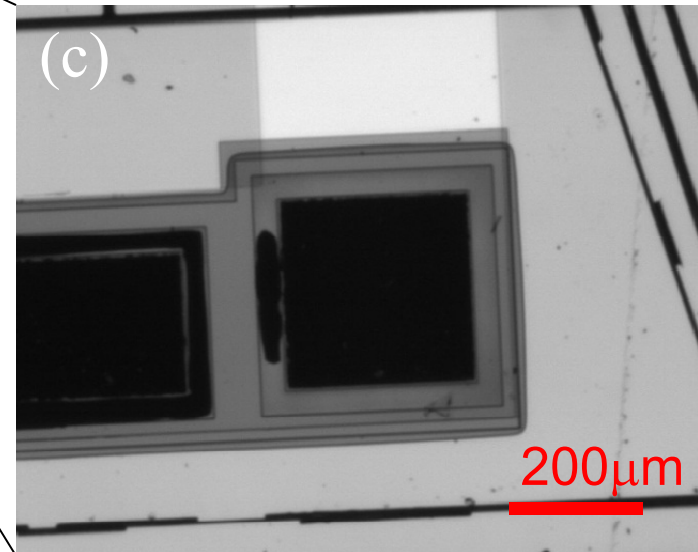
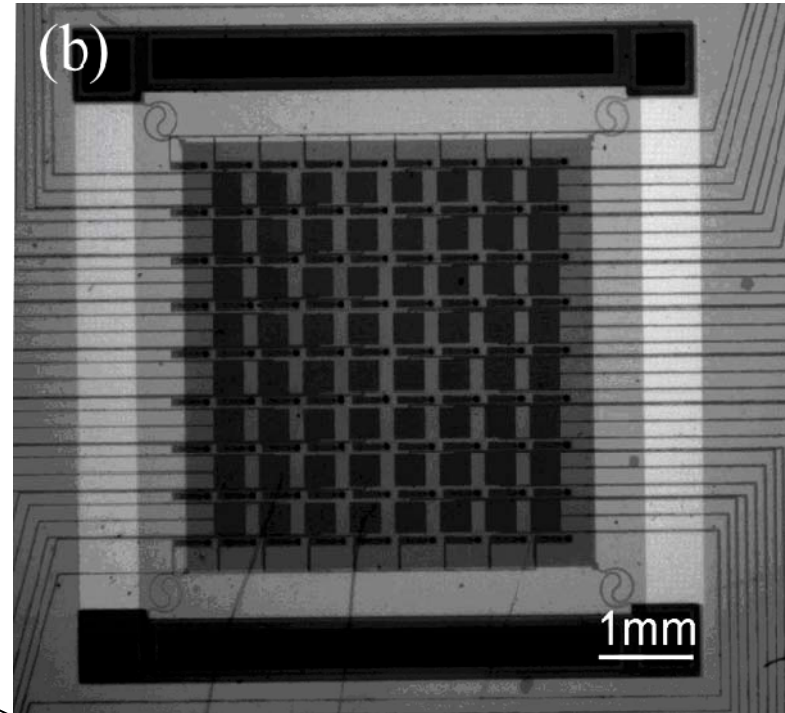
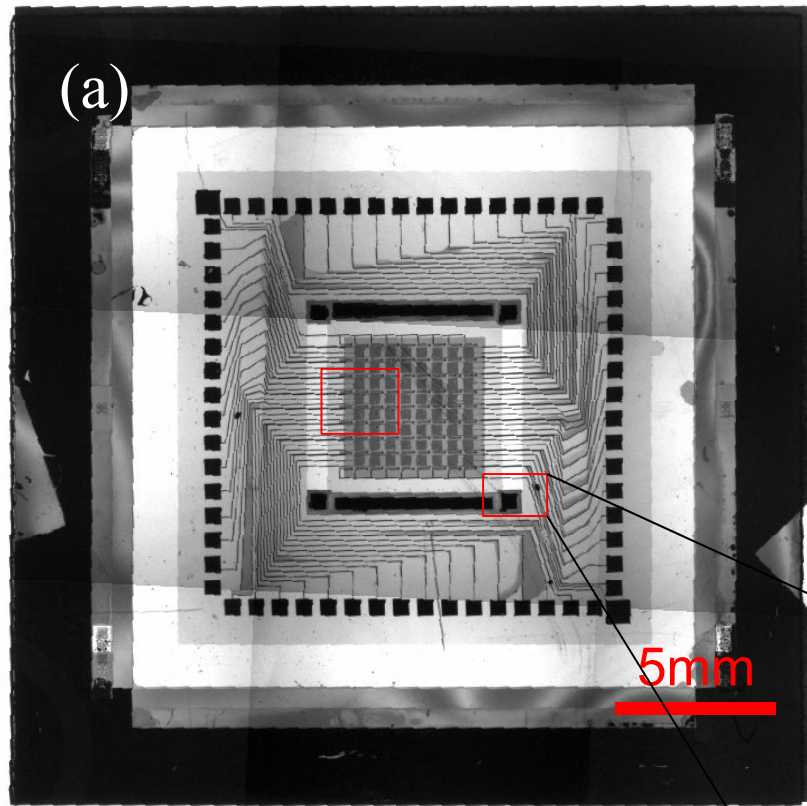


Bimorph springs with bonding pad



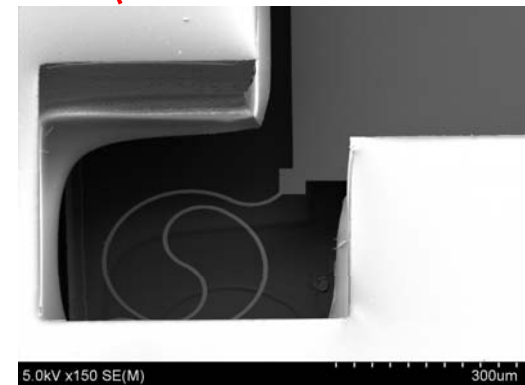
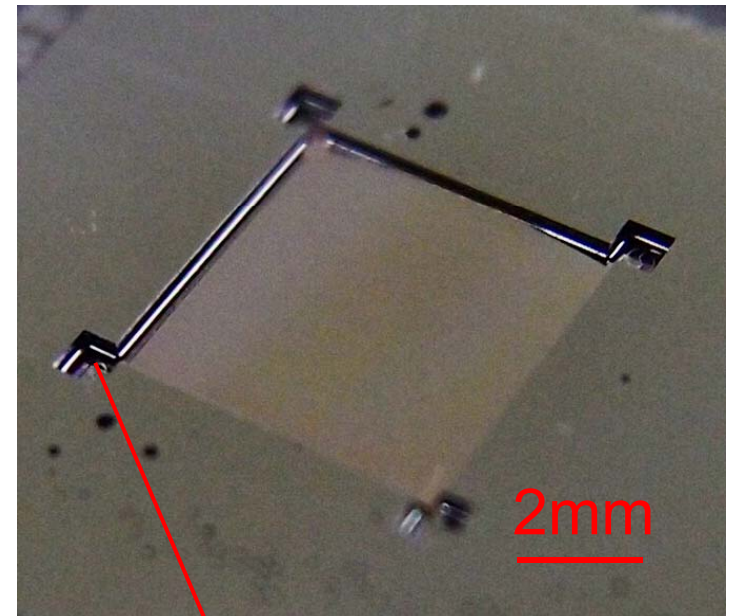
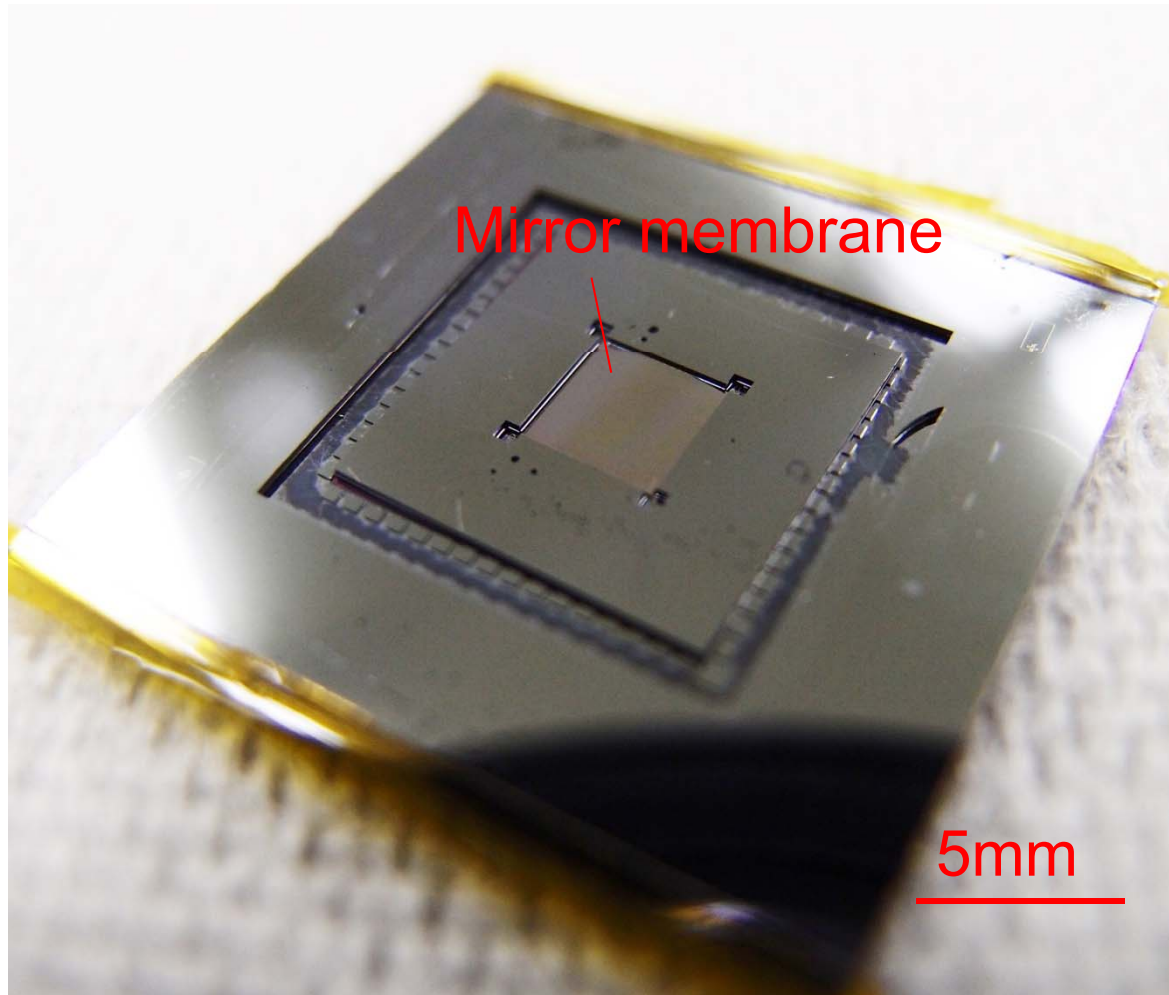
Al electrode pad for wire bonding

8 × 8 pixel MEMS-DM –Fabrication result (2)



Infra red micrographs of the DM after bonding process

8 × 8 pixel MEMS-DM –Fabrication result (3)

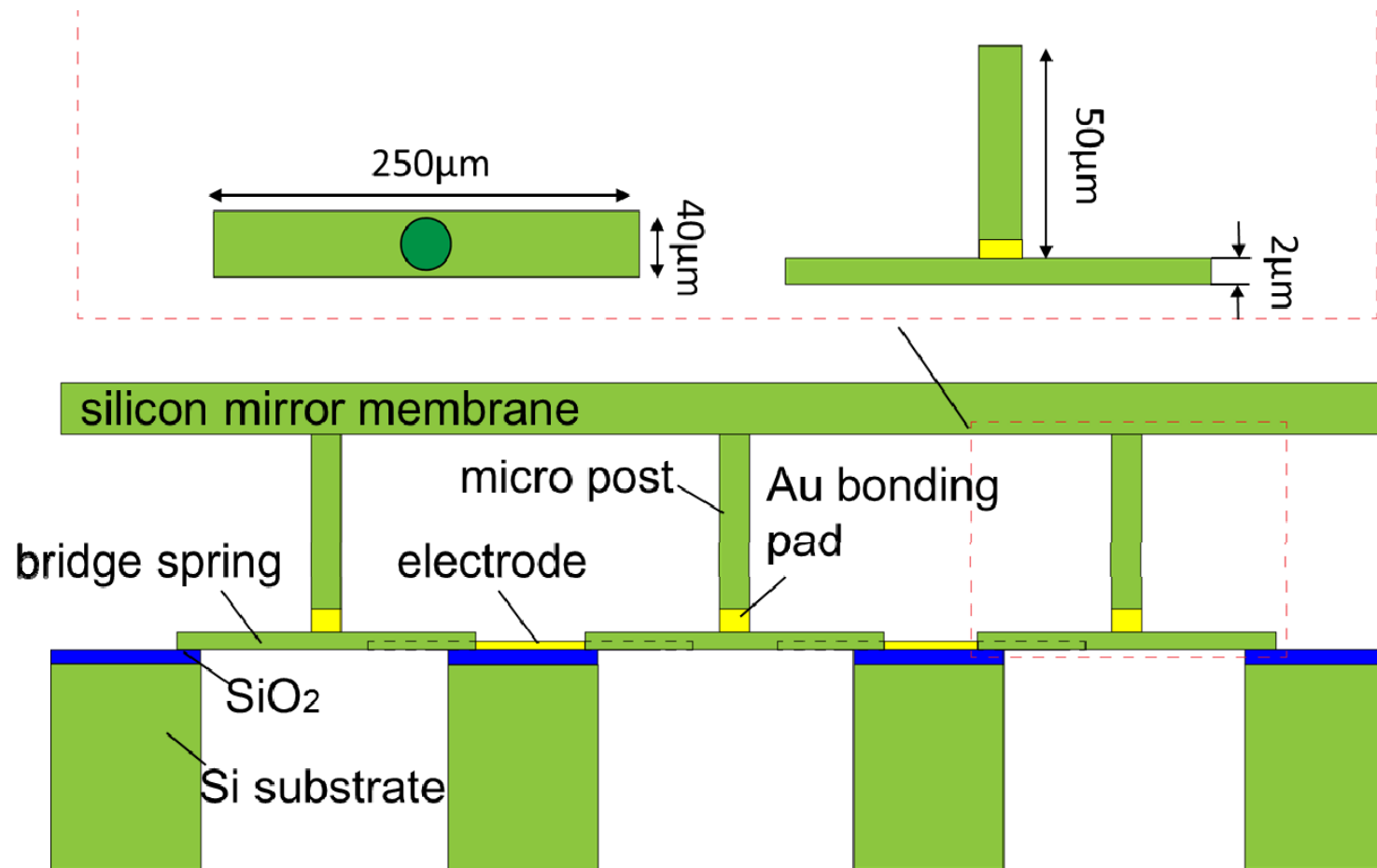


Conclusions

An improved membrane transfer process for large-stroke continuous membrane DM is proposed and demonstrated

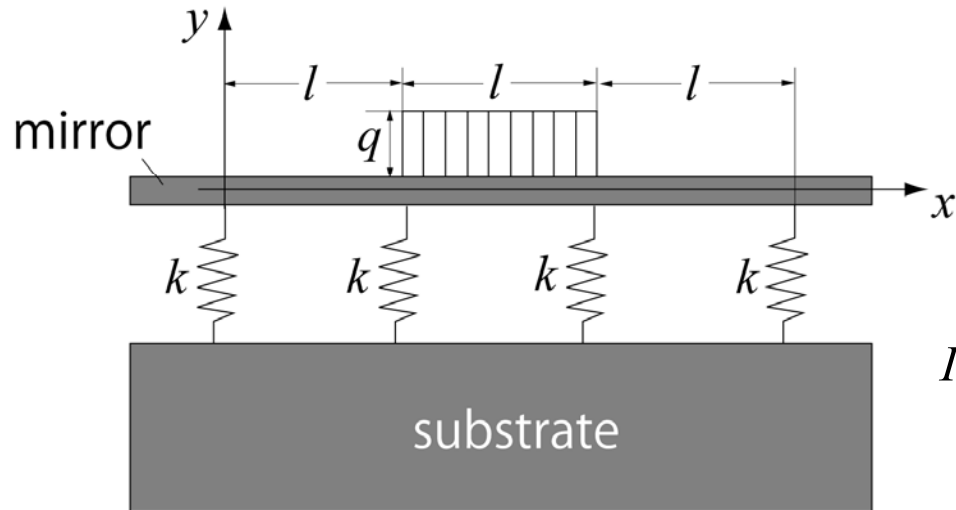
- A 2 μm -thick membrane is transferred to a flexible bimorph array by combining bulk micromachining and Au-Si eutectic bonding technology;
- Au-Si eutectic bonding shown a reliable bonding quality for transferring large scale mirror membrane;
- The stroke of the DMs are 4.0 μm at 125V(2 \times 2 Pixel DM) and 3.5 μm at 115V(4 \times 4 Pixel DM), respectively. The fill factor of the fabricated mirror is \sim 99.9%.
- A freestanding 4mm \times 4mm \times 2 μm mirror membrane is successfully fabricated.

New design



MEMS-DM – Influence function(IF) at adjacent actuator

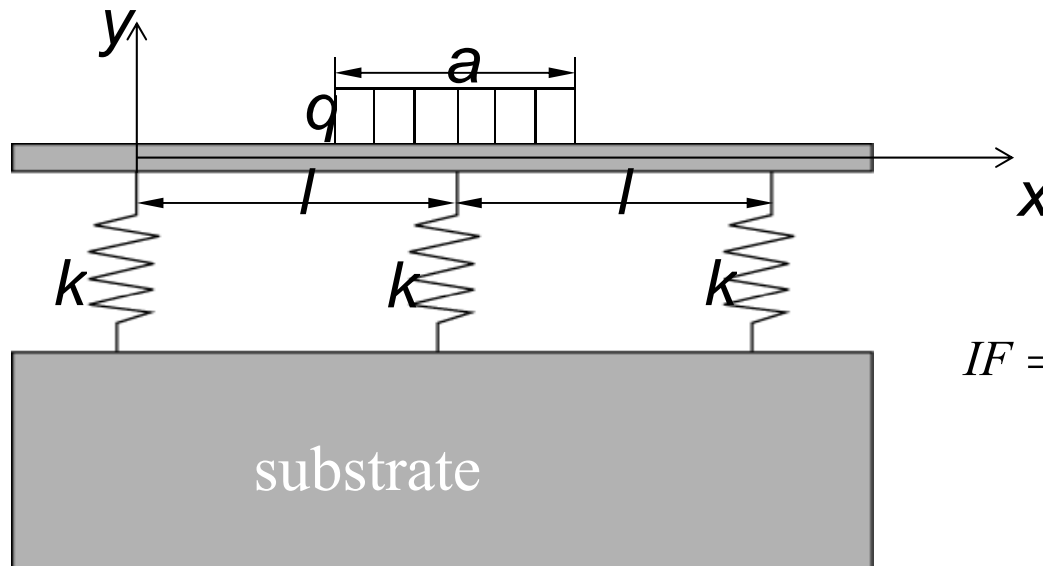
Previous calculation model (design 1):



$$IF = \frac{y|_{x=l/2}}{y|_{x=3l/2}} = \frac{552EIkl^3 + 1152E^2I^2 - 6k^2l^6}{1260EIkl^3 + 1152E^2I^2 + 13k^2l^6}$$

Current calculation model (design 2):

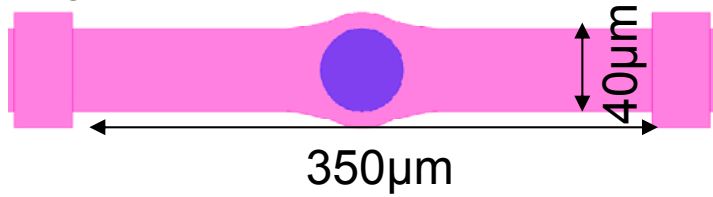
(EI is the flexure rigidity)



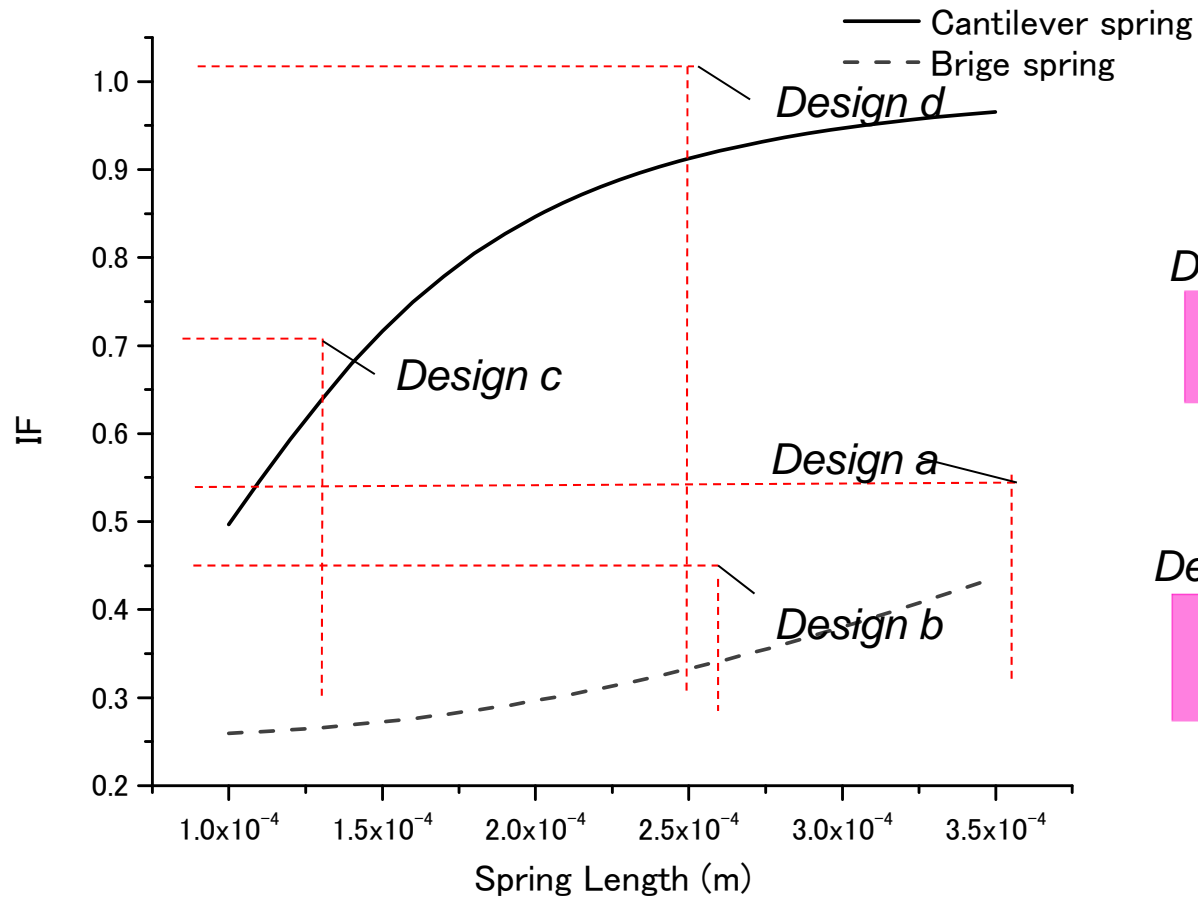
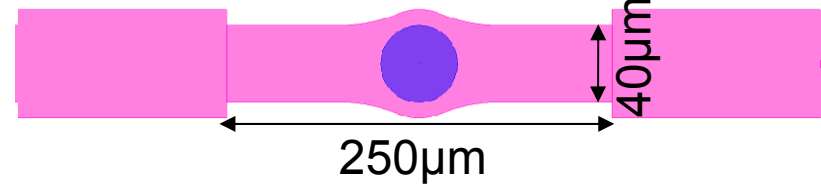
$$IF = \frac{y|_{x=2l}}{y|_{x=l}} = \frac{3ka^3 + 16kla^3 + 384EI}{-39ka^3 + 96kla^3 + 384EI}$$

MEMS-DM – Spring design

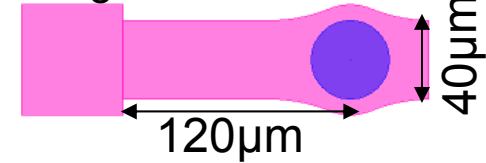
Design a



Design b



Design c



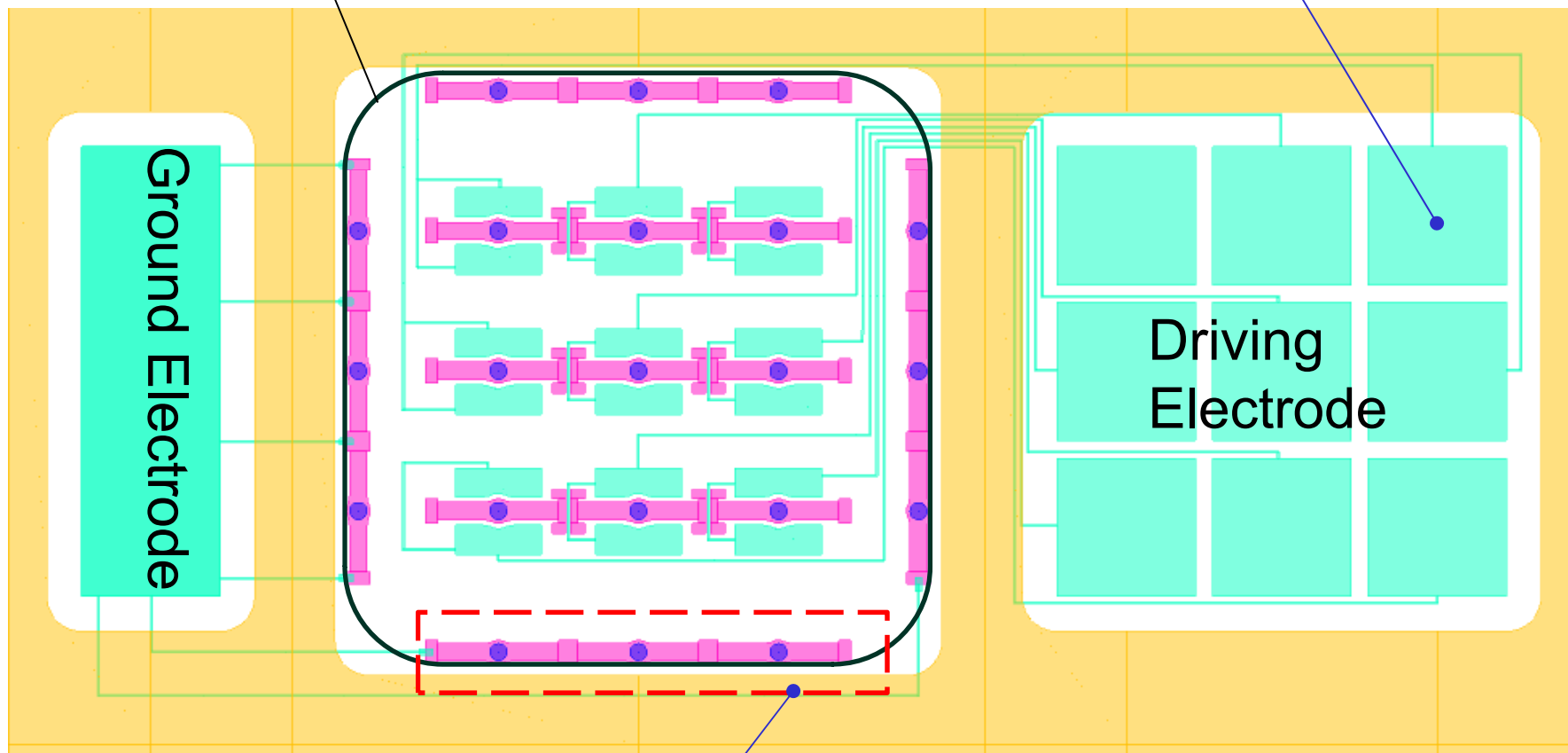
Design d



MEMS-DM – 3 × 3 DM layout

Mirror membrane

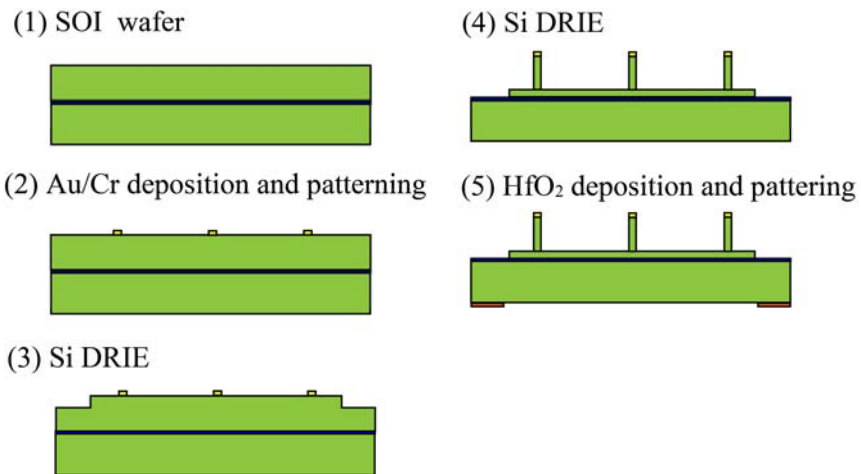
金属配線、電極を採用



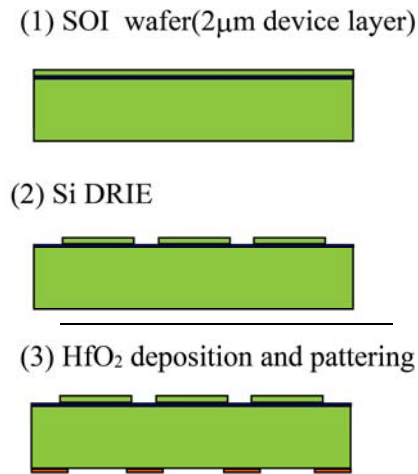
初期変位を低減させるため、周辺支持を設ける

MEMS-DM – fabrication

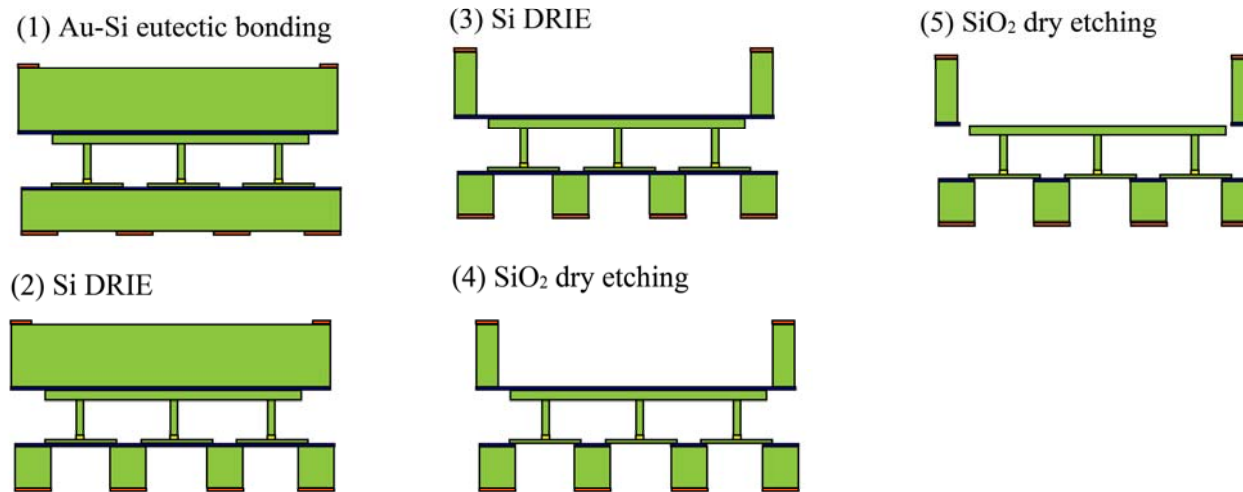
(a) Mirror chip



(b) Actuator chip



(c) Bonding and release

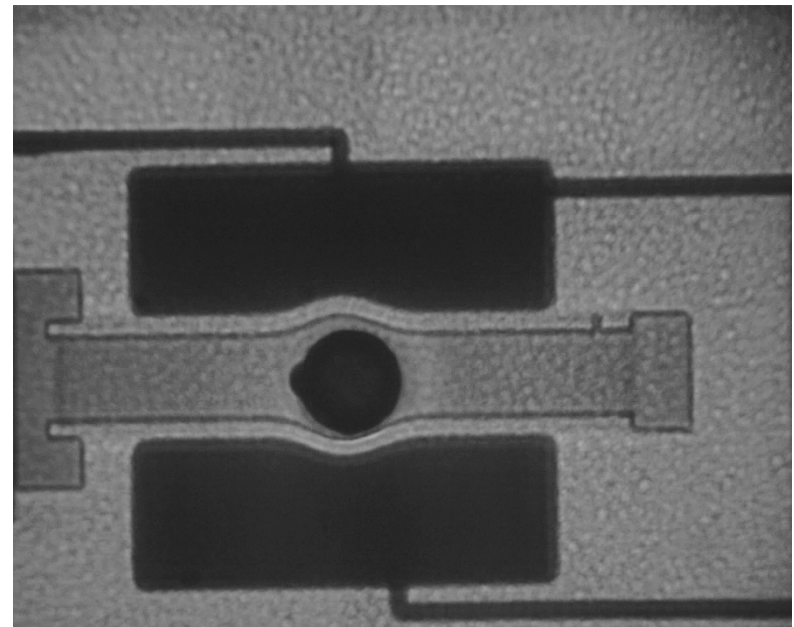
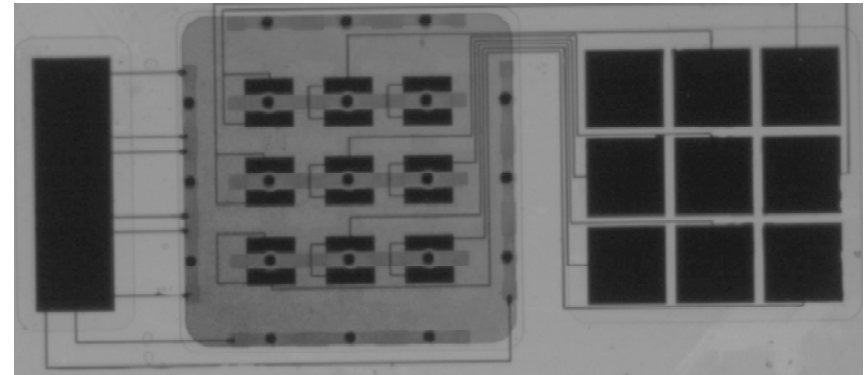
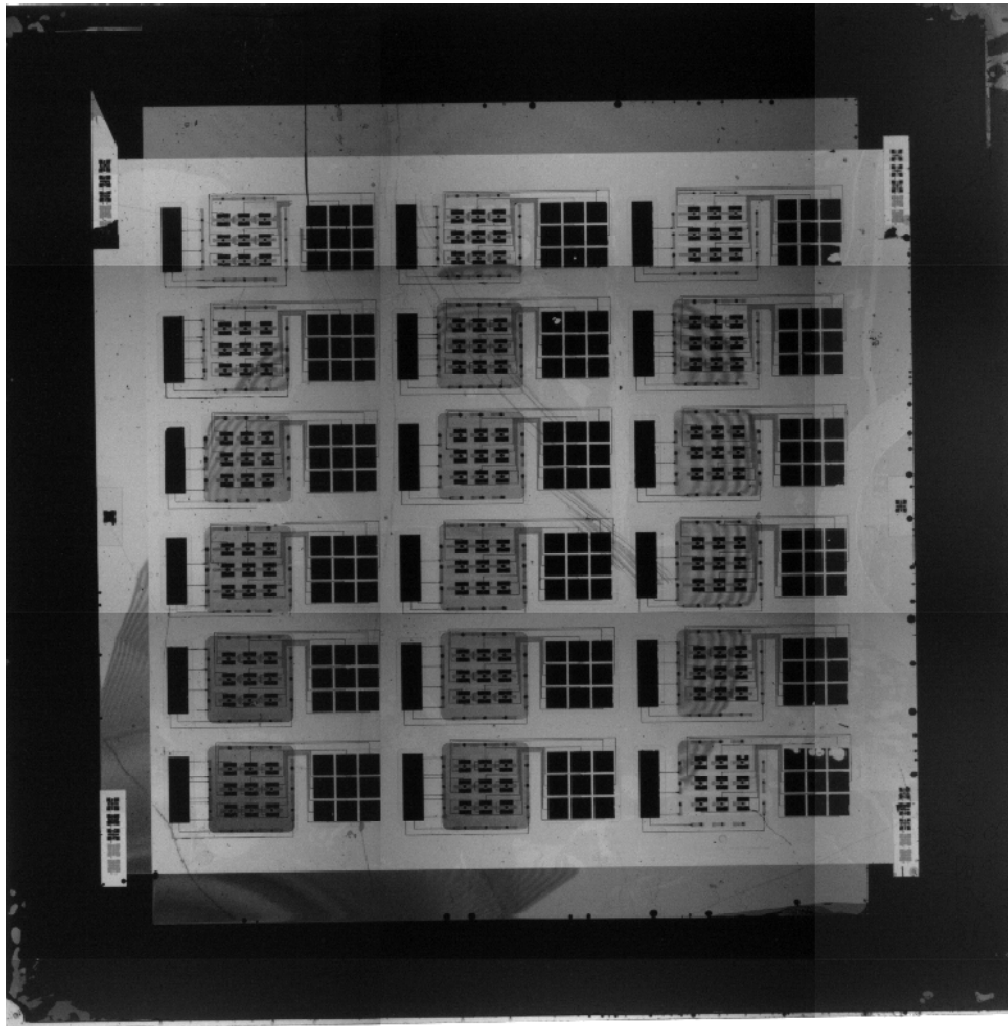


Si
 Au/Cr
 HfO₂
 SiO₂

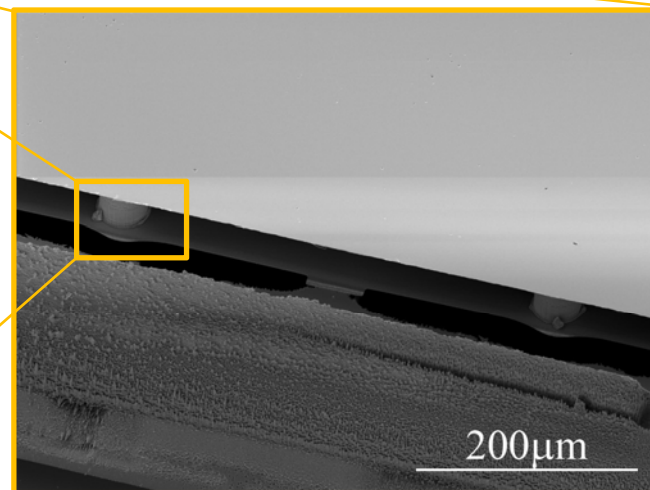
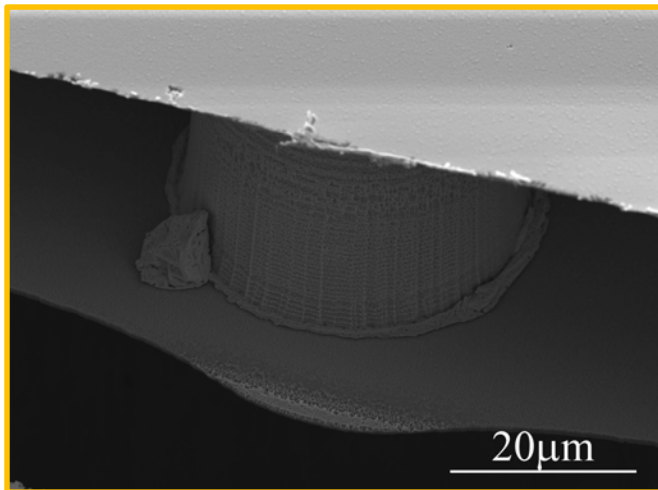
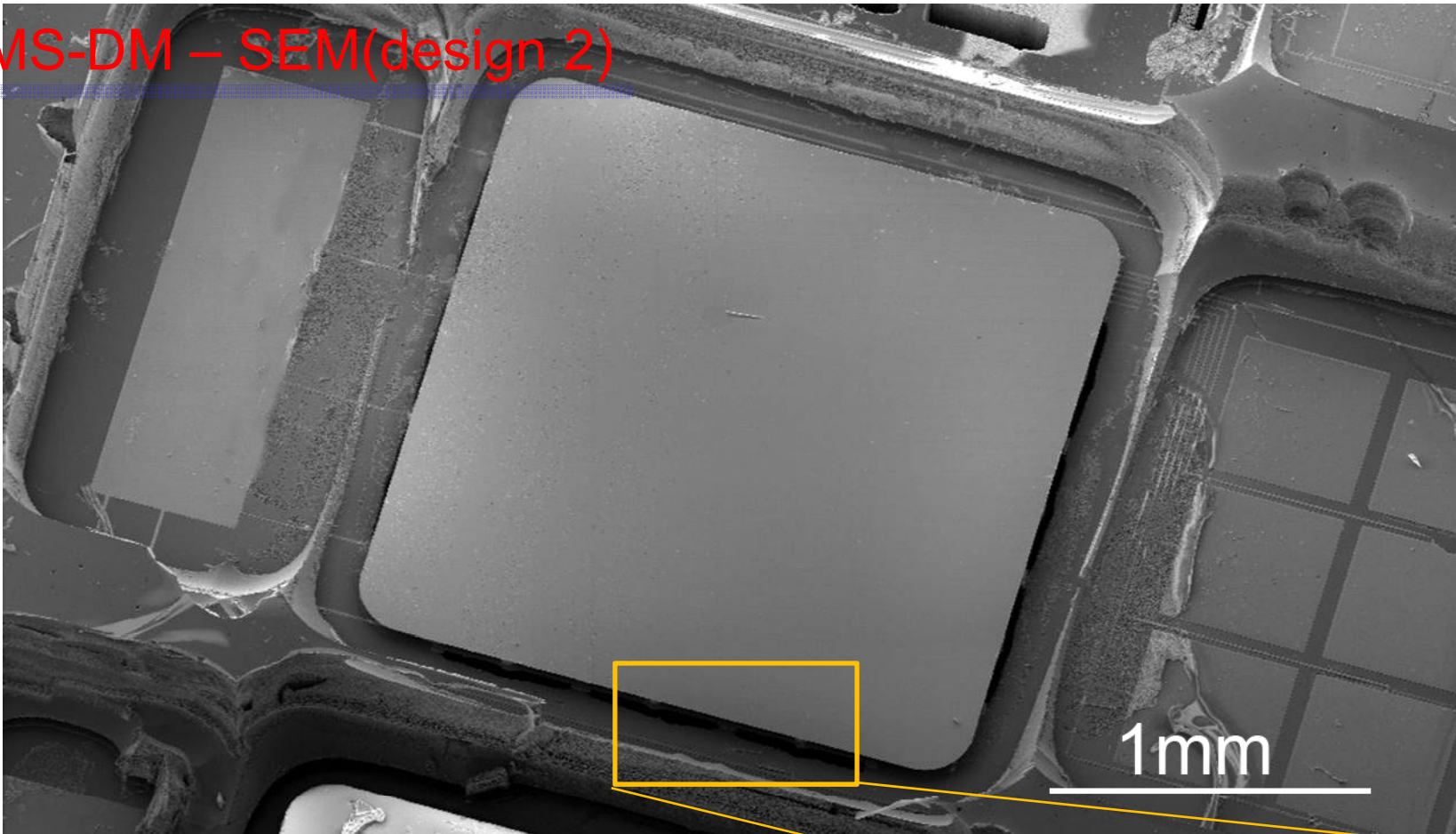
MEMS-DM – Bonding(design 2)

Bonding experiment@Kyoto tou, Suss Bonder SB-6e

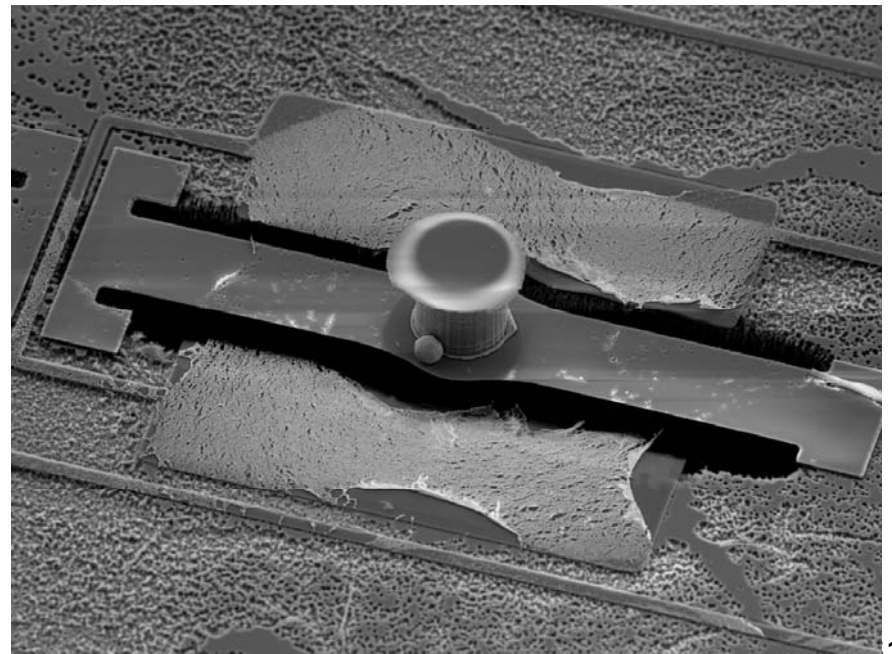
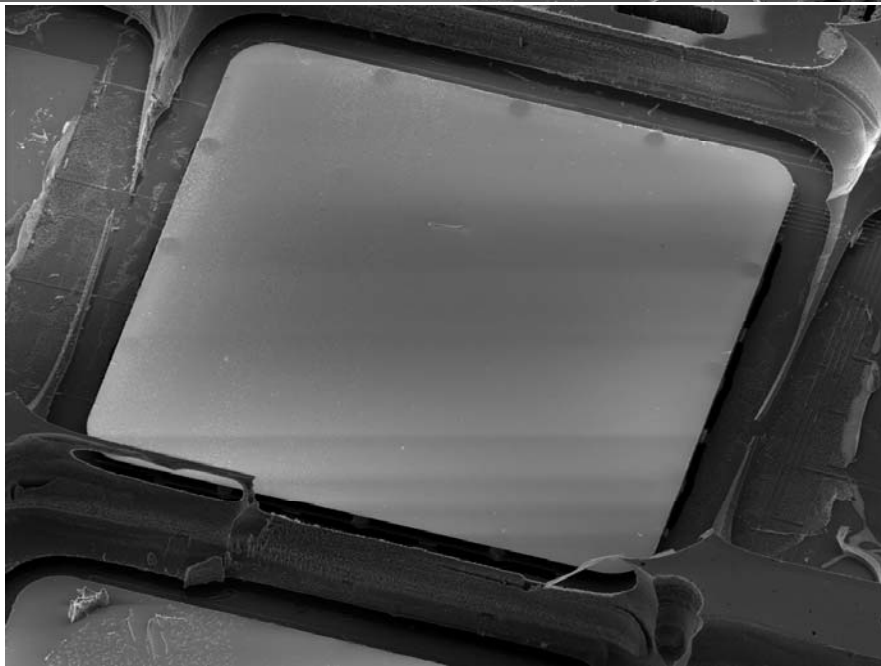
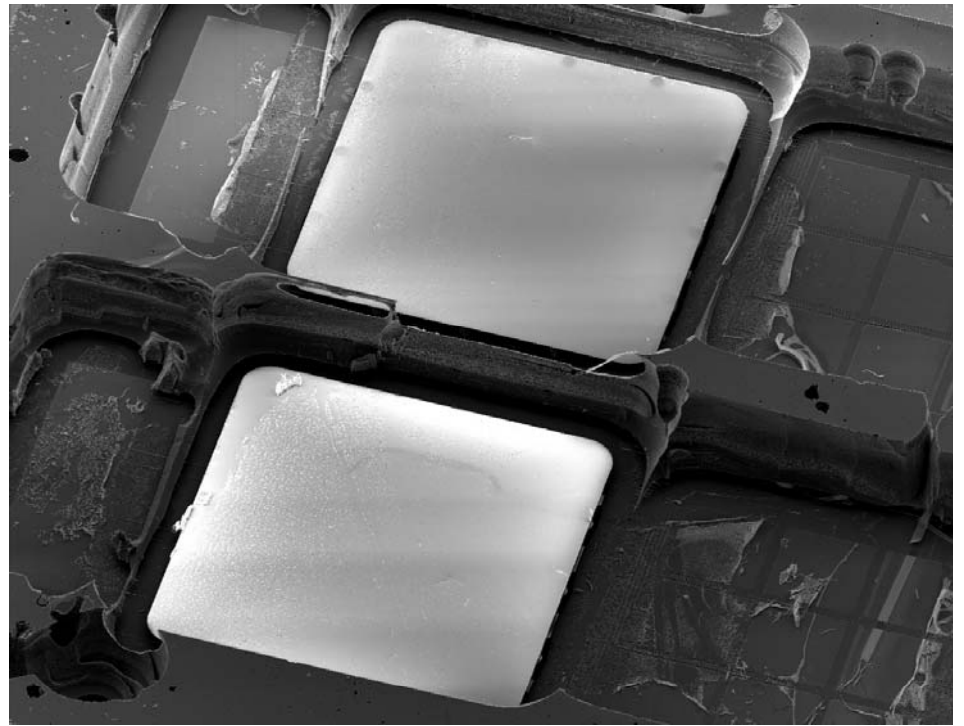
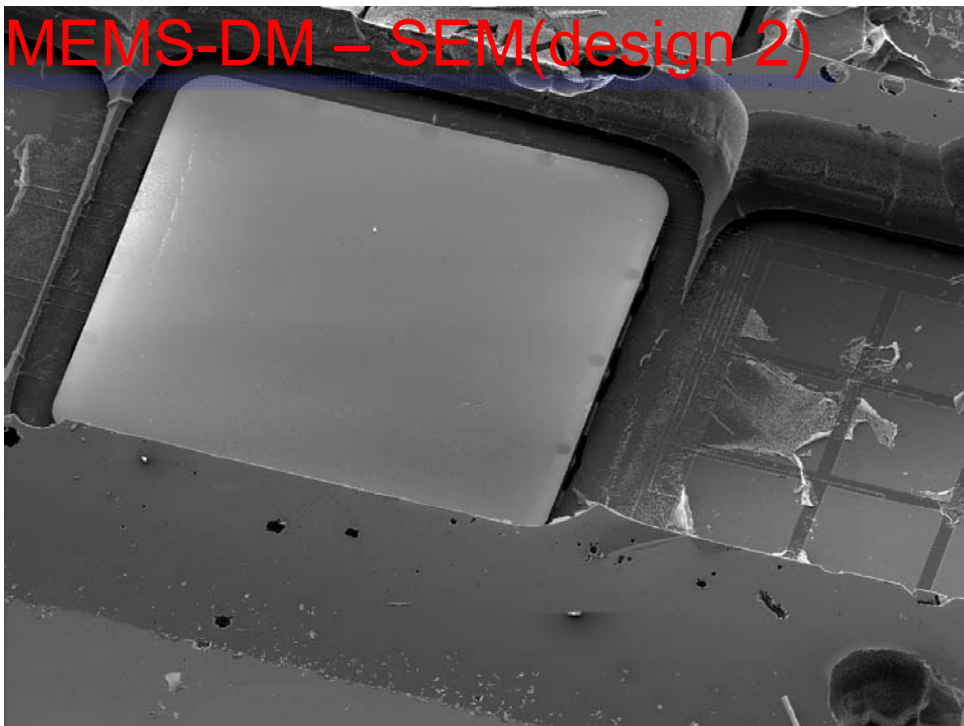
Au-Si eutectic bonding condition :400°C, 12KPa(30min)



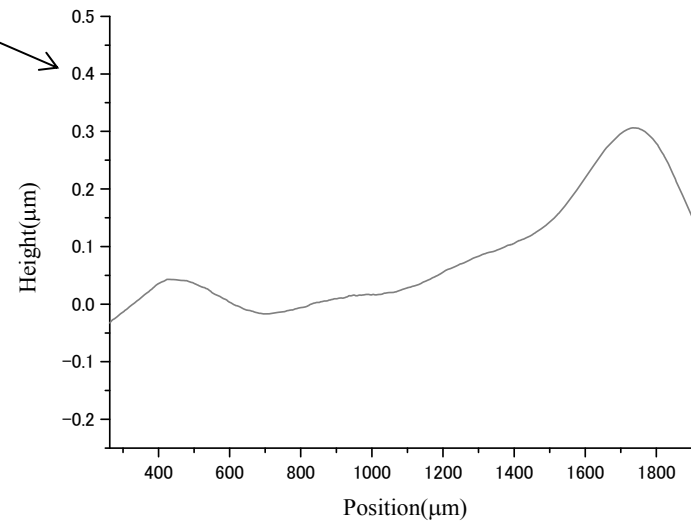
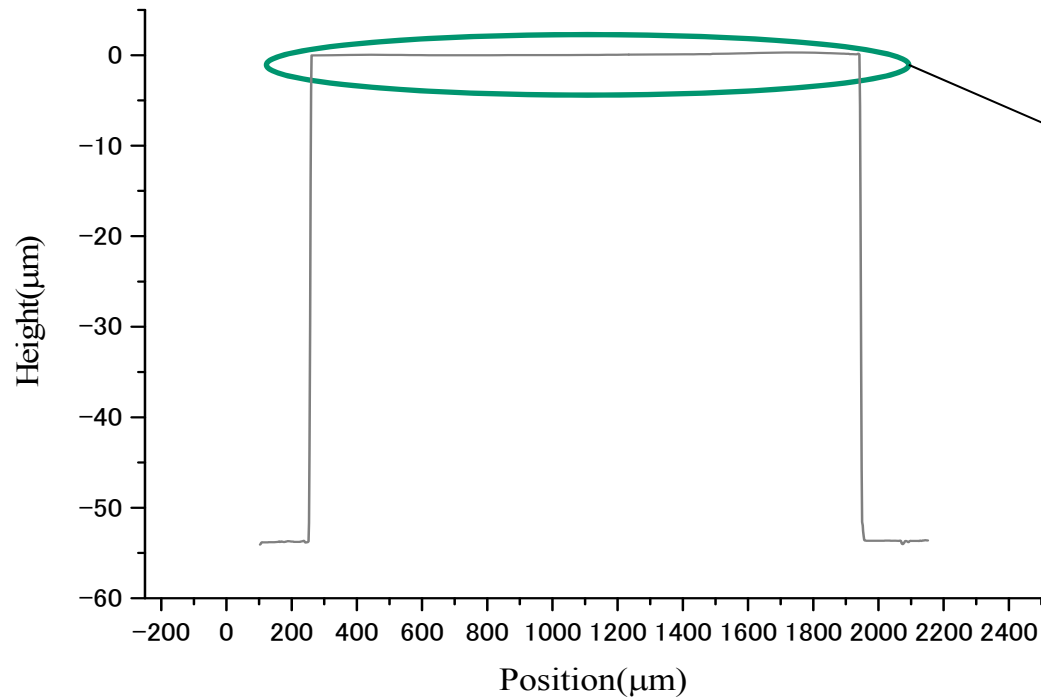
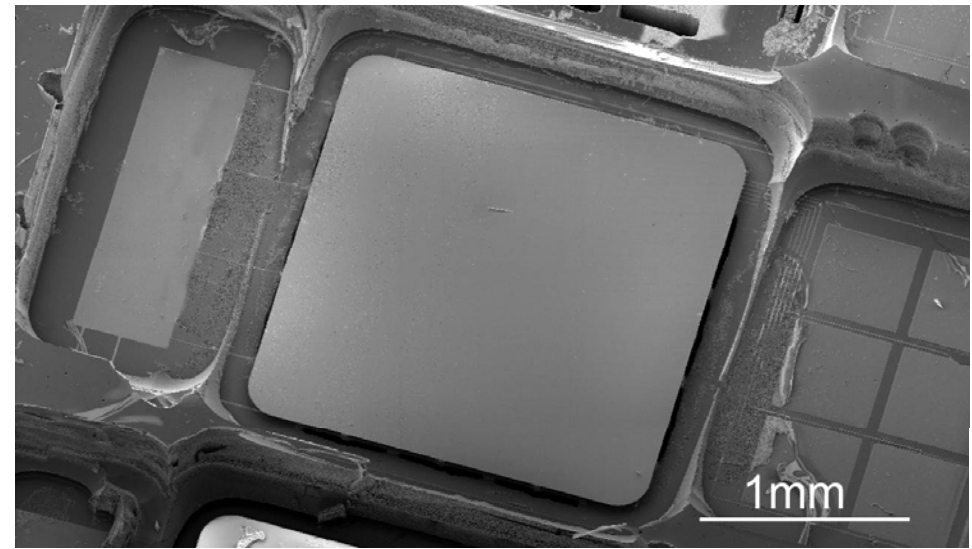
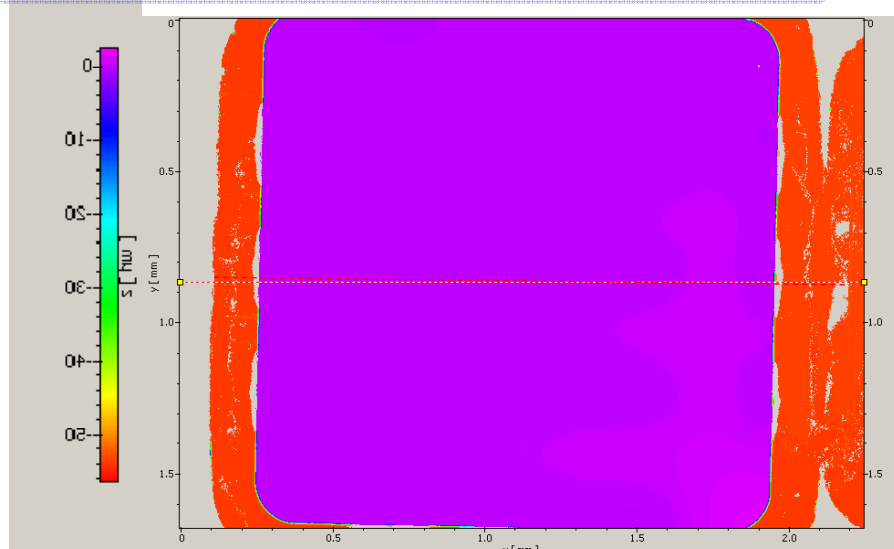
MEMS-DM – SEM(design 2)



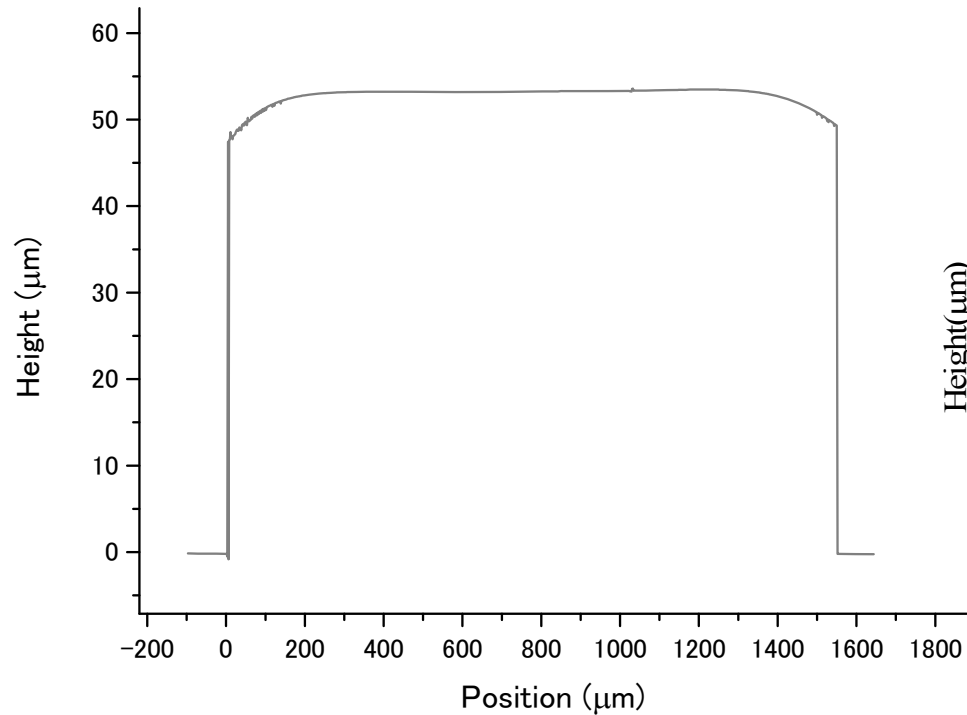
MEMS-DM – SEM (design 2)



MEMS-DM – initial deflection(design 2)



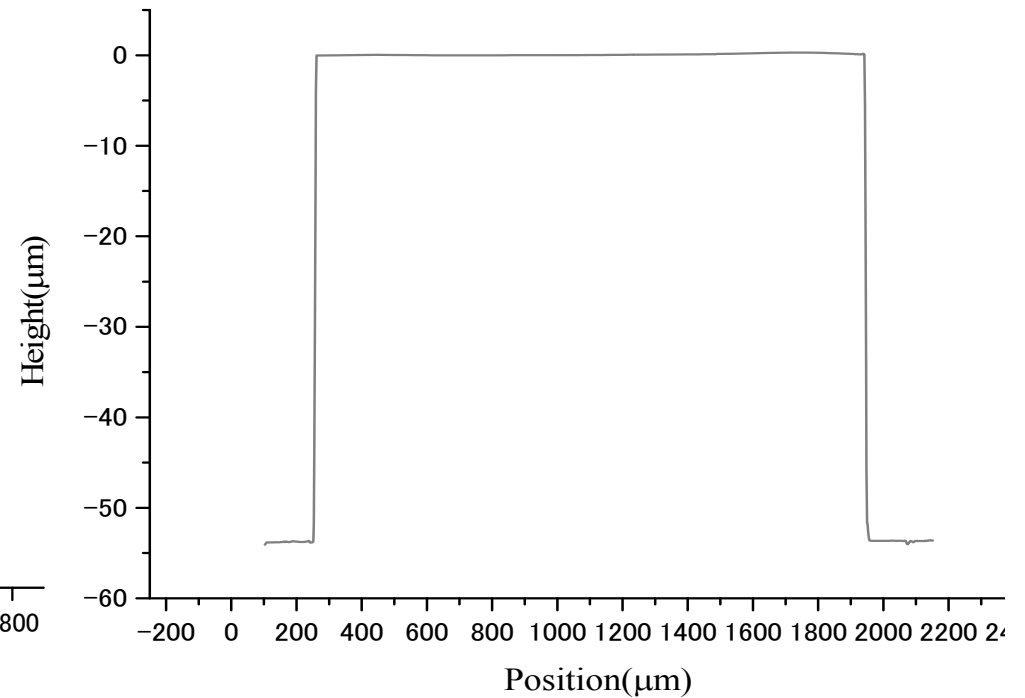
MEMS-DM – membrane initial deformation



Without edge support

Mirror size: 1.5mm × 1.5mm

Mirror initial deformation: PV value=3900nm

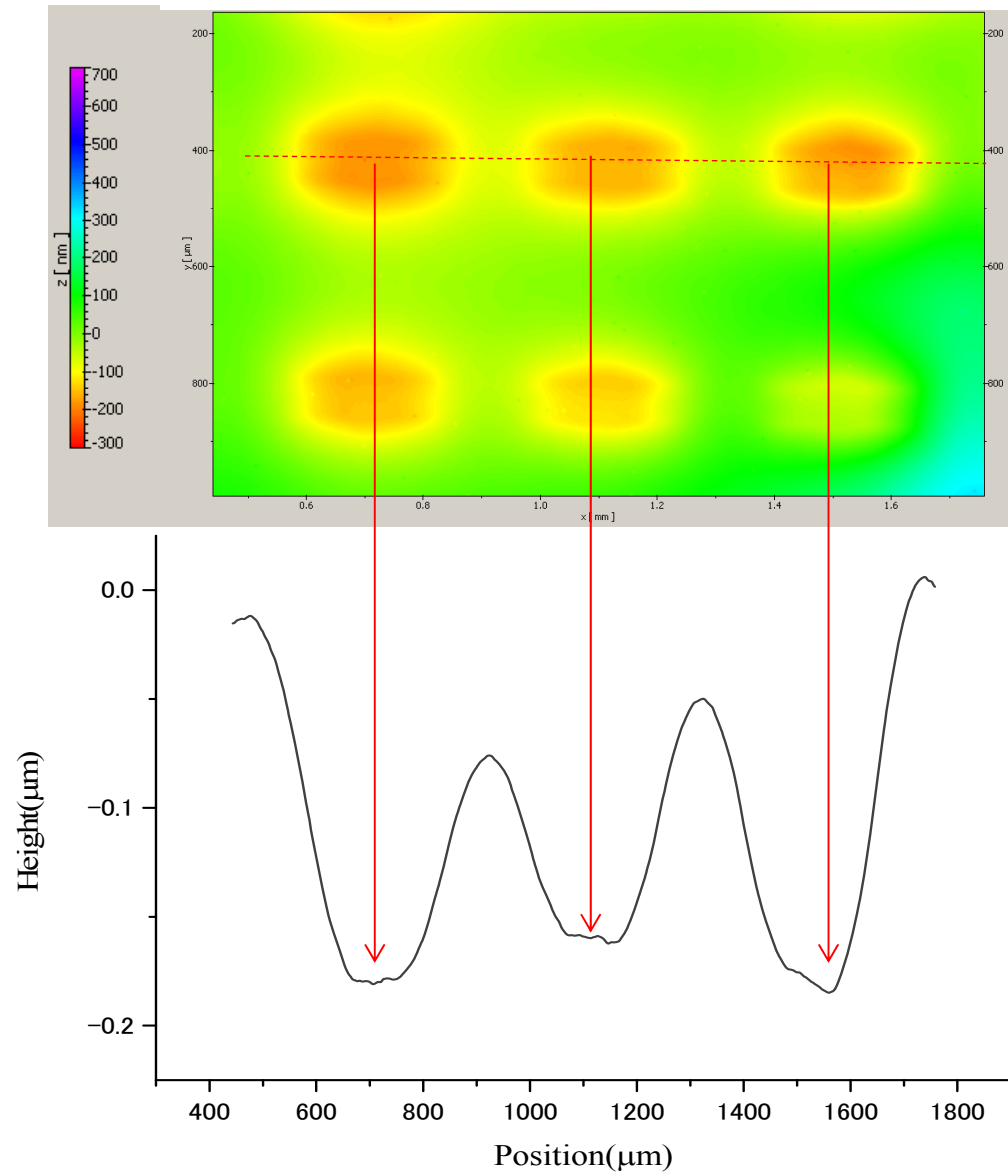


With edge support

Mirror size: 1.7mm × 1.7mm

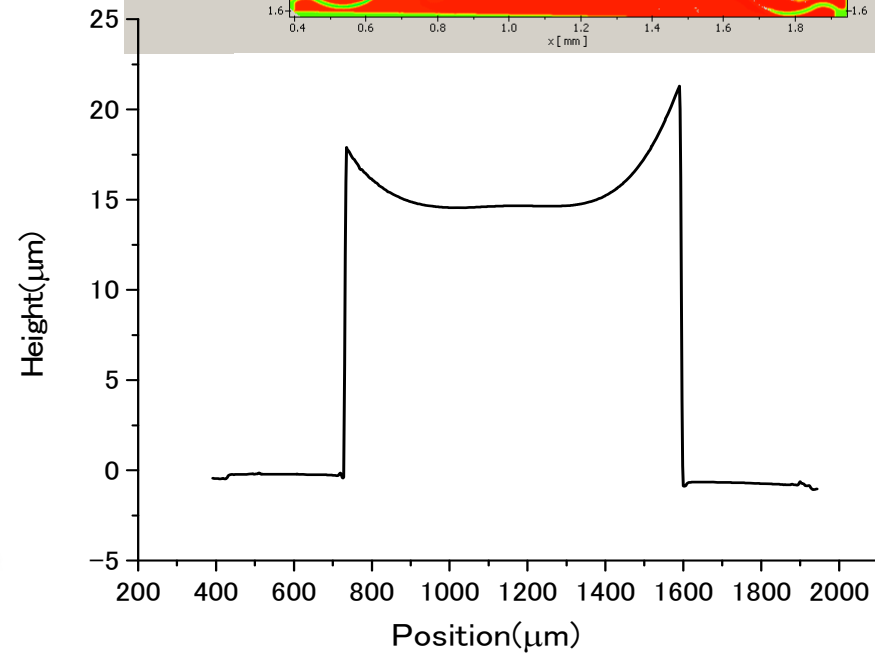
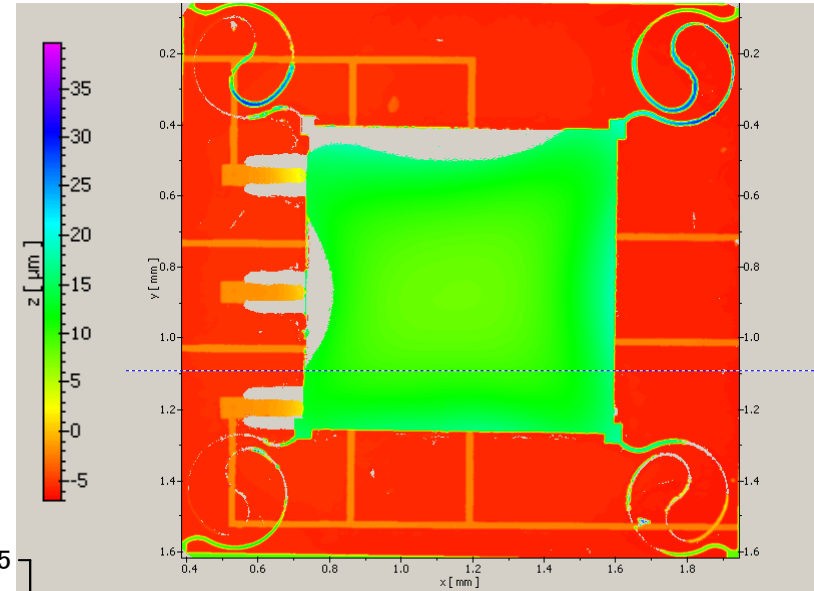
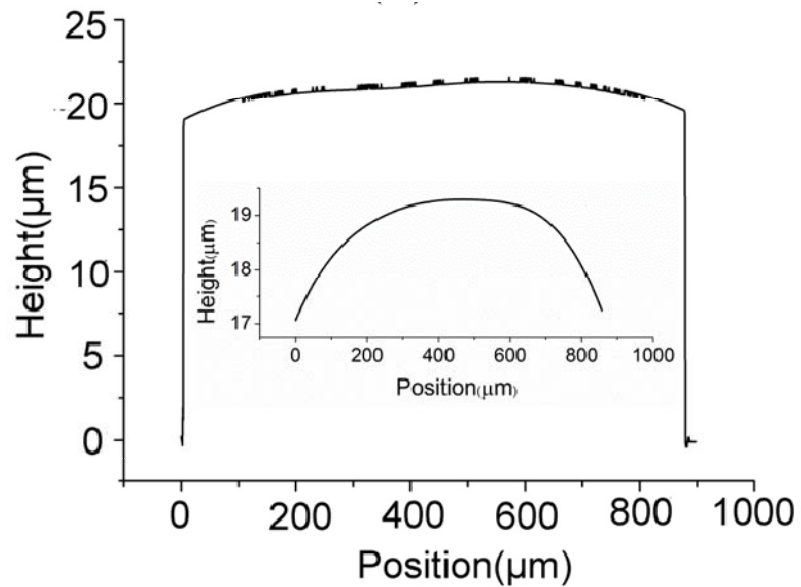
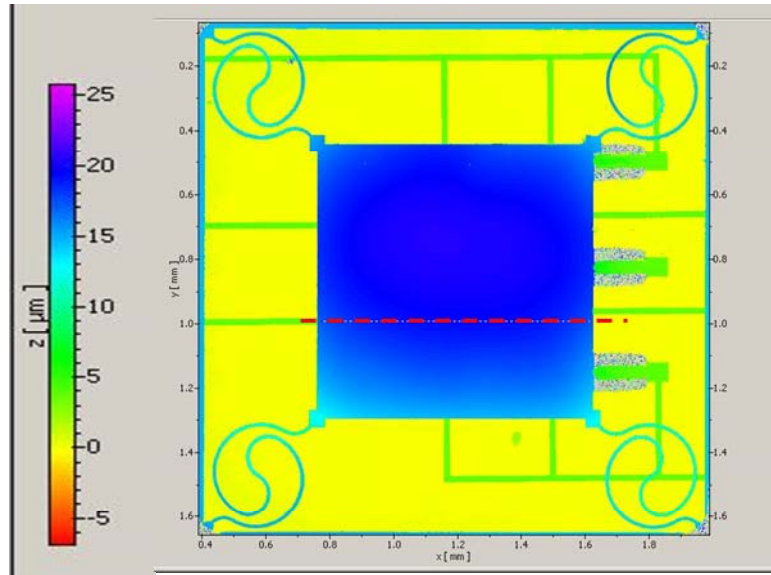
Mirror initial deformation: PV value=350nm

MEMS-DM – initial deflection mirror: bonding point



MEMS-DM – Au/Cr coating

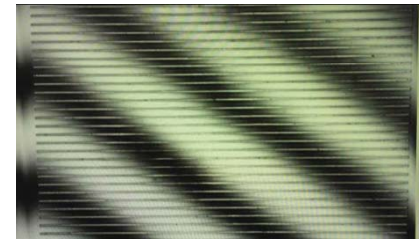
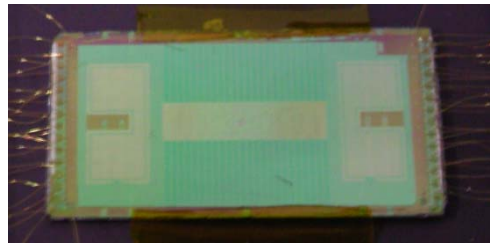
Cr: ~10nm, Au: ~60nm is coated on the mirror by sputtering



まとめ

1. 集積型MEMSミラーについて

2. LSIと集積した波長選択スイッチ用MEMSミラー



3. Si連続メンブレン-デフォーマブルミラーの開発状況

